Cl **00** *SC* **0** *P* **0** *L* **0** # 10

Thompson, Geoffrey Independent

Comment Type **TR** *Comment Status* **X**

I am going to have to add my weight to Mr Frazier's unsatisfied comment #35

Your response and the lack of of any specified tolerance leads one to inevitable conclusion there there is no accuracy requirement whatsoever for this reported measurement. That being the case, there is no way for a developer manufacturer of higher layer equipment to put a conformance burden on their supplier that lies within the standard. Further, there is no way for a design verification function within a suppliers operation to tell whether this function works as opposed to supplying a random number not at all associated with the event.

SuggestedRemedy

Put some numeric tolerance on this measurement so that functionality can be verified. If it requires that the vendor supply a fixed delay number to center the tolerance variance then that is acceptable.

Proposed Response *Response Status* **O**

Cl **01** *SC* **1.4** *P* **14** *L* **7** # 8

Marris, Arthur Cadence Design Syst

Comment Type **TR** *Comment Status* **X**

This is a pile on to comment 28 against draft 3.0. TSSI should be properly defined here. Also the reference to 802.3-2008 is incorrect.

SuggestedRemedy

Change to:
Time Synchronization Service Interface (TSSI). The interface between the TimeSync client and the generic Reconciliation Sublayer to provide SFD indication. (See IEEE 802.3, Clause 90.)

Proposed Response *Response Status* **O**

Cl **01** *SC* **1.4** *P* **14** *L* **7** # 2

Marris, Arthur Cadence Design Syst

Comment Type **ER** *Comment Status* **X**

Clause 90 is not in 802.3-2008

SuggestedRemedy

Change reference to be just to IEEE std 802.3 Clause 90.

Proposed Response *Response Status* **O**

Cl **30** *SC* **30.13.1.1** *P* **20** *L* **23** # 3

Marris, Arthur Cadence Design Syst

Comment Type **E** *Comment Status* **X**

Make 1.1800.1, 2.1800.1, 3.1800.1, 4.1800.1, 5.1800.1, and 6.1800.1 link to Clause 45

SuggestedRemedy

Add links (cross references) from the registers referenced in Clause 30 to Clause 45.

Proposed Response *Response Status* **O**

Cl **30** *SC* **30.13.1.3** *P* **20** *L* **46** # 7

Marris, Arthur Cadence Design Syst

Comment Type **TR** *Comment Status* **X**

This is a pile on to comment 29 against draft 3.0. The most significant thing that 802.3bf is doing from the PHY implementers point of view and the system implementers point of view is defining how to report the transmit and receive latency through the PHY. This definition must be clear and unambigous.

SuggestedRemedy

See my other comment on this.

My view is that the value of the attribute is simply the
sum of the delay through the PCS and the delay through the PMA/PMD. Any latency in the TSSI indication and extra delay between sublayers is irrelevant as all the 802.1AS system implementer cares about is the asymmetry between the transmit and receive latency.

Mentioning XAUI without including SFI and XFI confuses matters as XAUI is a legacy MAC/PHY interconnect. The easiest way to get around this confusion is to focus on PCS and PMA latency. XAUI is really just a PCS/PMA combination.

Proposed Response *Response Status* **O**

Cl **30** *SC* **30.13.1.3** *P* **20** *L* **46** # 1
Marris, Arthur Cadence Design Syst

Comment Type **ER** *Comment Status* **X**

The following does not make sense:

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute accounts for maximum transmit path data delay values, accounts for the sum of the values of the registers in the instantiated sublayers (for each MMD, in case of multiple instances):

SuggestedRemedy

Reword to:

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute is the sum of the values of the following registers in the instantiated sublayer:
-- for PMA/PMD: registers 1.1801 and 1.1802,
-- for WIS: registers 2.1801 and 2.1802,
-- for PCS: registers 3.1801 and 3.1802,
-- for PHY XS: registers 4.1801 and 4.1802,
-- for DTE XS: registers 5.1801 and 5.1802,
-- for TC: registers 6.1801 and 6.1802.

and make similar change in 30.13.1.4, 30.13.1.5 and 30.13.1.6

Proposed Response *Response Status* **O**

Cl **45** *SC* **45.2.3.40** *P* **27** *L* **17** # 4
Marris, Arthur Cadence Design Syst

Comment Type **E** *Comment Status* **X**

Cahnge Table 45-115cc to Table 45-115c

SuggestedRemedy

Remove a 'c'

Proposed Response *Response Status* **O**

Cl **45** *SC* **45.2.4** *P* **113** *L* # 5
Marris, Arthur Cadence Design Syst

Comment Type **TR** *Comment Status* **X**

Title of Table 45-108 is incorrect

SuggestedRemedy

Change title to:

Table 45-108--PHY XS registers

Proposed Response *Response Status* **O**

Cl **90** *SC* **90.1** *P* **35** *L* **6** # 9
Thompson, Geoffrey Independent

Comment Type **E** *Comment Status* **X**

(Not classified DISAPPROVE because it is not new text)
I would expect the following text to be easily proved false: "The TSSI can be used to support any protocol that requires knowledge of packet egress and ingress time."

SuggestedRemedy

I would suggest text that is not so presumptious, perhaps: "The TSSI can be used to support protocols that require knowledge of packet egress and ingress time."

Proposed Response *Response Status* **O**

Cl **90** *SC* **90.5** *P* **38** *L* **14** # 6
Marris, Arthur Cadence Design Syst

Comment Type **E** *Comment Status* **X**

grammar
"used to interface MAC with any type of PHYs supporting"

SuggestedRemedy

change to:
"used to interface a MAC with a PHY supporting"

Proposed Response *Response Status* **O**