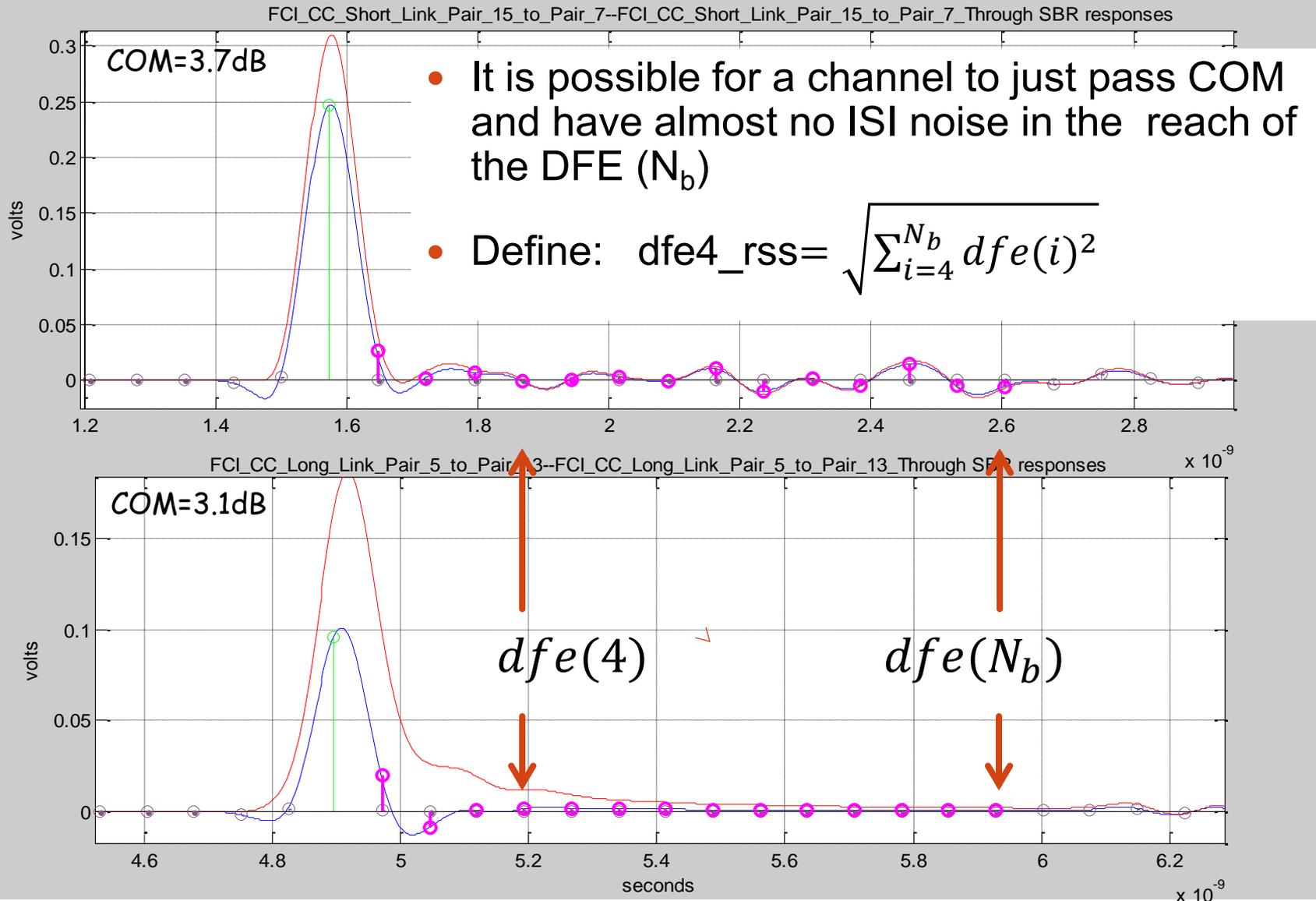


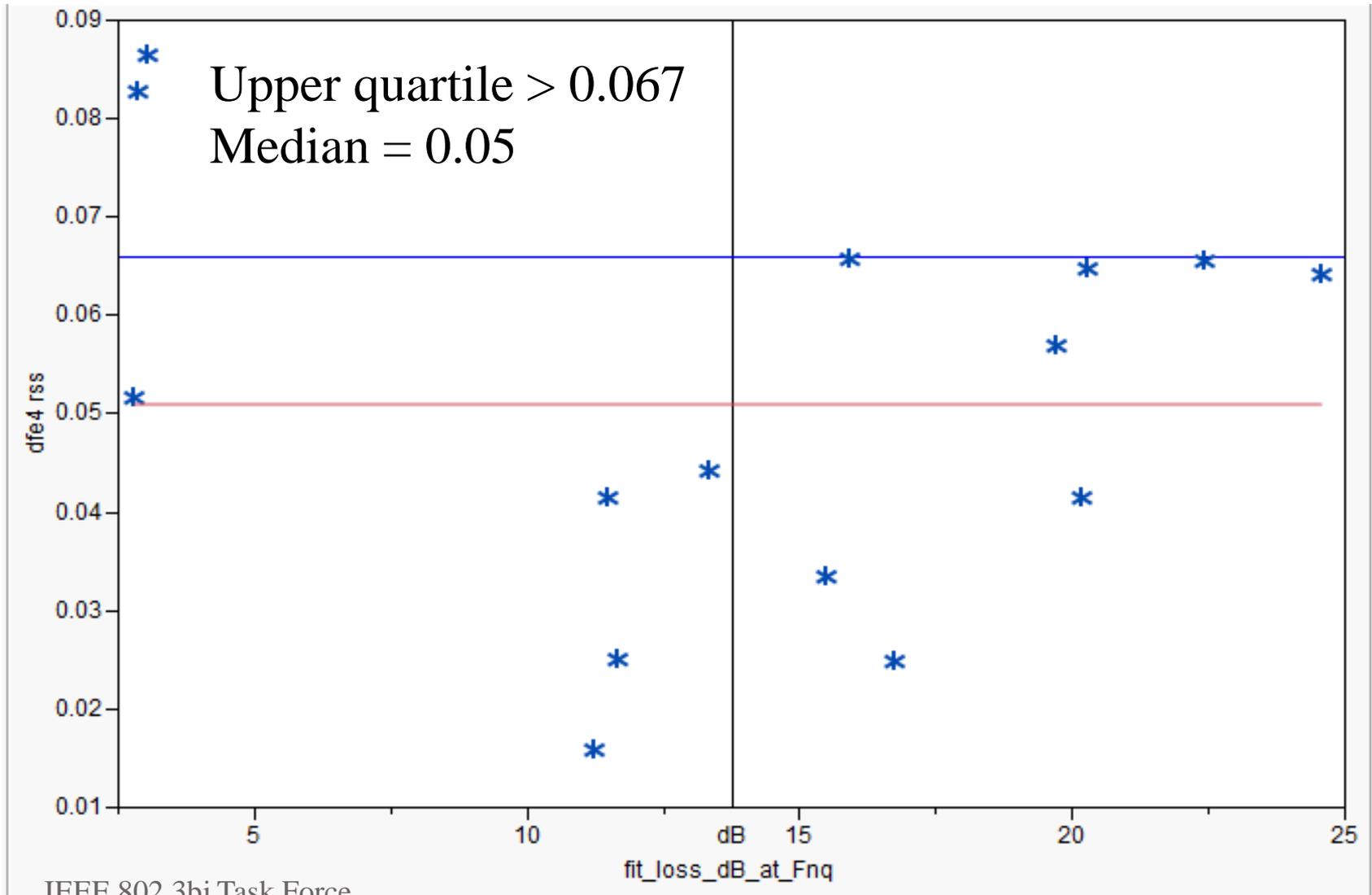
# Data for Comment 33 and 223 Resolution

Richard Mellitz, Intel Corporation

# Some ISI is managed in the DFE region, $N_b$

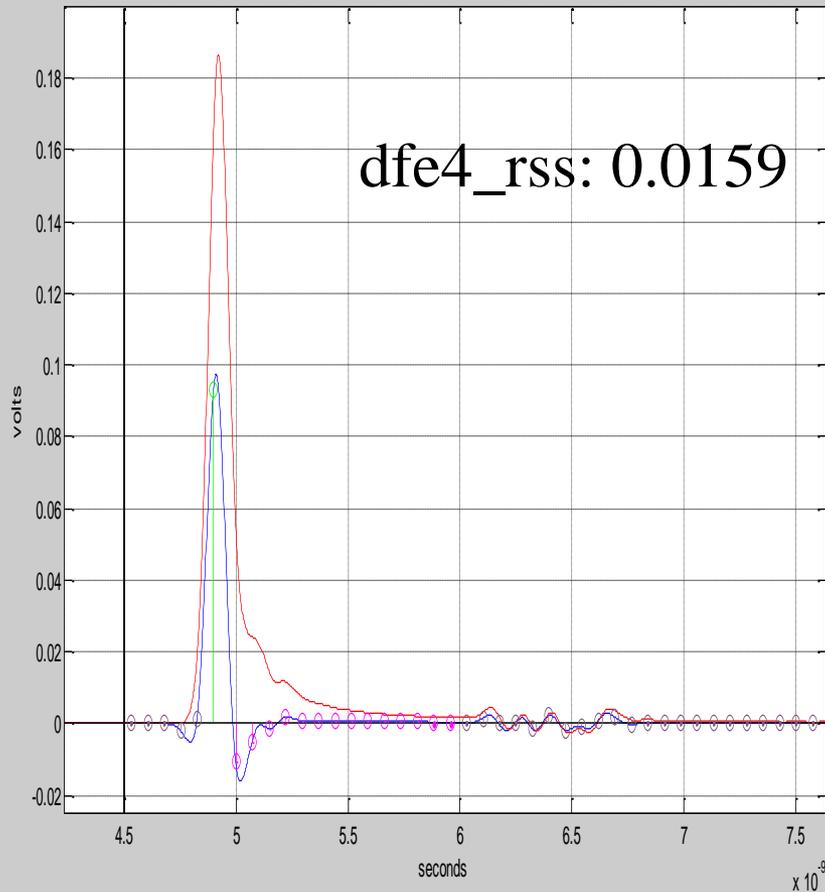


# PAM4 Channels DFE4\_rss to IL Scatter Plot. (FCI, IBM, TEC, Emerson, Channel)

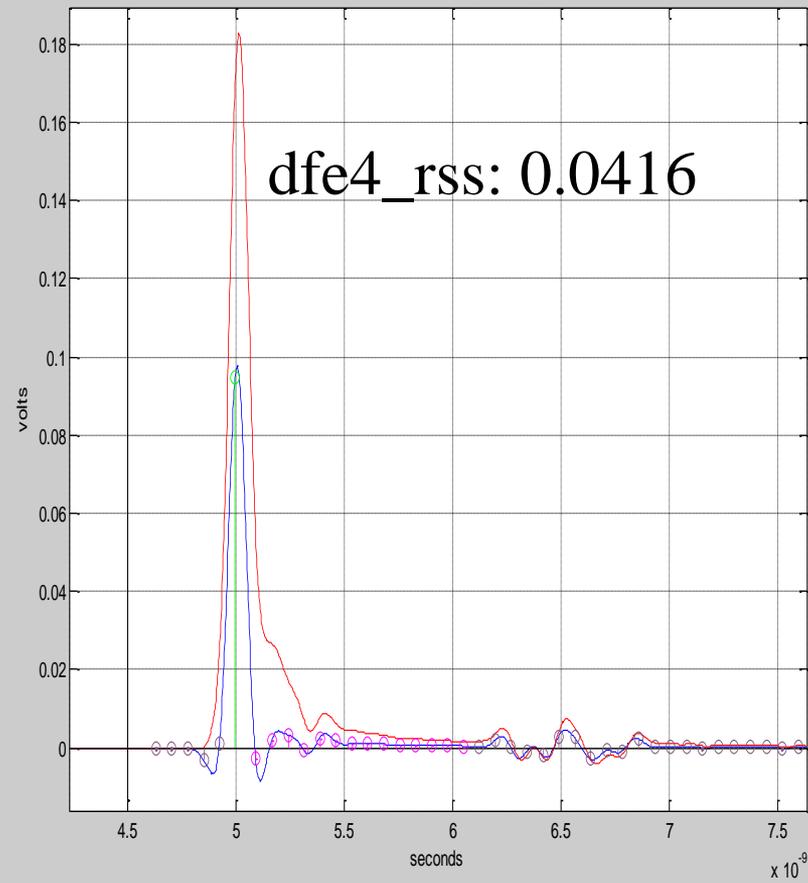


# Its possible to have very little ISI after DFE tap 4

FCL\_CC\_Long\_Link\_Pair\_5\_to\_Pair\_13-FCL\_CC\_Long\_Link\_Pair\_5\_to\_Pair\_13\_Through SBR responses

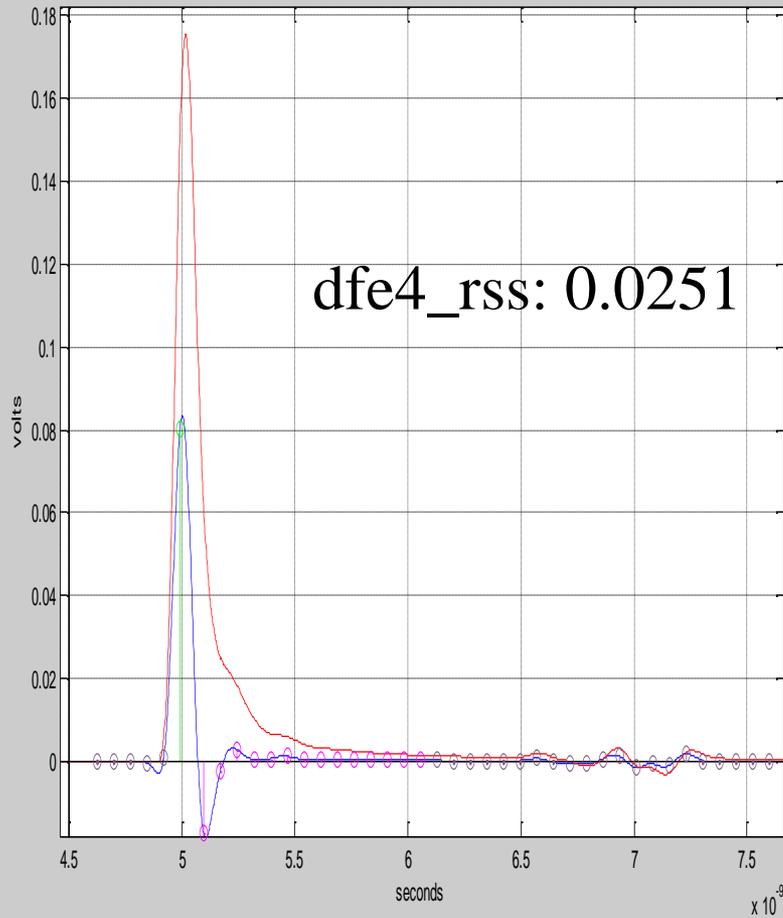


FCL\_CC\_Long\_Link\_Pair\_15\_to\_Pair\_7-FCL\_CC\_Long\_Link\_Pair\_15\_to\_Pair\_7\_Through SBR responses

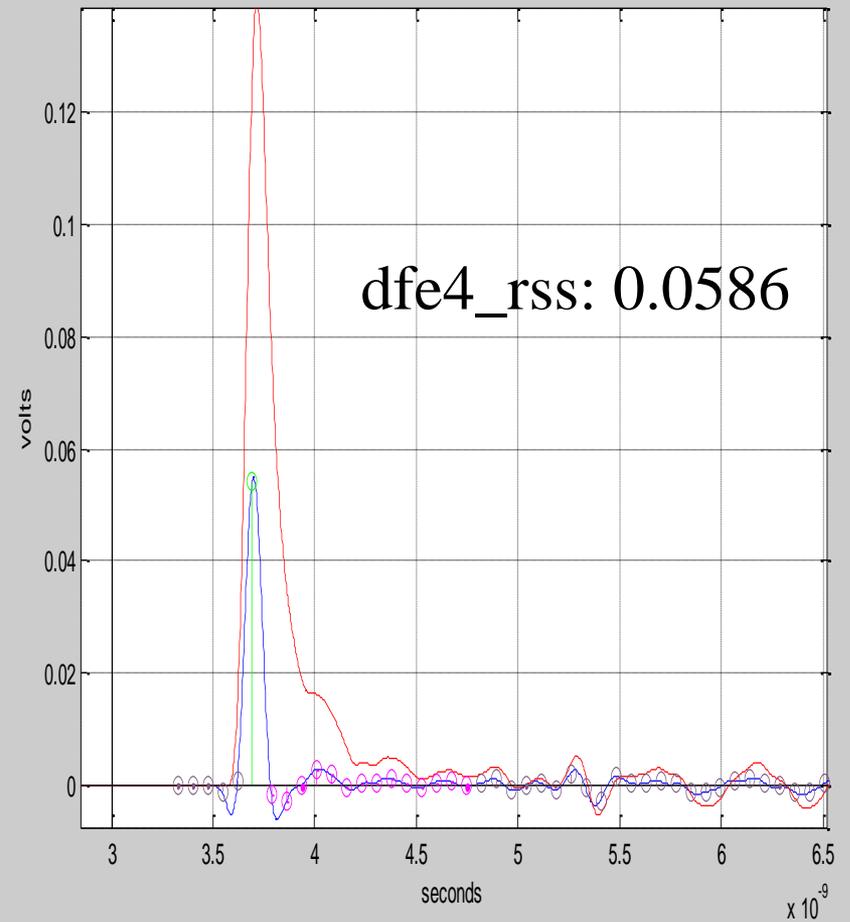


# These Middle links have more dfe4\_rss

TEC\_STRADAWhisper29p8in\_Meg6\_Channel\_IEEE802\_3\_100GbCu\_04282011--TEC\_Whisper29p8in\_Meg6\_THRU\_C8C9 SBR responses

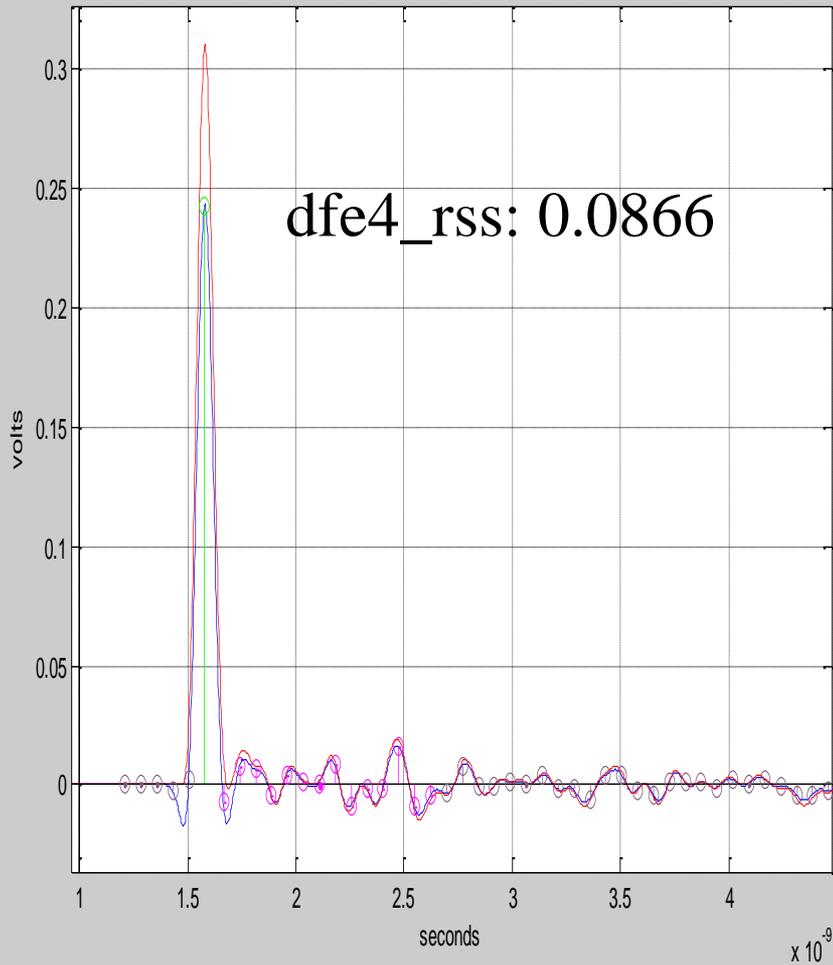


Middle Link--Thru\_S10-P23-04-EF\_S06-P22-06-GH\_NNN SBR responses

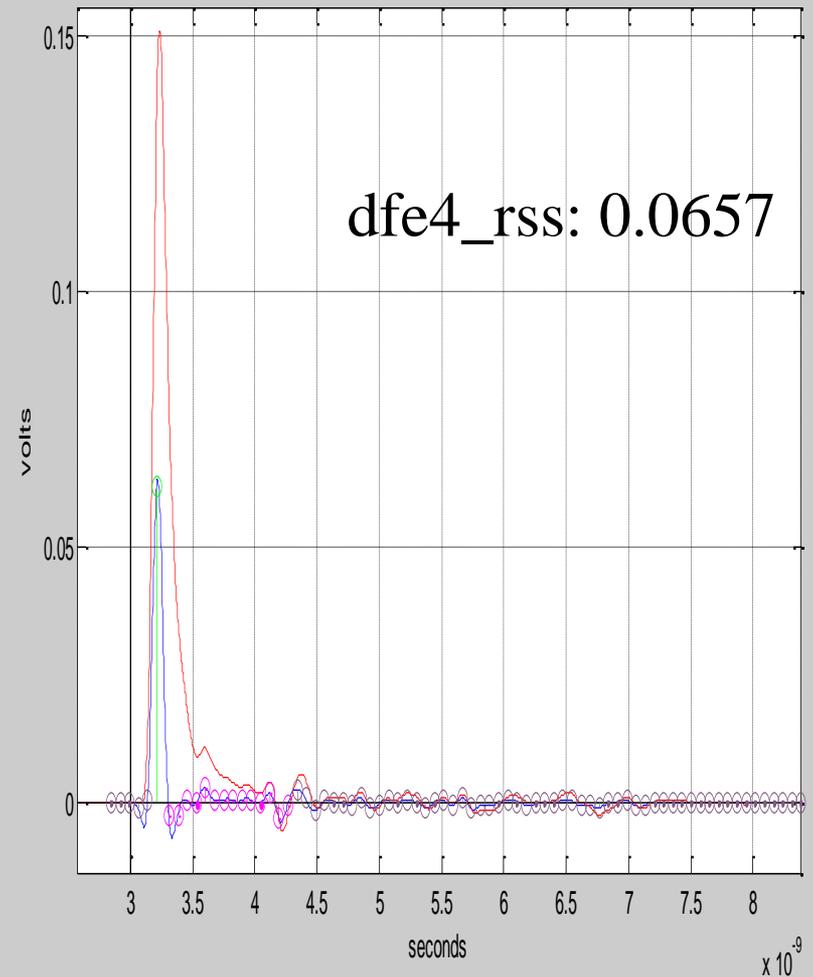


# Some short links have $dfe4\_rss > 0.08$

FCI\_CC\_Short\_Link\_Pair\_15\_to\_Pair\_7-FCI\_CC\_Short\_Link\_Pair\_15\_to\_Pair\_7\_Through\_SBR responses



Shortest Link--Thru\_S07-P23-02-AB\_S09-P23-02-CD\_NNN SBR responses



# Summary

- A compliant receiver (Rx) must work with any compliant channel.
- Corners (loss and reflection) should be considered for test channels
- If test channels are specified for Rx interference tolerance
  - Suggest  $dfe4\_rrs > 0.05$
- Should test channels be dropped in standard?
  - Suggestion: Specify test channels as “recommended”.