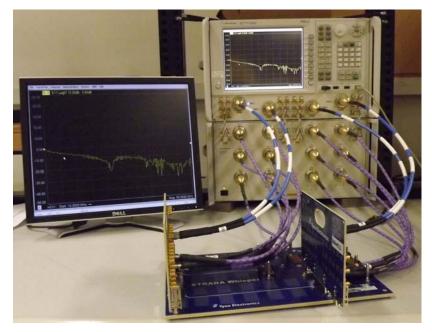
1m Backplane Channels Using Standard and Embedded Capacitor Connectors 40" System Measurements

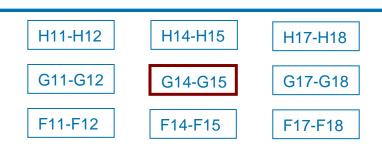
Nathan Tracy TE Connectivity

July 2013 Plenary Meeting, Geneva, Switzerland



## STRADA Whisper<sup>TM</sup> Backplane Channel 40" Link Test Set-up





- All data is measured and includes 2.4mm test points
- Measurements are pair G14-G15 centric .s4p files
- 4 Near-End and 4 Far-End measurements
- Data is from 0-30GHz in 10MHz steps

#### DAUGHTER CARD

- Board Material = Megtron6 VLP
- Trace length = 5"
- Trace geometry = Stripline
- Trace width = 6 mils
- Differential trace spacing = 9 mils
- PCB thickness = 110mils, 14 layers
- Counterbored vias, up to 6mil stub
- Test Points = 2.4mm (included in data)

### BACKPLANE

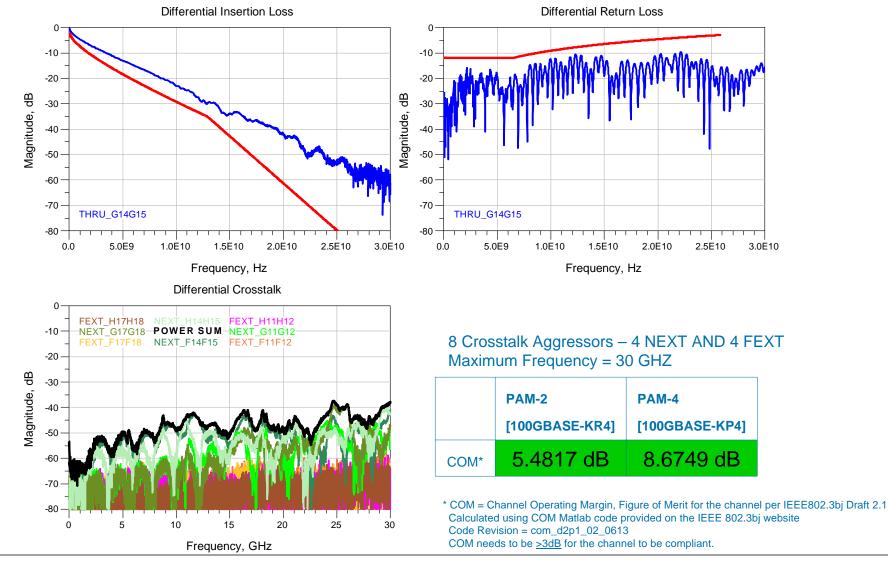
- Board Material = Megtron6 HVLP
- Trace length = 30"
- Trace geometry = Stripline
- Trace width = 6 mils
- Differential trace spacing = 9 mils
- PCB thickness = 200 mils, 20 layers
- Counterbored vias, up to 6mil stub

### **CONNECTORS**

- Dataset 1 includes
  - Mated standard STRADA Whisper connector at each end
- Dataset 2 includes
  - Mated Embedded Capacitor STRADA Whisper connector at one end and,
  - Mated standard STRADA Whisper connector at other end

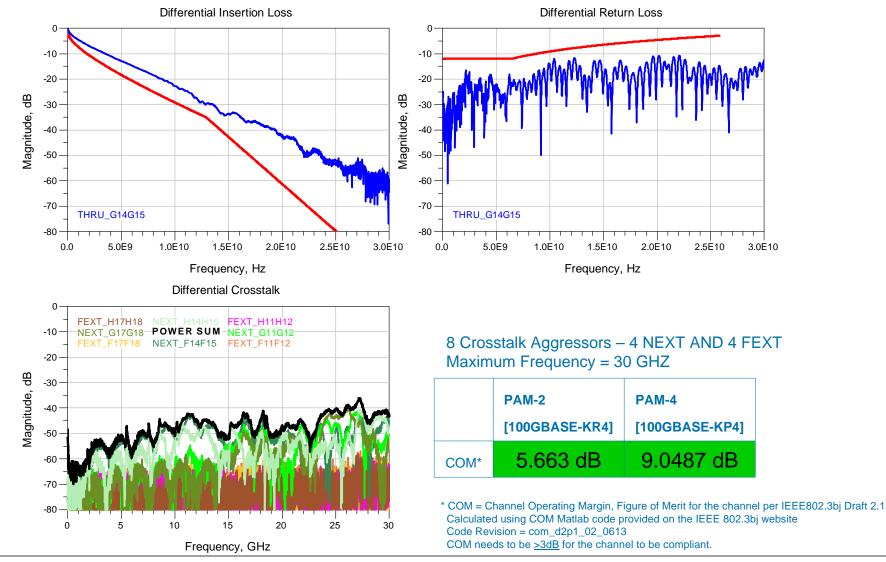


## STRADA Whisper 4.5mm 40" System STANDARD





# STRADA Whisper 4.5mm 40" System EMBEDDED CAPACITOR







• Data will be uploaded to the 802.3bj public channel data web site

• Please use the data for your simulations and provide feedback

