Cost, power, size differences of proposed MMF PMDs:
20 m reach un-retimed
vs
100 m reach retimed baseline

11<sup>th</sup> April 2013 Jonathan King, Finisar Ryan Latchman, Mindspeed

# Introduction

- 100 m reach MMF PMD baseline
  - 4 lanes, fully retimed, FEC supported
  - 850 nm VCSEL compatible specs
- Proposed 20 m reach MMF PMD
  - 4 lanes, un-retimed, FEC supported
  - 850 nm VCSEL compatible specs
    - (same optics as 100 m reach PMD)
- Main difference is power consumption delta for retiming function on 8 lanes (Tx + Rx lanes)
  - Same optics BOM, PCBA, shell, connectors, much IC functionality in common

# Cost

- Same BOM cost for 20 m un-retimed and 100m retimed modules
  - Test cost for retimed module probably slightly lower than un-retimed module
    - BERT vs high speed oscilloscope measurements
  - Cost of CDRs is negligible
    - CDRs are a small percentage of total IC area

#### cost difference is small

# Power comparison between retimed and un-retimed

| Function                | Retimed | Unretimed |
|-------------------------|---------|-----------|
| Amplification (TIA/LA)  | Yes     | Yes       |
| Equalization (CTLE)     | Yes     | Yes       |
| VCSEL Laser Driver      | Yes     | Yes       |
| Host Driver             | Yes     | Yes       |
| uC and other            | Yes     | Yes       |
| Clock and data recovery | Yes     | No        |

In Q1'13, how much power did the CDR function consume? 55 mW/channel Will this go down over time? Yes

# Power

#### • Finisar:

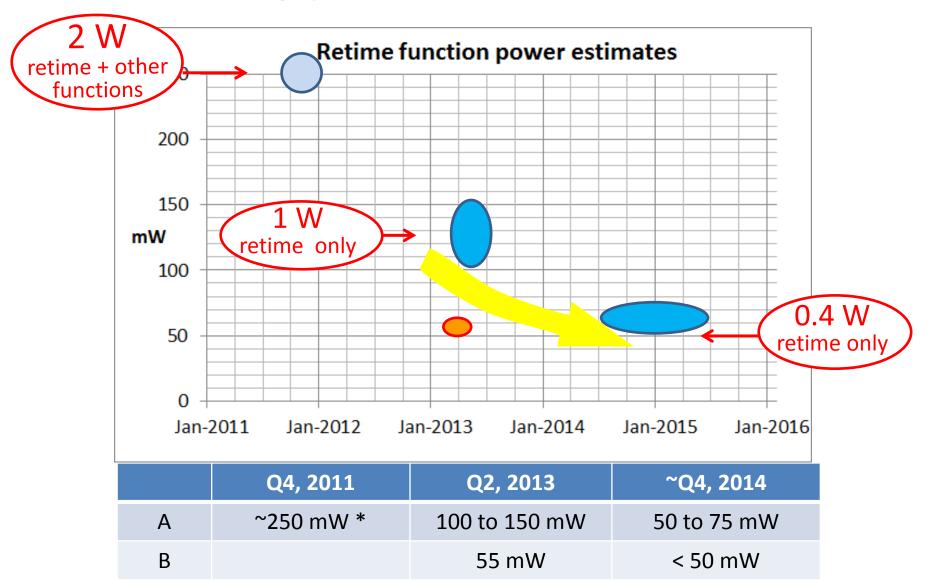
- End of 2011 estimate was ~ 250 mW per CDR IC (including IO and other functionality) (king\_01\_1111)
- Current 26 Gb/s CDR designs: 100 to 150 mW per CDR for retime function alone (SiGe)
  - 800 to 1200 mW per module
- Expect 50 to 75 mW per CDR for retime function alone in 1 to 2 year time frame
  - 400 to 600 mW per module (SiGe)

### Mindspeed

- Current 26 Gb/s CDR designs: 55 mW per CDR measured
  - 440 mW per module

Power difference < 0.5W

# Retiming power/module with time



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# Size

- Same component count for 100 m reach retimed and 20 m reach un-retimed modules
  - Removing CDRs doesn't reduce size of components
    - 4x: VCSELs, drivers, receivers, input/output CTLEs; management functions and
       12C; 8 lane wide electrical connector, optical connector.
  - Both are QSFP+/CFP4 compatible
- Similar power dissipation
  - Retimed 100 m reach module ~ 3 W
  - Un-retimed 20 m reach module ~ 2.5 W ?
  - Both are QSFP+/CFP4 compatible

same BOM + similar power = same size

# Conclusion

 By about the same time 802.3bm is technically stable (H2 2014) there will be no significant power, cost or size advantage to be gained from an un-retimed short reach PMD.