

# Editor's report

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# Introduction

The IEEE P802.3bm 40 Gb/s and 100 Gb/s Operation Over Fiber Optic Cables Task Force has adopted objectives to:

Define re-timed 4-lane 100G PMA to PMA electrical interfaces for chip to chip and chip to module applications

Define a 40 Gb/s PHY for operation over at least 40 km of SMF

Define a 100 Gb/s PHY for operation up to at least 500 m of SMF

Define a 100 Gb/s PHY for operation up to at least 100 m of MMF

Define a 100 Gb/s PHY for operation up to at least 20 m of MMF

It is now the job of the Task Force to work towards adopting baseline proposals that satisfy the remaining objectives.

# Amended clauses

Clause	Change	
1	Add new references, definitions, abbreviations	
30	Add new management objects / attributes	
45	Add new registers / bits	
78	Add new EEE PHYs	If EEE adopted
80	Add new PHYs	
86	Minor changes for EEE option	If EEE adopted
87	40GBASE-ER4 and changes for EEE option	Baseline adopted
88	Add CAUI-4 option and changes for EEE option	If EEE adopted
89	Minor changes for EEE option	If EEE adopted
Annex A	Add any new bibliography entries	

# New clauses

Clause	Content	
XX	100GBASE-SR4	Need baseline
XX+1	100GBASE-?R4	Need baseline
83D	CAUI-4 chip to chip	Need baseline
83E	CAUI-4 chip to module	Need baseline

# What's next

Develop consensus presentations

Adopt baseline proposals

Thanks!