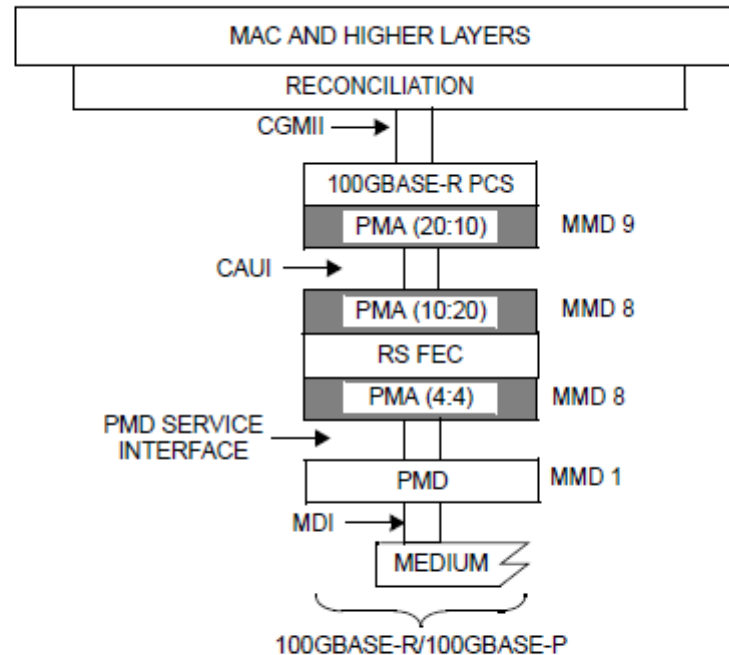


The background of the slide is a detailed architectural drawing of a building's floor plan. The drawing is rendered in a light blue color with a fine grid pattern. Several areas of the drawing are highlighted with a yellow diagonal hatching pattern, indicating specific structural or design elements.

**LAYER DIAGRAMS WITH FEC AND  
CAUI-4  
PIERS DAWE  
IEEE P802.3BM NOVEMBER 2012**

## Example layer diagram from P802.3bj D1.2

### 83C.1a.2 Single CAUI with RS FEC

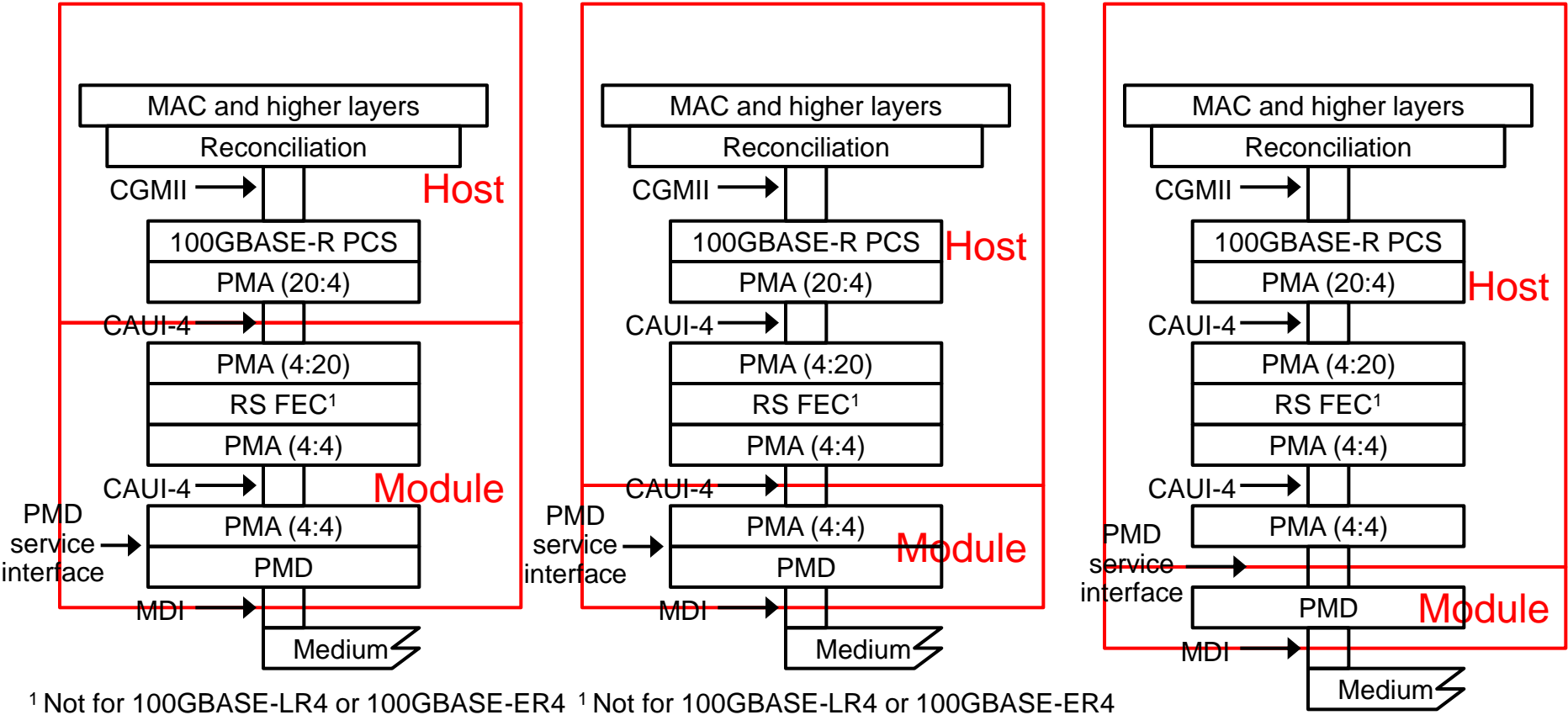


CAUI = 100 Gb/s ATTACHMENT UNIT INTERFACE      MMD = MDIO MANAGEABLE DEVICE  
 CGMII = 100 Gb/s MEDIA INDEPENDENT INTERFACE      PCS = PHYSICAL CODING SUBLAYER  
 MAC = MEDIA ACCESS CONTROL      PMA = PHYSICAL MEDIUM ATTACHMENT  
 MDI = MEDIUM DEPENDENT INTERFACE      PMD = PHYSICAL MEDIUM DEPENDENT

Figure 83C-2a—Example single XLAUI/CAUI with RS FEC

- 10-lane PMAs are not allowed below this FEC

## Possible CAUI-4 positions

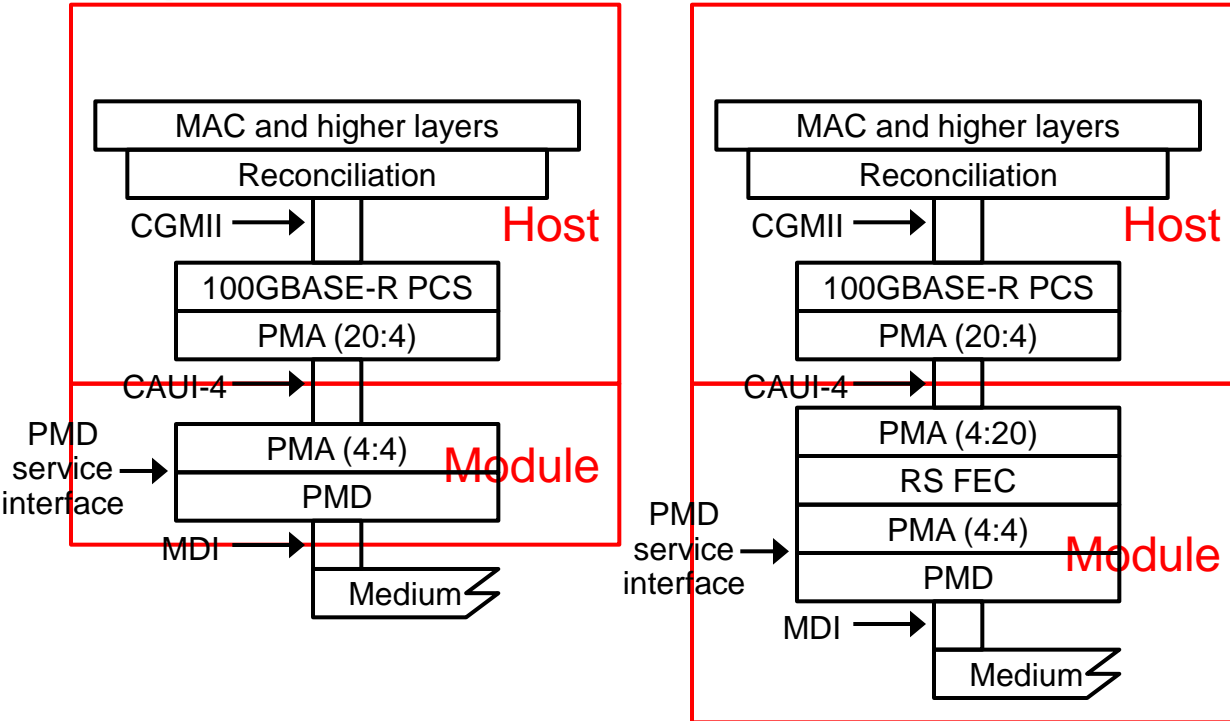


<sup>1</sup> Not for 100GBASE-LR4 or 100GBASE-ER4

<sup>1</sup> Not for 100GBASE-LR4 or 100GBASE-ER4

- There are other possibilities with fewer CAUI-4 interfaces
  - Examples on next two slides
- Note some CAUI-4 are FEC protected, others are not

## Examples for host without FEC

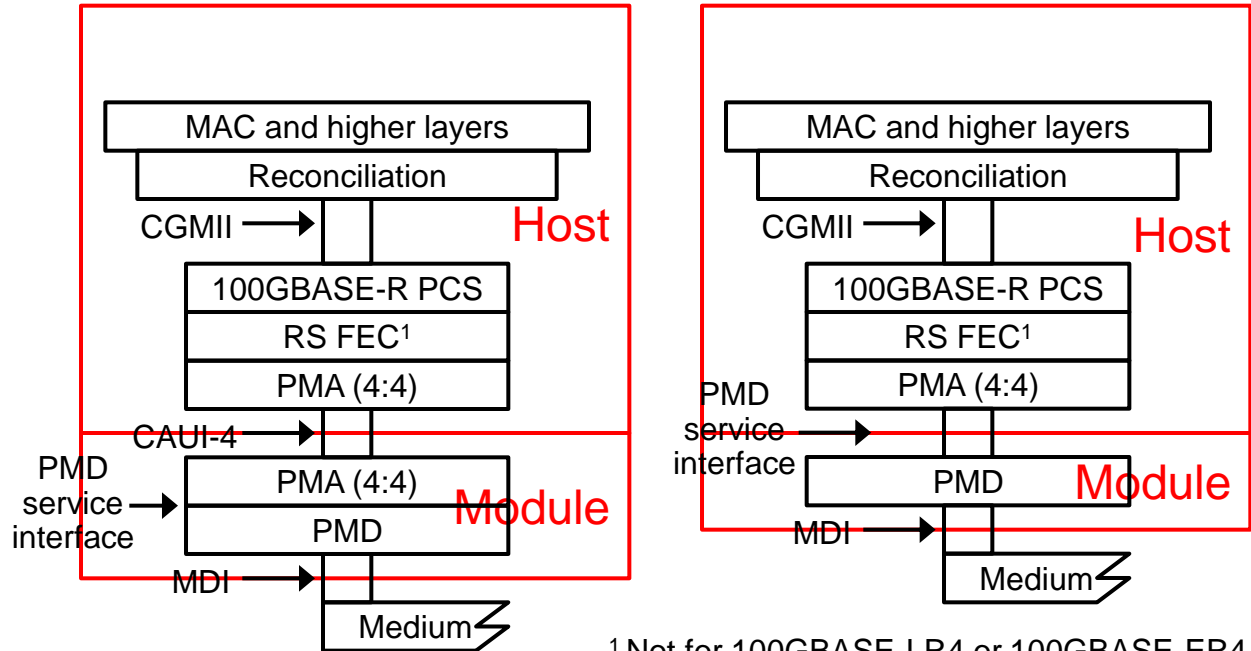


100GBASE-LR4 or  
100GBASE-ER4

New 100G PHY types  
that use FEC

- Two variants of first example on previous slide

## Examples for host with FEC



<sup>1</sup> Not for 100GBASE-LR4 or 100GBASE-ER4

<sup>1</sup> Not for 100GBASE-LR4 or 100GBASE-ER4

- Simplifications of second and third examples on previous slide