Comment #122 on Draft 1.1

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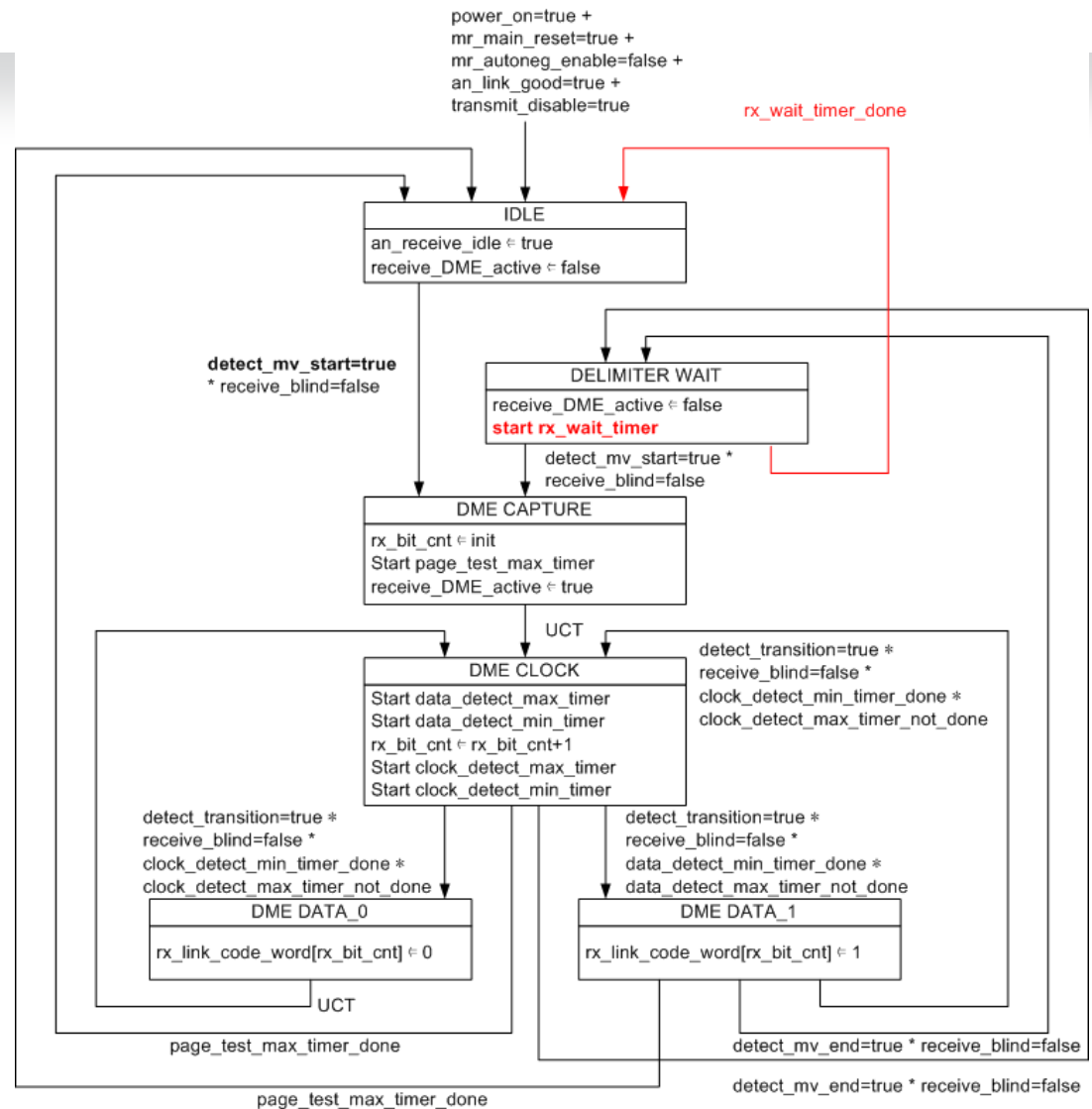MARVELL®

Comment #122

▶ **Issue**

- **There is a lockup condition in state DELIMITER WAIT.**

- **If the link partner stops transmitting there is no path back to the IDLE state which triggers TRANSMIT_DISABLE**

▶ **Remedy**

- **Add a transition to IDLE. Clause 73 had this transition. See Fig 73-10**

- **Take this path after a sufficient timeout rx_wait_timer = 100 to 105us**

power_on=true +
mr_main_reset=true +
mr_autoneg_enable=false +
an_link_good=true +
transmit_disable=true

rx_wait_timer_done

IDLE
an_receive_idle ⇐ true
receive_DME_active ⇐ false

detect_mv_start=true
* receive_blind=false

DELIMITER WAIT
receive_DME_active ⇐ false
start rx_wait_timer

detect_mv_start=true *
receive_blind=false

DME CAPTURE
rx_bit_cnt ⇐ init
Start page_test_max_timer
receive_DME_active ⇐ true

UCT

DME CLOCK
Start data_detect_max_timer
Start data_detect_min_timer
rx_bit_cnt ⇐ rx_bit_cnt+1
Start clock_detect_max_timer
Start clock_detect_min_timer

detect_transition=true *
receive_blind=false *
clock_detect_min_timer_done *
clock_detect_max_timer_not_done

detect_transition=true *
receive_blind=false *
clock_detect_min_timer_done *
clock_detect_max_timer_not_done

detect_transition=true *
receive_blind=false *
data_detect_min_timer_done *
data_detect_max_timer_not_done

DME DATA_0
rx_link_code_word[rx_bit_cnt] ⇐ 0

DME DATA_1
rx_link_code_word[rx_bit_cnt] ⇐ 1

UCT

page_test_max_timer_done

detect_mv_end=true * receive_blind=false

detect_mv_end=true * receive_blind=false

page_test_max_timer_done

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THANK YOU

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