

# 802.3bq Editors Closing Report

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# Status

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- Draft 2.0 has been produced from comments on Draft 1.2
  - Thanks to hard work by Valerie Maguire, and some extra consulting by Matt Brown
- 99 Received Comments from 7 individuals
  - One withdrawn, one added from the floor in response to draft preview
    - 50 from the Editor, mainly typos, references, editorial coordination and cleanup of informative text to align with prior changes
  - 70 E or ER: 41 Editorial, 29 Editorial Required
  - 28 T or TR: 19 Technical, 9 Technical Required

# Progress of Comments

	<b>Draft</b>	<b>1</b>	<b>1.1.1</b>	<b>1.2</b>
<b>Commenters</b>		9	7	7
<b>Total Comments</b>		129	36	98
<b>TR</b>		23	4	9
<b>T</b>		50	17	19
<b>ER</b>		27	9	29
<b>E</b>		29	6	41
<b>T+TR</b>		73	21	28
<b>E+ER</b>		56	15	70
<b>TR+ER</b>		50	13	38

# Progress on Technical Comments

	Draft/(T+TR count)	1.0 / (73)	1.1.1 / (21)	1.2 / (28)
<b>Typos &amp; Xrefs in Tech Text</b>		2	1	4
<b>Filling out technical specs</b>		21	5	3
<i>Link Segment (included in above)</i>		16	5	2
<b>Cleaning up informative text</b>		4	2	9
<b>NEW FEATURE OR CHANGE PROPOSALS</b>	46/11 areas	13/7 areas	12/4 areas	
Proposal for alternative MDI		1		5
Proposal to add Class II references		4		5
Loop timing mandatory		5	1	
40G clause references			4	
leftover 10G references		4	2	
Additional RS-FEC		4	2	
Test modes			2	
Test procedures		3	1	
Training & Autoneg		12	1	
EEE		7		1
Management bits (non editorial coord)		3		
Fast Retrain		2		1
PMA Electrical		1		

# Summary of changes by Topic

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- 47 accepted “EZ or “EZ Check” comments (+ 1 other typo)
  - typos or cross-references or external reference issues
  - Also separately cleaned up technical typo in Cabling equation 113-15
  - Accepted editorial change (in preview draft) of technical typo in RS-FEC proposal
- 4 Removals of informative TBDs by equations
- Deleted most editors notes meant to draw attention in Task Force review
- Simplified Direct Attach NEXT equations with latest TIA specification
- Text clean up:
  - Overview statement that fast retrain is optional (already in the requirements text)
  - Cleaned out hanging text left over from optional loop timing
- Incorporated results of EM/Common-Mode Noise Immunity ad hoc
- 6 “ED-COORD” – (Clause 45 coordination & naming)
  - Editorial style or coordination from Pete Anslow on where registers go
- Cleaned up editing instructions and deleted blank cells tables in Clause 78
- Excluded 40GBASE-T from optical 40G EEE fast wake
- Modified text referring to ISO Class I and Class II cabling support

# Adding in 25GBASE-T... Maybe Next Time

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- BIG WORK: repeat much of the detail work we've just done to take clause 55 text and convert it to 40G:
  - Frequency scale PMA/PMD and rate specs
    - 15 references to Msymbol/sec + 31 references to MHz on PHY
      - 8 of which are test setups
  - Scale or clean-out fixed references to times (e.g., msec in EEE)
  - Autoneg changes
  - Management register name changes & bit allocs to include 25G
- Little stuff:
  - Every 64-bit XLGMII transfer will have parallel reference to 2 32-bit 25GMII transfers
  - Do we want to define use of the 25G-AUI? (NEW!)
  - Figures and tables for inclusion in 25G clauses
- If you want to help, contact me – needs checking for completeness
- What we don't HAVE to do:
  - New link segment, new PCS to protect unprotected bits, loop timing changes