JOINT P802.3bq CHANNEL MODELING AND PHY BASELINE PROPOSAL AD HOC Agenda and Meeting Minutes - 24 April 2014

Participants are encouraged to review IEEE meeting guidelines available at the following URL - <u>https://development.standards.ieee.org/myproject/Public/mytools/mob/preparslides.pdf</u>

The approved agenda for the meeting follows.

10:06 AM Pacific Time meeting start

- Roll call : Please send an email indicating your attendance, employer and affiliation to mailto:george@cmeconsulting.onmicrosoft.com?subject=802.3bq PHY ad hoc attendance 27Apr 2014
- 2. Reminder of IEEE patent policy www.ieee802.org/3/patent.html
- 3. Housekeeping

Review & approve meeting agenda. : Approved as modified with George Zimmerman presenting for Paul Langner. Approved by voice vote without opposition.

Approve minutes from 3 April 2014 PHY ad hoc meeting http://www.ieee802.org/3/bq/public/phyproposal/minutes_3bq_phyadhoc_3Apr2014_unapproved.pdf

Approve minutes from 8 April 2014 Channel Modeling ad hoc meeting <u>http://www.ieee802.org/3/bq/public/channelmodeling/2014 Apr8 Channel Model Ad Hoc Meeting</u> <u>Minutes.pdf</u>

Both sets of minutes were approved by voice without objection

4. Old business from previous ad hoc meetings:

Channel Modeling ad hoc sub-team updates with discussion – status of new/ongoing work

- Noise modeling update/requests? No new requests channel modeling ad hoc collection of system background noise is complete.
- MDI models updated RJ45 ICM?
 - Waiting on materials not sure if he'll have anything for May, likely ad hoc between May & July
- Additional cabling channels?

For the PHY ad hoc, the following future contributions were planned in February and are still outstanding:

- Cable bending End user inputs (Dave C./Pete C.)
- 5. New business at this meeting:

No Contributions for Channel Ad Hoc

Contribution: langner_3bqah_01_0414.pdf Title: DSQ128+ Author/Presenter: Paul Langner, Aquantia (emailed to reflector) – presented by George Zimmerman

Abstract: This contribution details a method for providing FEC protection for the uncoded bits of 40GBASE-T without increasing the symbol rate. The method is simple and substantially reuses the coding and modulation machinery from 10GBASE-T.

Discussion: there were questions for clarification. A request was made for comparing the error detection capability with the existing CRC-8. One participant suggested that he was preparing a contribution with an alternative technique.

Multiple participants indicated interest and desire to have the uncoded bits covered with an error correcting code, and thought this was a good idea.

The TF Chair urged participants to bring proposals forward early and to drive for consensus at the May Task Force meeting.

- 6. Next meeting time: Proposed Thursday May 8, **11AM** PDT. (TBD see reflector for update)
- 7. Adjournment: 10:58AM PDT.

Name	Employer	Affiliation (if different)
David Chalupsky	Intel	
Jerry Chiang	Foxconn Interconnect	
	Technology	
Chris DiMinico	MC Communications	Panduit
Mike Good	Nexans	
Bob Wagner	Panduit	
Paul Wachtel	Panduit	
Wayne Larsen	Commscope	
Victor Renteria	BelFuse/TRP	
Paul Vanderlaan	Berk-Tek LLC	
Dieter Schicketanz	University of Science, Reutlingen	Leoni Kerpen
Tom Souvignier	Broadcom	
Peter Wu	Marvell	
Thuyen Dinh	Pulse Electronics	
George Zimmerman	CME Consulting	Aquantia, Commscope

Meeting Attendance 4/23/14