

802.3bq Editors Report

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Editor, IEEE P802.3bq

Editorial Team (June 2014)

- George Zimmerman, Chief Editor
 - Clause 98.x, PHY specifications, excluding 98.7
- Chris Diminico, Editor
 - Clause 98.7, Link Segment Characteristics
- Valerie Maguire, Editor
 - Front matter, Clause 1 (Defs), Clause 28 Autoneg
- Curtis Donahue, Editor
 - PICS
- *Help Wanted, Editor*
 - Clause 78 Energy Efficient Ethernet
 - Clauses 98.2 & 98.3 Architecture/PCS/FEC sublayer

Editorial Responsibilities

Clause	Amendments	Owner
1	New normative references, definitions, abbreviations	Val
28	Auto-Negotiation support for new PHY	Val
	Annex 28b, 28C, 28D	Val
30	Add new management objects/attributes	TBD
45	Add new registers/bits	TBD
78	Add new Energy Efficient PHYs	TBD
80	Add new 40 Gb/s Ethernet PHY, EEE primitives	Val
81	XLGMII - any change?	
82	EEE signaling and state machines	TBD
83	XLAUI - any change? (add base-t to diagrams)	
98	40GBASE-T 98.1-.6	George
	98.7 channel	Chris
	.8 MDI	George
	.9 Environmental	George

Previously in 802.3bq...

- Adopted Strawman based on 10GBASE-T
 - Copied in Clause 55 text as a starting point
 - Adopted PHY / PMA / LDPC specifications
 - Adopted new PBO schedule, alien test levels
 - Added transcoding and RS FEC to 'PCS'
- Adopted FEC sublayer proposal and architecture
 - Reviewed changes due to architecture – discussion this meeting
- Adopted many link segment parameters
 - Crosstalk parameters unresolved
- MDI untouched - left as Clause 55 without comment
 - (Draft 0.8 should have had editors note)

Items to wait until further definition of other clauses

- Definitions (Clause 1)
- Management Registers (Clause 30, 45)
- Autonegotiation bit modifications (Clause 28)
- Clause 98.12 – PICS
- All these are much more easily done after the rest of the document is full

Big Ticket Items this Meeting

- General comments received
- Order of the below based on availability of key outside participants
 - MDI Status
 - Comments & Contributions
 - Link Segment Parameters
 - Comments received, Contributions
 - PCS/Architecture Review
 - Discussion of draft impacts, needs & reuse

Draft 0.8 Comments

- Comments received informally have been posted, where files are available
 - Thanks to:
 - Alan Flatman (Posted Comments)
 - Wayne Larsen ([Posted Comments](#))
 - Dieter Schicketanz ([Posted Comments](#))
 - Chris Diminico ([diminico_3bq_01_0914.pdf](#))
 - Brett McClellan (comment by email, in this deck)
- Comments predominantly on 98.7

General Comments

- Good example of how to make comments
 - Brett McClellan:
 - Clause 98.6.1.2
 - Page 164
 - Table 98-15
 - Bit 16 description is incorrect.
 - “U16 10GBASE-T ability
 - (1 = support of 40GBASE-T and 0 = no support)”
 - Should be
 - “U16 10GBASE-T ability
 - (1 = support of 10GBASE-T and 0 = no support)”

Review of draft

- (Review Draft 0.8 here, section by section)
- Break for Architecture, MDI and Link Segment characteristics as scheduled