

Logic Ad Hoc Report

IEEE P802.3bs 400 Gb/s Ethernet Task Force

May 2016 Whistler

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400GbE Logic Report

- The stated charter is: **To address all issues in relation to the overall architecture of IEEE P802.3bs to ensure progress towards a technically complete draft**
- Dates of future logic ad hoc meetings will be announced via the reflector
- The 400 Gb/s Ethernet Task Force Logic Ad Hoc held two meetings

400GbE Logic Ad Hoc Presentations (4/28/16)

- **PreFEC BER Signaling Features - David Ofelt, Juniper**
- **SSPRQ test pattern - Pete Anslow, Ciena**
- **400GbE AMs revisited - Pete Anslow, Ciena**

Thanks!