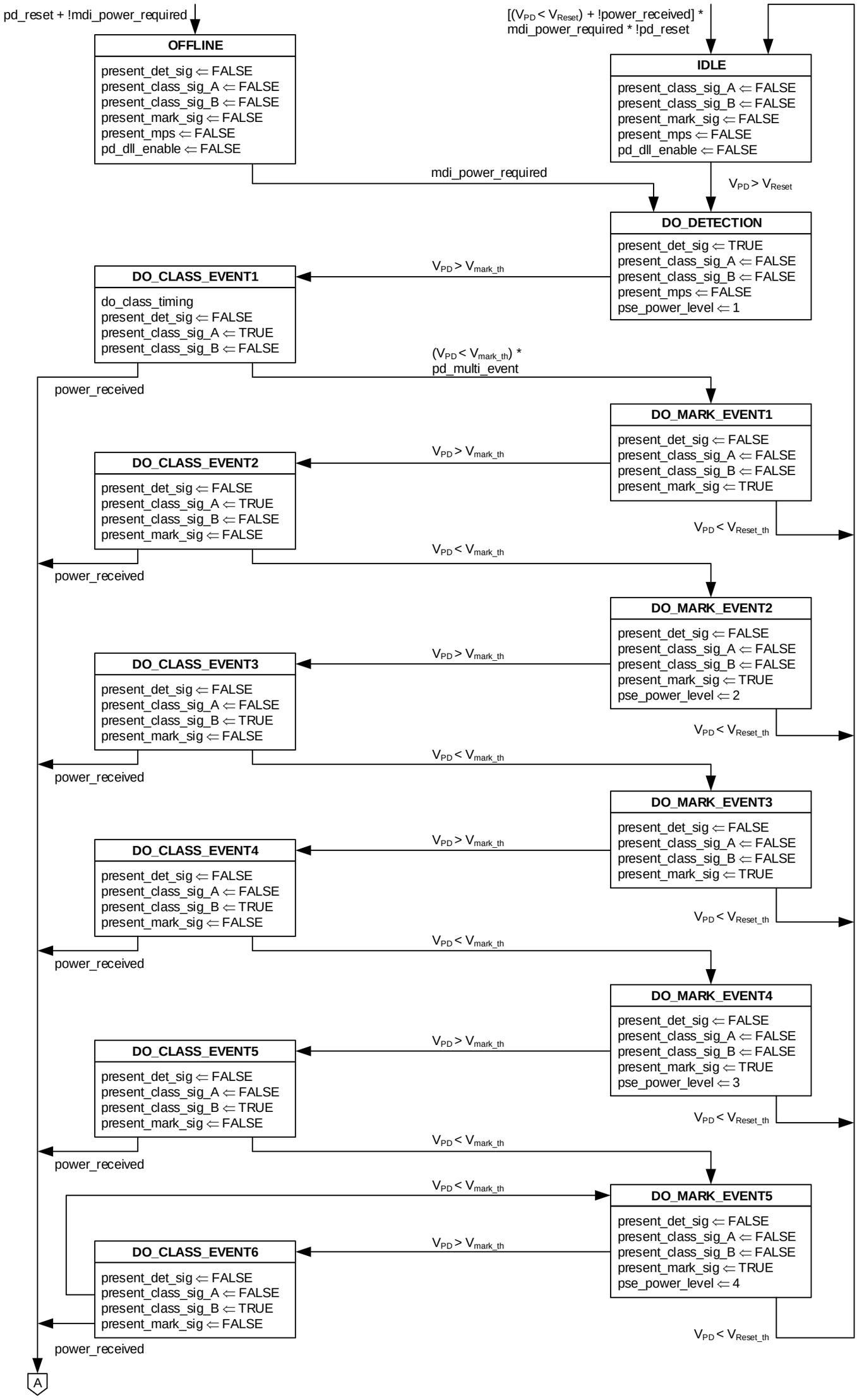
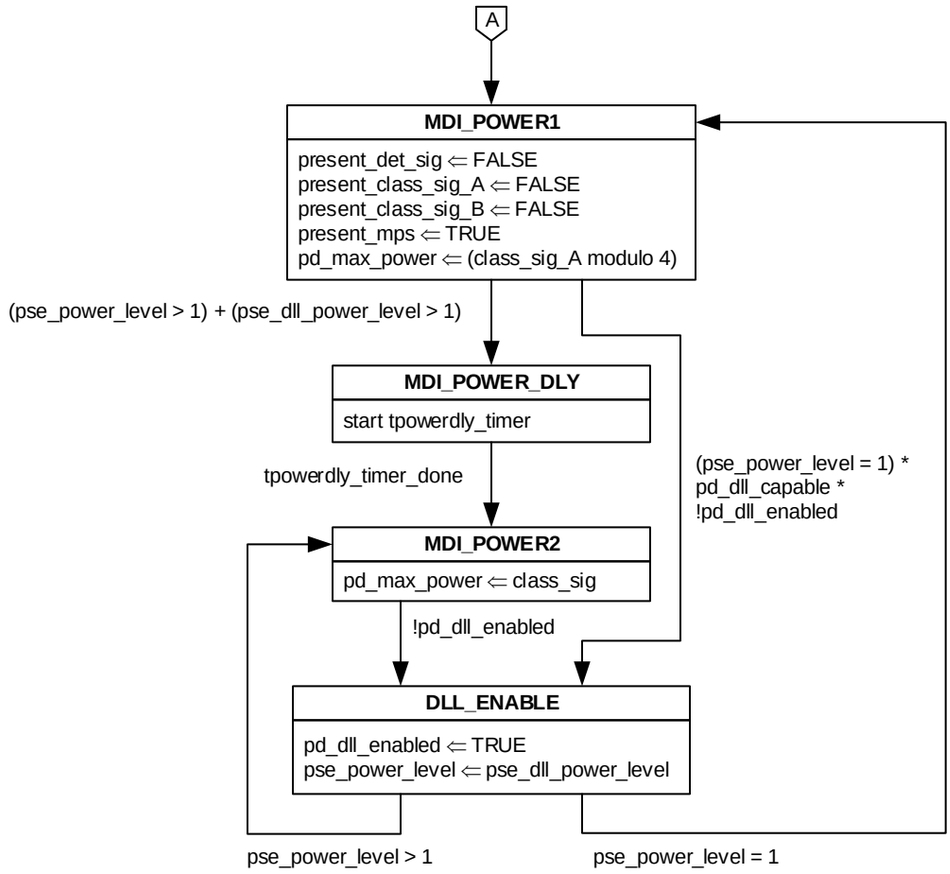

pd_reset + !mdi_power_required
OFFLINE
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ FALSE
present_mps ⇐ FALSE
pd_dll_enable ⇐ FALSE
[(VPD < VReset) + !power_received] *
mdi_power_required * !pd_reset
IDLE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ FALSE
present_mps ⇐ FALSE
pd_dll_enable ⇐ FALSE
mdi_power_required
VPD > VReset
DO_DETECTION
present_det_sig ⇐ TRUE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mps ⇐ FALSE
pse_power_level ⇐ 1
VPD > Vmark_th
DO_CLASS_EVENT1
do_class_timing
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ TRUE
present_class_sig_B ⇐ FALSE
power_received
(VPD < Vmark_th) *
pd_multi_event
DO_MARK_EVENT1
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ TRUE
VPD > Vmark_th
DO_CLASS_EVENT2
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ TRUE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ FALSE
VPD < VReset_th
VPD < Vmark_th
power_received
DO_MARK_EVENT2
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ TRUE
pse_power_level ⇐ 2
VPD > Vmark_th
DO_CLASS_EVENT3
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ TRUE
present_mark_sig ⇐ FALSE
VPD < VReset_th
VPD < Vmark_th
power_received
DO_MARK_EVENT3
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ TRUE
VPD > Vmark_th
DO_CLASS_EVENT4
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ TRUE
present_mark_sig ⇐ FALSE
VPD < VReset_th
VPD < Vmark_th
power_received
DO_MARK_EVENT4
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ TRUE
pse_power_level ⇐ 3
VPD > Vmark_th
DO_CLASS_EVENT5
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ TRUE
present_mark_sig ⇐ FALSE
VPD < VReset_th
VPD < Vmark_th
power_received
VPD < Vmark_th
DO_MARK_EVENT5
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mark_sig ⇐ TRUE
pse_power_level ⇐ 4
VPD > Vmark_th
DO_CLASS_EVENT6
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ TRUE
present_mark_sig ⇐ FALSE
VPD < VReset_th
power_received
A


A
MDI_POWER1
present_det_sig ⇐ FALSE
present_class_sig_A ⇐ FALSE
present_class_sig_B ⇐ FALSE
present_mps ⇐ TRUE
pd_max_power ⇐ (class_sig_A modulo 4)
(pse_power_level > 1) + (pse_dll_power_level > 1)
MDI_POWER_DLY
start tpowerdly_timer
tpowerdly_timer_done
(pse_power_level = 1) *
pd_dll_capable *
!pd_dll_enabled
MDI_POWER2
pd_max_power ⇐ class_sig
!pd_dll_enabled
DLL_ENABLE
pd_dll_enabled ⇐ TRUE
pse_power_level ⇐ pse_dll_power_level
pse_power_level > 1
pse_power_level = 1