## 33.2.4.9 Type 3 and Type 4 variables

## CC\_DET\_SEQ

A constant indicating the sequence in which the PSE performs connection check and detection. Values:

- 0: Connection Check is followed by staggered detection for a single-signature PD and parallel detection for a dual-signature PD.
- 1: Detection on a pairset is followed by connection check and then detection on the other pair-set for a single-signature PD and parallel or staggered (starting with first pairset) detection for a dual-signature PD.
- 2: Connection check and detection on both pairsets are performed within a single Tdet window.
- 3: Connection check is followed by staggered detection.

## pinpong\_en

A variable indicating if the PSE uses the method consisting in alternating between Alt A and Alt B during initial detection phase until there is a valid detection.

TRUE: alt\_pri alternates between a and b until a first valid detection.

FALSE: alt pri does not change and is user defined.

## semi\_pwr\_en

A variable indicating if, in the case of SS PD, the PSE uses the method consisting in turning off only the pairset on which a short-circuit, overload or out of range  $V_{PSE}$  is detected.

TRUE: Only the pairset with the fault condition is turned off.

FALSE: both pairsets are turned off if there is a fault on one pairset.

# option\_vport\_lim \_pri

This optional variable indicates if  $V_{PSE}$  on the primary alternative is out of the operating range during normal operating state.

### Values:

FALSE:  $V_{PSE}$  on the primary alternative is within the  $V_{Port\_PSE-2P}$  operating range as defined in Table 33–17.

TRUE:  $V_{PSE}$  on the primary alternative is outside of the  $V_{Port\_PSE-2P}$  operating range as defined in Table 33–17.

### option vport lim sec

This optional variable indicates if  $V_{\text{PSE}}$  on the secondary alternative is out of the operating range during normal operating state.

### Values:

FALSE:  $V_{PSE}$  on the secondary alternative is within the  $V_{Port\_PSE-2P}$  operating range as defined in Table 33–17.

TRUE:  $V_{PSE}$  on the secondary alternative is outside of the  $V_{Port\_PSE-2P}$  operating range as defined in Table 33–17.

#### class 4PID mult events sec

A variable indicating if the PSE uses the method consisting in generating 3 class events on the secondary alternate to determine if the dual signature PD is a candidate for 4-pair power.

TRUE: the PSE generates at least 3 class events to determine if the PD is a candidate for 4-pair power.

FALSE: the PSE does not need to generate 3 class events to determine if the PD is a candidate for 4-pair power.

### mps\_sum

A variable indicating if the PSE uses the method consisting in measuring the sum of  $I_{PORT-2P}$  of both pairsets to determine if the DC MPS component is present.

TRUE: MPS presence determination is based on the sum of current of both pairsets FALSE: MPS presence determination is based on I<sub>PORT-2P</sub> of the pairset having the highest current.

## highest\_2p

A variable indicating which of the 2 pairsets has the highest current.

pri: the primary alternative has the highest current.

sec: the secondary alternative has the highest current.