

P802.3bt Motions

Norfolk, VA

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Chad Jones

Motion 6

Move that 802.3bt specify the Channel Pair to Pair resistance unbalance for operating 4P systems

Move by Yair Darshan
2nd Christian Beia

Y: 34

N: 0

A: 8

Motion 7

Motion to add the following text to clause 33 after 33.1.4.2.

33.1.4.3 4 Pair Operation Channel Requirement for Pair to Pair Resistance unbalance

4 pair operation requires the specification of resistance unbalance between each two pairs of the channel, not greater than 200 milliOhms or 6%(TBD) whichever is greater. Resistance unbalance between the channel pairs is a measure of the difference of resistance of the common mode pairs of conductors used for power delivery. Channel pair to pair resistance unbalance is defined by equation 33-1.1:

$$\left\{ \frac{(R_{ch_max} - R_{ch_min})}{(R_{ch_max} + R_{ch_min})} \times 100 \right\} \% \quad 33-1.1$$

Where:

Rch_max is the sum of channel pair elements with highest common mode resistance.

Rch_min is the sum of channel pair elements with lowest common mode resistance

Common mode resistance is the resistance of the two wires in a pair (including connectors), connected in parallel.

Move by Yair Darshan

2nd Christian Beia

Y: 33

N: 1

A: 7

Motion 8

Move to adopt slides 4 to 6 in yseboodt_02_0514.pdf as baseline text IHOLD , Annex 33B and CPORT for Type 3 (in the note in 33.3.8) are TBD.

Move by: L. Yseboodt

Second: F. Ahmad

Y: 19

N: 0

A: 15

Straw Poll 1

- IEEE 802.3BT should provide a means to identify a PD capable of receiving power simultaneously on both Alternatives.
- Y: 23
- N: 0
- A: 8

Straw Poll 2

I support a LLDP extension that provides a 4-pair power identification (4P-ID).

Do you agree with the above statement for .3bt?

- Y: 19
- N: 2
- A: 11

Straw Poll 3

I support a modification of clause 33.2.3 in order to define a fixed voltage polarity at the PSE PI for PSEs capable of sourcing more than 30W

- Y: 18
- N: 1
- A: 13

Straw Poll 4

I support a modification of clause 33.2.3 in order to define a fixed voltage polarity at the PSE PI for PSEs capable of sourcing more than 60W

- Y: 14
- N: 0
- A: 19

Straw Poll 5

I support a modification of clause 33.3.1 in order to define a fixed voltage polarity at the PD PI for PDs capable of drawing more than 51W

- Y: 1
- N: 14
- A: 14

Motion 9

Move to accept text in slide 7 of Abramson_01a_0514.pdf as IEEE802.3bt text for section 33.1.1, excluding the note regarding Type 4

- Mover: David Abramson
- Seconder: Yair Darshan
- Y: 30
- N: 0
- A: 7

Motion 10

Move to accept text in slide 8 of
Abramson_01a_0514.pdf IEEE802.3bt as text for section
33.1.4, removing “and Type 4” from the last sentence

- Mover: David Abramson
- Seconder: Matthias Wendt
- Y: 28
- N: 0
- A: 5

Motion 11

Move to accept text in slide 9 of
Abramson_01a_0514.pdf as IEEE802.3bt text for section
33.1.4.1, excluding the note regarding Type 4

- Mover: David Abramson
- Seconder: Yair Darshan
- Y: 31
- N: 0
- A: 5

Motion 12

Motion to accept text from slide 11 of
Abramson_01a_0514.pdf as IEEE802.3bt text for
Section 33.2.3

- Mover: David Abramson
- Seconder: Yair Darshan
- Y: 26
- N: 0
- A: 5

Motion 13

Motion to accept text from slide 12 of
Abramson_01a_0514.pdf as IEEE802.3bt text for
Section 33.3.1

- Mover: David Abramson
- Seconder: Yair Darshan
- Y: 29
- N: 0
- A: 4

Straw Poll 6

Add to 33.2.5:

A PSE capable of powering both alternatives shall only power both Alternatives simultaneously for PDs identifying themselves of being capable of accepting power on both Alternatives simultaneously.

Do you agree with the above statement for .3bt?

- Y: 6
- N: 14
- A: 11

Straw Poll 7

The task force should form an adhoc to attempt to find a L1 method or examining existing Clause 33 PD and PSE specifications for legacy Type1 and Type2 PDs to enable 4P power.

- Y: 19
- N: 3
- A: 9

Straw Poll 8

The task force should form an adhoc to collect use cases for P802.3bt.

- Y: 20
- N: 0
- A: 11