



Update Figure 33-14

IEEE802.3bt

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Objectives

- Objective
 - Update Figure 33-14 to capture PSE types 1-4 preferably in single drawing

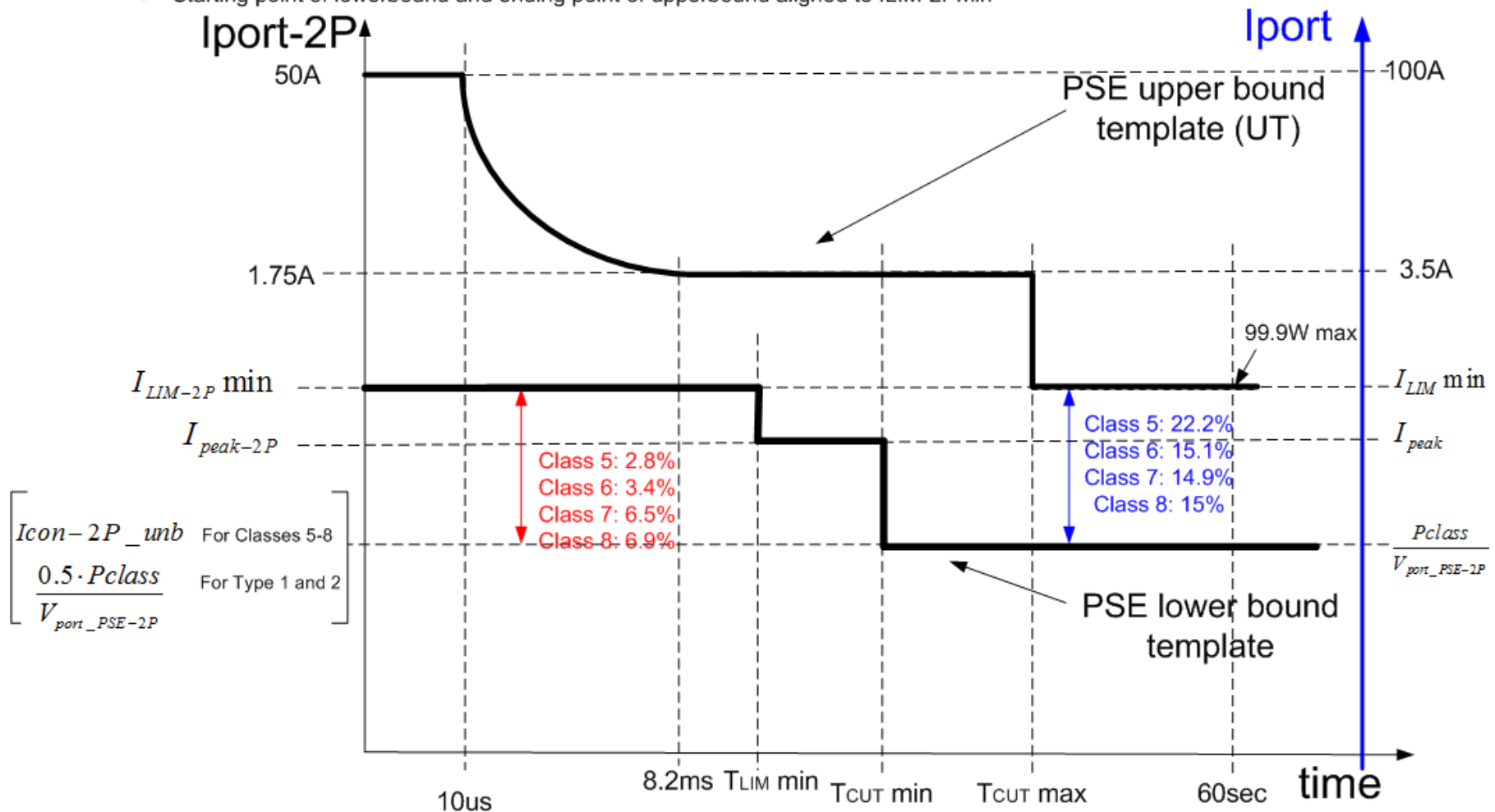
Working Assumptions

- We need to specify the requirements for per pairset and the total current of bot pairsets for design flexibility
 - When total current is observed, it allows cancelation of E2EP2PRunb effect.
- To show that upperbound template after 60sec must be limited to 99.9W
- The following are two options for figure 33-14 that is presented for discussion

Option 1

Option 1:

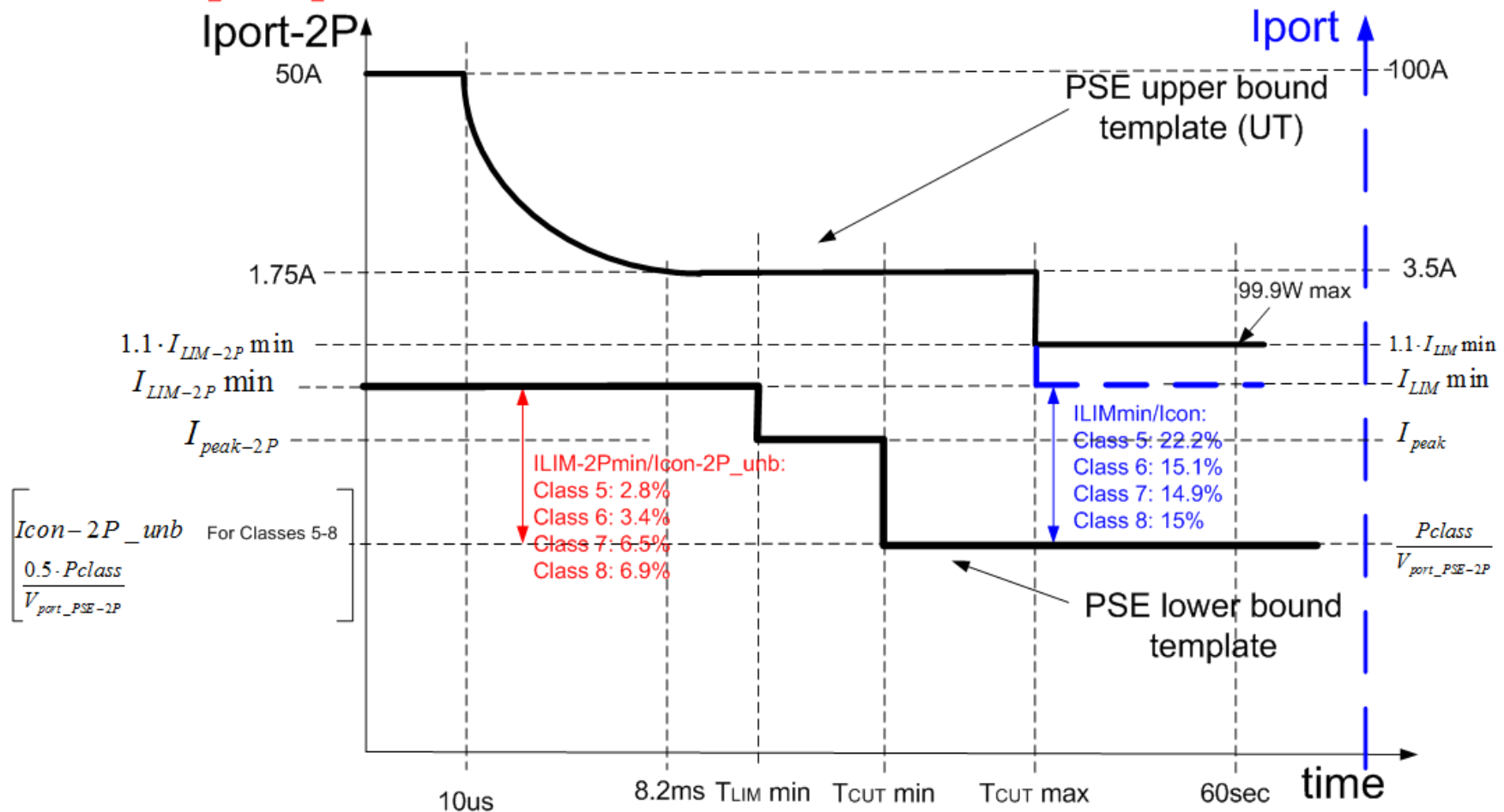
- Single drawing covers all PSE types
- Covers pairset current and total current on different Y axis.
- Starting point of lowerbound and ending point of upperbound aligned to ILIM-2Pmin



Option 2

Option 2:

- Single drawing covers all PSE types
- Covers pairset current and total current on different Y axis.
- Starting point of lowerbound (ILIM-2Pmin) and ending point of upperbound has 10% maximum margin on the aligned to ILIM-2Pmin.
- The assumption is that this will not create permission to supply $>ILIM_MIN \cdot VPSE > 100W$ for ever due to the fact that PSE has to police 100W max after T_{cut_max}/T_{lim_max} .



Discussion

Thank You