System Class

$(P_{PSE}-P_{PD})/P_{PSE}$ 20%	I (12V CC)	I (12V)	II (12V)	II (24V CC)	III (24V)	III (48V CC)	IV (48V)	V (48V)	VI (Open)
$V_{PSE(max)}$ (V)[1]	18	18	18	28	28	56	56	56	-
$V_{PSE(min)}$ (V)[1]	5.3	14	14	12	21.6	36	43.2	43.2	-
R_{PSE} (Ω)	4.0	4.0	1.0	4.0	1.0	4.0	4.0	1.0	-
$I_{PI(max)}$ (A)	0.09	0.24	0.36	0.21	0.55	0.62	0.74	1.11	-
$R_{Loop(max)}$ (Ω)[2]	6.5	6.5	6.5	6.5	6.5	6.5	6.5	6.5	-
Vhyst(max)	1.0	2.5	2.7	2.2	4.1	6.5	7.8	8.3	
$V_{PD(min)}$	4.709	12.44	11.67	10.66	18.01	31.99	38.38	36.01	-
P_{PSE} (W)[3]	0.482	3.361	5.016	2.47	11.94	22.23	32	47.76	-
P_{PD} (W)[4]	0.428	2.987	4.181	2.194	9.954	19.75	28.44	39.81	-

[1]V_{PSE} is the voltage measured at the PSE PI over all operating conditions.

[2]R_{Loop} is the round trip link segment resistance.

[3]P_{PSE} is the maximum power the PSE is required to source at the PI.

[4]P_{PD} is the power available at the PD PI.

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