



IEEE802.3bu 1PODL Task Force
Table 200-1: Flexible Design guide lines for PD available power
Rev 001a

May 2015
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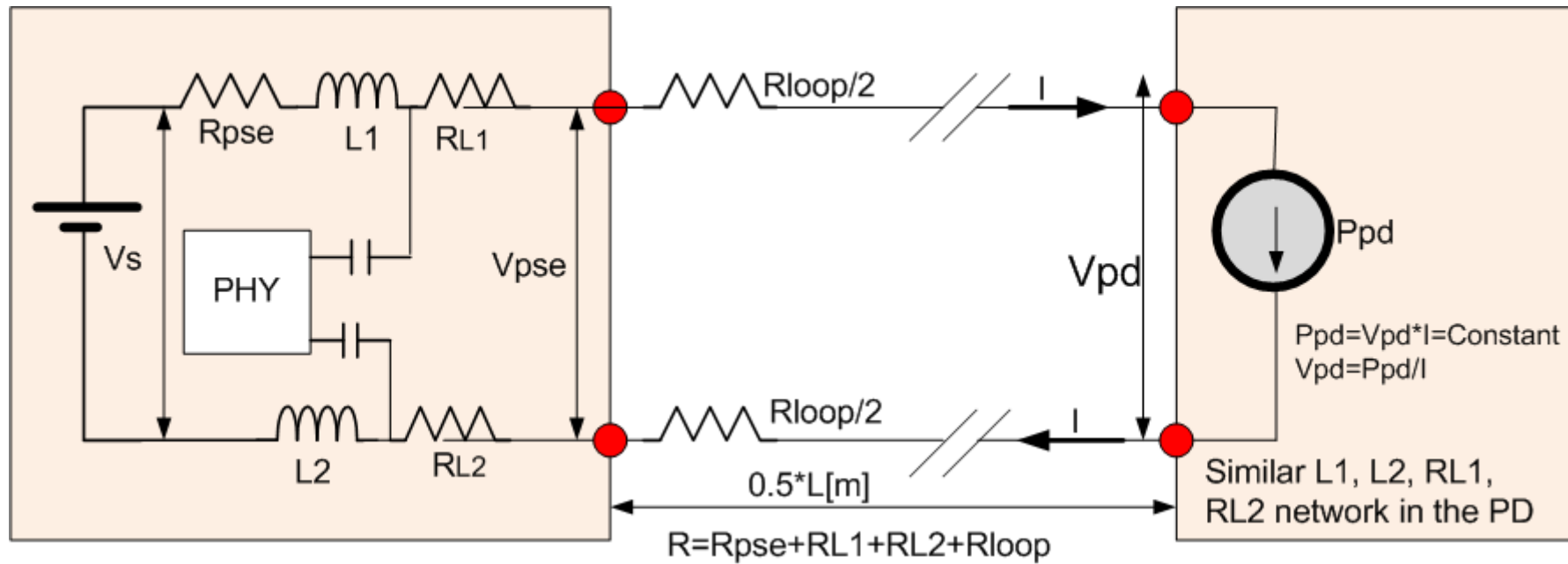
Objectives

- To complete the proposed remedy for Comment #51 in D0.2 which is: to supply design guide lines by form of equation for addressing use case that are not specified in Table 200-1.
- Background material:
 - http://www.ieee802.org/3/bu/public/sep14/darshan_3bu_1_0914.pdf
 - http://www.ieee802.org/3/bu/public/jan14/darshan_3bu_01_0114.pdf
- See revision history details AT Annex E.

Background

- The automotive OEMs use all different gauge cables among themselves and for different use cases.
- As a result, this presentation will address the general case for setting the requirements for wire resistance per meter as function of system parameters.

Simplified system model



Wire resistance per meter as function of system parameters

$$\rho(\Omega / m) = \frac{1}{L} \cdot \left(\frac{K \cdot V_s^2}{P_{pd_{\max}}} - R_s \right)$$

$$K = \frac{P_{pd_{\max}}}{P_{pd_{P_{\max}}}} = \alpha \cdot (1 - \alpha)$$

$$\alpha = \frac{P_{pd_{\max}}}{P_s}, \quad 0.5 < \alpha < 1$$

- Model and calculation example: See annex A.
- Equation derivation: See Annex B.

- α typical cost effective range is 0.7 to 0.85.
- R_s =PSE internal output resistance
- $P_{pd_{\max}}$ =Actual PD input power measured at the PI at stable operating region.
- $P_{pd_{P_{\max}}}$
- V_s =PSE open load voltage at the PI. $V_s=V_{pse}$ at no load.
- P_s is PSE power supply output power.
- $L[m]$ =Round loop of Wire length. E.g if channel length is 15m than $L=30m$.

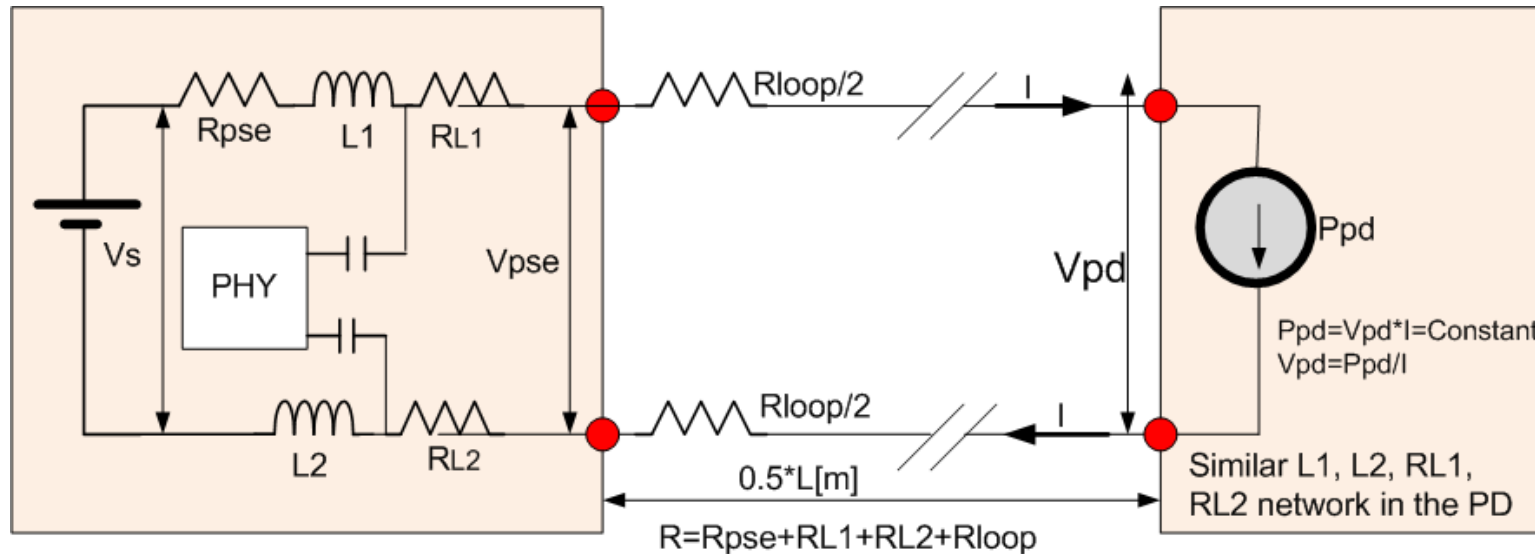
Thank You

See detailed analysis in next slides



Backup slides

Annex A1: Detailed analysis.



System Equation

$$\frac{V_s - V_{pd}}{R} = I = \frac{P_{pd}}{V_{pd}} = \text{constant}$$

Solving for Vpd:

$$V_{pd} = 0.5 \cdot \left(V_s \pm \sqrt{V_s^2 - 4 \cdot R \cdot P_{pd}} \right)$$

Working only with the positive solution of the quadratic equation for stable operation and higher power efficiency:

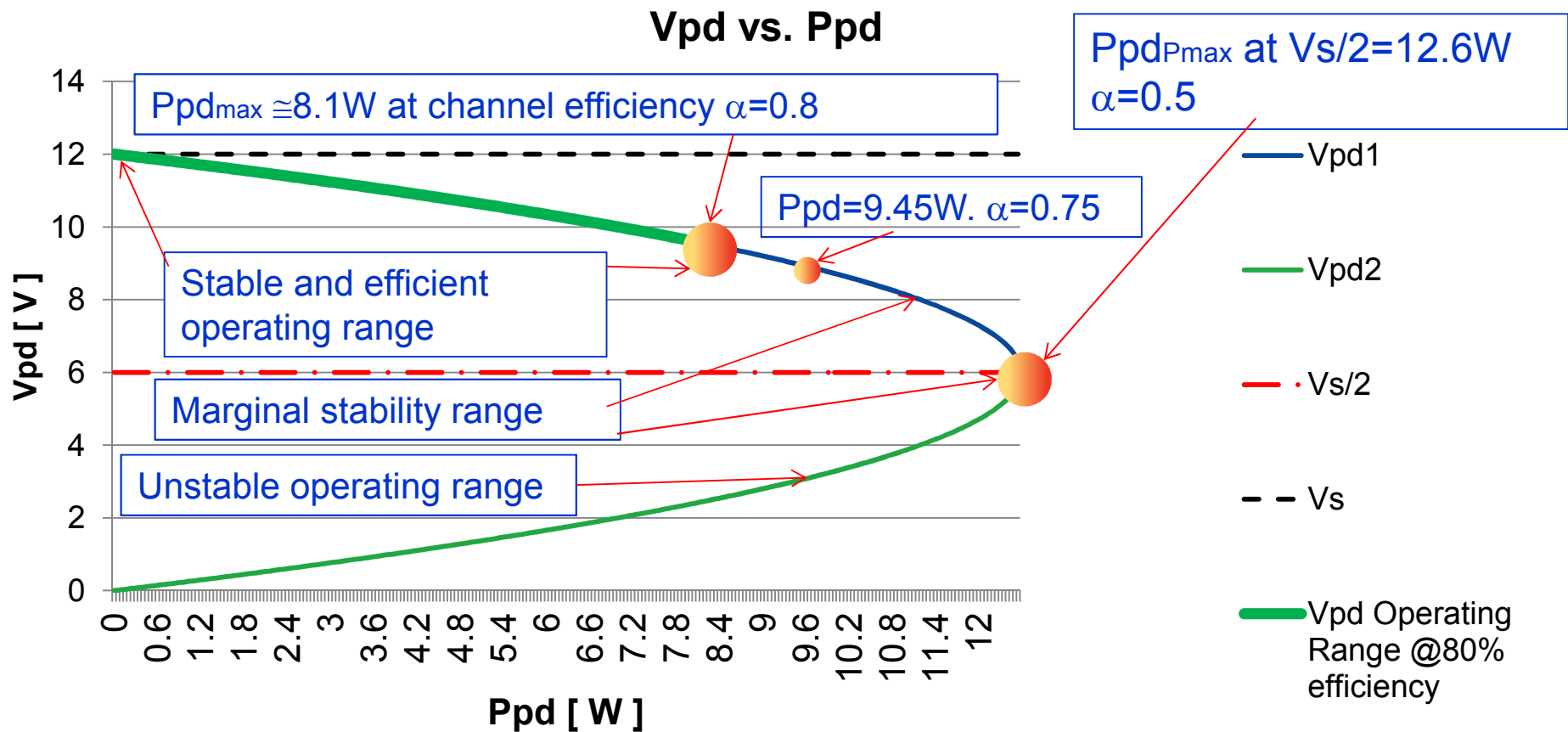
$$V_{pd} = 0.5 \cdot \left(V_s + \sqrt{V_s^2 - 4 \cdot R \cdot P_{pd}} \right)$$

$$I = 0.5 \cdot \left(V_s - \sqrt{V_s^2 - 4 \cdot R \cdot P_{pd}} \right) / R$$

For stable operation: $P_{pd} < P_{pd_{max}} = \frac{V_s^2}{4 \cdot R}$, $P_{pd} = \alpha \cdot P_s$ $\alpha=0.8$ is cost effective choice.

Annex A2: Detailed analysis.

- Example with $V_s=12V$. $R_s=0.4\Omega$,
- Wire size: AWG24 ($d=0.511mm$, $0.205mm^2$, $0.082\Omega/m$).



Annex B1: Detailed mathematical analysis.

From Annex 1, Vpd solution at worst case operating range is at Vs_min:

$$V_{pd} = \frac{V_{s_{\min}} + \sqrt{V_{s_{\min}}^2 - 4 \cdot R \cdot P_{pd}}}{2} \quad \text{Eq-1}$$

Where: $R = R_s + R_{loop}$ Eq-2

For stable operation: $V_s^2 - 4 \cdot R \cdot P_{pd} \geq 0$

Resulting with Ppdmax for stable operation:

$$P_{pd_{\max}} < \frac{V_{s_{\min}}^2}{4 \cdot (R_s + R_{loop})} = P_{pd_{P_{\max}}} \quad \text{Eq-3}$$

$P_{pd_{\max}}$ =Actual maximum PD power at stable operating range.

$P_{pd_{P_{\max}}}$ =Maximum possible theoretical PD input power.

At $P_{pd_{P_{\max}}}$ which happens at $V_{pd} = V_s/2$, we get channel power loss, $P_c = P_{pd}$ which is equivalent to a channel power efficiency of $\alpha = P_{pd}/P_s = 50\%$. To be in stable operating region, we need to work at $\alpha > 50\%$.

$$\alpha = \frac{P_{pd_{\max}}}{P_s} > 0.5 \quad \text{Eq-4}$$

See curve in Annex A2 for the reason why α need to be > 0.5 for stable operation.

P_s is the PSE power supply output power. $\alpha = 1$ only if $R = 0$. Since $R > 0$, $\alpha < 1$


$$\text{Therefore: } \alpha = \frac{P_{pd_{\max}}}{P_s} \quad 0.5 < \alpha < 1 \quad \text{Eq-5}$$

Annex B3: Detailed mathematical analysis.

From Eq-4:

$$Ppd_{\max} = \alpha \cdot Ps$$

$$Vpd \cdot I = \alpha \cdot Vs_{\min} \cdot I$$

$$I = Ppd_{\max} / Vpd$$


$$Vpd = \alpha \cdot Vs_{\min} \quad \text{Eq-6}$$

$$I = \frac{Ppd_{\max}}{\alpha \cdot Vs_{\min}} \quad \text{Eq-7}$$

$$Ploss = R \cdot I^2 = Ps - Ppd_{\max} =$$

$$R \cdot I^2 = Vs \cdot I - Vpd \cdot I =$$

$$R \cdot I = Vs - Vpd \quad \text{Eq-8}$$

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Combining Eq-6 and Eq-7 in Eq -8

$$R \cdot \frac{Ppd_{\max}}{\alpha \cdot Vs} = Vs - \alpha \cdot Vs$$

$$Ppd_{\max} = \frac{\alpha \cdot (1 - \alpha) \cdot Vs^2}{R} = \frac{\alpha \cdot (1 - \alpha) \cdot Vs^2}{Rs + Rloop} = \frac{K \cdot Vs^2}{Rs + Rloop}$$

$$\rho(\Omega / m) = \frac{Rloop}{L} = \frac{1}{L} \cdot \left(\frac{K \cdot Vs_{\min}^2}{Ppd_{\max}} - Rs \right) \quad \begin{array}{l} K = \alpha \cdot (1 - \alpha) \\ 0.5 < \alpha < 1 \end{array}$$

Annex E: Revision History

Rev	Updates
000	-
001	-Converting Epse to Vs for using conventional voltage notation -Clarifying the definition of K.