

Bill & Ed's D1.5 Action items - **DRAFT**

Bill Powell - Nokia

Receive Sensitivity - 141.7.10,11

- ❑ D1.5, p.75, L48, 52
- ❑ Current text:

141.7.10 Receive sensitivity

{TBD}

141.7.11 Stressed receiver conformance test

{TBD}

- ❑ From 802.3av (10G EPON):

75.7.11 Receive sensitivity

Receiver sensitivity is defined for the random pattern test frame, or test pattern 1, or test pattern 3, and an ideal input signal quality with the specified extinction ratio. The measurement procedure is described in 58.7.10 for 1 Gb/s PHYs and 52.9.8 for 10 Gb/s PHYs. The sensitivity shall be met for the bit error ratio defined in Table 75-6, Table 75-7, or Table 75-11 as appropriate.

75.7.12 Stressed receiver conformance test

Compliance with stressed receiver sensitivity is mandatory for the following PMDs: 10GBASE-PR-D1, 10GBASE-PR-D2, 10GBASE-PR-D3, 10GBASE-PR-U1, 10GBASE-PR-U3, 10/1GBASE-PRX-D3, 10/1GBASE-PRX-U1, 10/1GBASE-PRX-U2, and 10/1GBASE-PRX-U3. The stressed receiver conformance test is intended to screen against receivers with poor frequency response or timing characteristics that could cause errors when combined with a distorted but compliant signal. To be compliant with stressed receiver sensitivity, the receiver shall meet the specified bit error ratio at the power level and signal quality defined in Table 75-6, Table 75-7, or Table 75-11 as appropriate, according to the measurement procedures of 58.7.11 for 1 Gb/s PHYs and 52.9.9 for 10 Gb/s PHYs.

Receive Sensitivity - 141.7.10

For 25G - Reference
100GBASE-LR4/ER4
25G Rx sensitivity
meas. specs (802.3ca)

□ Proposed text:

4.7.10 Receive sensitivity

Receiver sensitivity is defined for the random pattern test frame, or test pattern 1, or test pattern 3, and an ideal input signal quality with the specified extinction ratio. The measurement procedure is described in 52.9.8 for 10 Gb/s PHYs and TBD2 for 25 Gb/s PHYs. The sensitivity shall be met for the bit error ratio defined in Table TBD3, Table TBD4, or Table TBD5 as appropriate.

List all .3ca
PHYs here

□ 4.7.11 Stressed receiver conformance test

Compliance with stressed receiver sensitivity is mandatory for the following PMDs: 10GBASE-PR-D1, 10GBASE-PR-D2, 10GBASE-PR-D3, 10GBASE-PR-U1, 10GBASE-PR-U3, 10/1GBASE-PRX-D3, 10/1GBASE-PRX-U1, 10/1GBASE-PRX-U2, and 10/1GBASE-PRX-U3.

The stressed receiver conformance test is intended to screen against receivers with poor frequency response or timing characteristics that could cause errors when combined with a distorted but compliant signal. To be compliant with stressed receiver sensitivity, the receiver shall meet the specified bit error ratio at the power level and signal quality defined in Table TBD1, Table TBD2, or Table TBD3 as appropriate, according to the measurement procedures of 52.9.9 for 10 Gb/s PHYs and TBD4 for 25 Gb/s PHYs.

10G .3ca - use new
tables due to 1E-2
BER

100GBASE-LR4/ER4 25G Rx sensitivity (for reference)

88.8.9 Receiver sensitivity

Receiver sensitivity, which is defined for an ideal input signal, is informative and compliance is not required. If measured, the test signal should have negligible impairments such as intersymbol interference (ISI), rise/fall times, jitter and RIN. Instead, the normative requirement for receivers is stressed receiver sensitivity.

88.8.10 Stressed receiver sensitivity

Stressed receiver sensitivity shall be within the limits given in Table 88–8 if measured using the method defined in 87.8.11 with the following exceptions:

- a) Added sinusoidal jitter is as specified in Table 88–13.
- b) The stressed eye J2 Jitter, stressed eye J9 Jitter, and vertical eye closure penalty are as given in Table 88–8.
- c) The test pattern is as given in Table 88–11.
- d) The reference receiver used to verify the conformance test signal is required to have the bandwidth given in 88.8.8.

Table 88–13—Applied sinusoidal jitter

Frequency range	Sinusoidal jitter, peak-to-peak (UI)
$f < 100 \text{ kHz}$	Not specified
$100 \text{ kHz} < f \leq 10 \text{ MHz}$	$5 \times 10^5 / f$
$10 \text{ MHz} < f < 10 \text{ LB}^a$	0.05

^a*LB* = loop bandwidth; upper frequency bound for added sine jitter should be at least 10 times the loop bandwidth of the receiver being tested.

Laser Timing Parameters - 141.7.13

- ❑ D1.5, p.76, L13,15
- ❑ Glen's 14 Feb 2019 proposed changes:

Here are updates/thoughts on action items 12-15:

12	Laser timing parameters	141.7.13	55	29	Ed H. Bill P.	Bill still looking
13	Receiver settling time measurements	141.7.14.1	56	49,40	Ed H. Bill P.	Bill still looking
14	Figure 141-3, timing parameters	141.7.13.2	56	41	Glen	Finished, comment submission pending
15	Receiver settling time measurements	141.7.14.2	58	37	Ed H. Bill P.	Bill still looking

Action item #12 is related to TBDs in this text:

141.7.13 Laser on/off timing measurement

T_{on} is defined in 141.7.13.1 and has the value of less than or equal to 128 ns (defined in Table 141-17 and Table 141-18).

A method for measuring $T_{receiver_settling}$ is illustrated in 141.7.13.2 (informative) and has a value of less than **{TBD}** ns (defined in Table 141-15 and Table 141-16).

T_{CDR} is defined in **{TBD, Clause 142}** and has the value of less than **{TBD}** ns.

T_{off} is defined in 141.7.13.1 and has the value of less than or equal to 128 ns (defined in Table 141-17 and Table 141-18).

First, paragraphs 2 and 3 are out of place here. Only paragraphs 1 and 4 should remain.

The paragraph 2 should be moved to section 141.7.14 (insert between 141.7.14 and 141.7.14.1 headers). Replace the TBD in this paragraph with 800 ns (based on the contribution from Curtis and Frank that we reviewed today.) I already made this change in Action Item #14 (see below).

Paragraph 3 should be removed from this location.

Bill and Ed need to figure out what to do with the T_{cdr} definition. Clause 142 talks about what ONU transmits during T_{cdr} time, but never provides any definition of it or measurement methodology. This is the only remaining gap for AI #12. If we need to add an illustration of T_{cdr} measurement to Figure 141-3, let me know.

Section
141.7.14
(TBD=800ns)

Move to ?

Laser Timing Parameters - 141.7.13

- ❑ Final proposed 141.7.13 text:

141.7.13 Laser on/off timing measurement

Ton is defined in 141.7.13.1 and has the value of less than or equal to 128 ns (defined in Table 141–17 and Table 141–18).

~~A method for measuring Treceiver_settling is illustrated in 141.7.13.2 (informative) and has a value of less than {TBD} ns (defined in Table 141–15 and Table 141–16).~~

~~TCDR is defined in {TBD, Clause 142} and has the value of less than {TBD} ns.~~

Toff is defined in 141.7.13.1 and has the value of less than or equal to 128 ns (defined in Table 141–17 and Table 141–18).

Glen and Bill will work on where and what to include for T_{CDR} between now and the comment deadline/meeting

Receiver settling time meas. - 141.7.14.1

- ❑ D1.5, p.77, L39,40
- ❑ Glen's 14 Feb 2019 proposed changes:

12	Laser timing parameters	141.7.13	55	29	Ed H. Bill P.	Bill still looking
13	Receiver settling time measurements	141.7.14.1	56	49,40	Ed H. Bill P.	Bill still looking
14	Figure 141-3, timing parameters	141.7.13.2	56	41	Glen	Finished, comment submission pending
15	Receiver settling time measurements	141.7.14.2	58	37	Ed H. Bill P.	Bill still looking

Action Item #13 is related to this text:

141.7.14.1 Definitions

$T_{\text{receiver_settling}}$ is denoted as the elapsed time beginning from the moment that the optical power in the receiver at TP7 reaches the conditions specified in 141.7.11 and ending at the moment that the electrical signal after the PMD at TP8[i] reaches within 15 % of its steady state average power, jitter (see **TBD**). $T_{\text{receiver_settling}}$ is presented in **Figure <TBD>**. The data transmitted may be any valid 256B/257B symbols (or a specific power synchronization sequence). The optical signal at TP7, at the beginning of the locking, may have any valid 256B/257B pattern, optical power level, jitter, or frequency shift matching the standard specifications.

The "Figure <TBD>" is resolved by action item #14 (see below). The only remaining gap here is to fill the jitter reference "... jitter (see **TBD**).". I believe this simply should point to the OLT receiver tables 141-15/16 with Stressed Jitter parameters that Curtis and Frank presented on the call today. I already made this change in Action Item #14 (see below). If you agree with this change, we can close this action item.

- ❑ 141.7.14 Receiver settling time measurement (add text below to 141.7.14)
A method for measuring $T_{\text{receiver_settling}}$ is illustrated in 141.7.13.2 (informative) and has a value of less than **TBD** 800 ns (defined in Table 141–15 and Table 141–16).
- ❑ 141.7.14.1 Definitions
"Figure <TBD>" should be replaced by "Figure 141-3"
"...steady state average power, jitter (see **TBD**)" should be replaced by
"...steady state average power and jitter (see Tables 141-15 and 141-16).
[Note, these tables include stressed RX sensitivity and stressed eye jitter]

Example - 802.3av Receiver settling time meas.

- 10G EPON (802.3av):

75.7.15 Receiver settling timing measurement

75.7.15.1 Definitions

Denote $T_{\text{receiver_settling}}$ as the time beginning from the time that the optical power in the receiver at TP7 reaches the conditions specified in 75.7.12 and ending at the time that the electrical signal after the PMD at TP8 reaches within 15% of its steady state parameter (average power, jitter) (see Table 75-6 for 10GBASE-PR-D1, 10GBASE-PR-D2, and 10GBASE-PR-D3, and Table 75-7 for 10/1GBASE-PRX-D1, 10/1GBASE-PRX-D2, and 10/1GBASE-PRX-D3). $T_{\text{receiver_settling}}$ is presented in Figure 75-9. The data transmitted may be any valid 64B/66B symbols (or a specific power synchronization sequence). The optical signal at TP7, at the beginning of the locking, may have any valid 64B/66B pattern, optical power level, jitter, or frequency shift matching the standard specifications.

- Tables 75-6 & 75-7 are .3av OLT PMD RX characteristics & include stressed RX sensitivity and stressed eye jitter

Receiver settling time meas. - 141.7.14.2

- ❑ D1.5, p.79, L37
- ❑ Glen's 14 Feb 2019 proposed changes:

12	Laser timing parameters	141.7.13	55	29	Ed H. Bill P.	Bill still looking
13	Receiver settling time measurements	141.7.14.1	56	49,40	Ed H. Bill P.	Bill still looking
14	Figure 141-3, timing parameters	141.7.13.2	56	41	Glen	Finished, comment submission pending
15	Receiver settling time measurements	141.7.14.2	58	37	Ed H. Bill P.	Bill still looking

Action item #15 is related to this text:

Figure 141–5 illustrates the test setup for measuring the OLT PMD receiver (upstream) $T_{\text{receiver settling time}}$. The optical PMD transmitter has well-known parameters, with a fixed known T_{on} time. After T_{on} time the parameters of the reference transmitter, at TP6 and therefore at TP7, reach within 15% of its steady state values as specified in **[TBD]**.

This TBD should simply reference the ONU Transmit Characteristics tables 141-17 and 141-18.

- ❑ We agree

PMA control register for C45 - 142.4.1

- ❑ D1.5, p.114, L50
- ❑ Current text

142.4.1 Differential Encoder

Differential encoding as shown in Figure 142–18 shall be implemented in the OLT TX PMA for downstream. Differential encoding is optional to use by setting the control bit in the register, as defined in **<Clause 45>**.

Editor's Note (to be removed prior to publication): AI for Bill to come up with a proposal for an associated Clause 45 register to control the encoder function.

- ❑ Proposed changes

Bill still working to link this to a Clause 45 Differential Encoding register