

# MLID assignment

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- MLID adopted in Berlin meeting (Motion #3)
  - Value to be assigned during registration
  - Details of value assignment TBD
  
- This presentation suggests a mechanism for MLID value assignment.

100G-EPON REGISTER	Octets
Destination Address	6
Source Address	6
Length/Type = 0x8808	2
Opcode = 0x0015	2
Timestamp	4
Assigned Port (PLID)	2
Assigned Port (MLID)	2
Flags	1
Sync Time	2
Echo Pending grants	1
Target Laser On Time	1
Target Laser Off Time	1
Pad/Reserved	30
FCS	4
<b>TOTAL</b>	<b>64</b>

## □ Extend REGISTER message

- explicit indication of the MLID assigned to the new ONU being registered (new field in red)

100G-EPON REGISTER_ACK	Octets
Destination Address	6
Source Address	6
Length/Type = 0x8808	2
Opcode = 0x0015	2
Timestamp	4
Flags	1
Echo Assigned Port (PLID)	2
Echo Assigned Port (MLD)	2
Echo Sync Time	2
Pad/Reserved	33
FCS	4
<b>TOTAL</b>	<b>64</b>

- ❑ Extend REGISTER ACK message
  - ONU would echo assigned value in existing REGISTER ACK message (in red)

- ❑ Advantages:
  - Allows any 16-bit value to be used for MLID
  - No additional overhead

# Thank You

Move to adopt MLD value assignment mechanism proposed in remein\_3ac\_1\_1117.pdf slide 3 & 4.

Moved: Duane Remein

Second: Glen Kramer

For: \_\_\_\_\_

Against: \_\_\_\_\_

Abstain: \_\_\_\_\_

Technical ( 75%)

Motion Passes/Fails