

Cl 1 SC 1.4.245a P20 L 33 # 391
Kramer, Glen Broadcom

Comment Type TR Comment Status D

We provide definition for EQ and also list the "EQ" under the abbreviations, showing the expanded name as "envelope quantum". But there is no definition for the "envelope quantum". Additionally, the definition of EQ is incorrect. EQ is not always 72 bits. In MPCP and above, EQ is 64 bits. In PCS, after 64B/66B encoding, an EQ is 66 bits.

SuggestedRemedy

Use the following definition of EQ:

1.4.245a Envelope Quantum: The unit of measurement of volume of information. Each envelope quantum represents 64 bits of data plus the layer-specific encoding. Thus, at the MAC sublayer and above, an envelope quantum is equal to 64 bits. Within the MCRS, an envelope quantum contains 72 bits (i.e., 64 bits of data and 8 bits of control). Within PCS, after the 64B/66B encoding, an envelope quantum contains 66 bits.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Added context as to where EQ is applicable to

1.4.245a Envelope Quantum: In Nx25G-EPON, the unit of measurement of volume of information. Each envelope quantum represents 64 bits of data plus the layer-specific encoding. Thus, at the MAC sublayer and above, an envelope quantum is equal to 64 bits. Within the MCRS, an envelope quantum contains 72 bits (i.e., 64 bits of data and 8 bits of control). Within PCS, after the 64B/66B encoding, an envelope quantum contains 66 bits.

Cl 1 SC 1.4.278 P19 L 26 # 409
Kramer, Glen Broadcom

Comment Type T Comment Status D

The definition of Grant provides specific details for C144, but is silent on similar details in C64 and C77

SuggestedRemedy

Change the definition to the following:

"1.4.278 Grant: Within P2MP protocols, a permission to transmit at a specific time, for a specific duration. Grants are issued by the OLT (master) to ONUs (slaves) by means of GATE messages. <u>In Clause 64 and Clause 77, a GATE MPCPDU contain one or multiple grants issued to a single LLID. Each grant results in one or multiple upstream bursts transmitted by the ONU. In Clause 144, a grant includes envelope allocations for multiple LLIDs. The OLT conveys a grant to the ONU using one or multiple GATE MPCPDUs, all having the same StartTime values. There is a one-to-one correspondence between the grants issued to an ONU and upstream bursts transmitted by that ONU, i.e., a grant issued to an ONU results in a single upstream burst transmitted by that ONU.</u>"

<u>...</u> - underline

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 30 SC 30 P31 L 1 # 322
Laubach, Mark Broadcom

Comment Type TR Comment Status D

Clause 30 changes to add to the draft

SuggestedRemedy

Insert new Clause 30 changes as per laubach_3ca_5_0119.pdf

Proposed Response Response Status W

PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 31A SC 31A P23 L15 # 335
 Powell, William Nokia
 Comment Type E Comment Status D SYNC_PATTERN_def
 Used by OLT to announce elements of the FEC-unprotected area (SP) to all ONUs on the given PON
 SuggestedRemedy
 Sync pattern used by the OLT to indicate the start of elements of the sync area to all ONUs on the given PON.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #402

Cl 31A SC 31A P23 L15 # 402
 Kramer, Glen Broadcom
 Comment Type T Comment Status D SYNC_PATTERN_def
 Description of SYNC_PATTERN in Table 31A-1 is inaccurate. SYNC_PATTERN cannot be sent to "all" ONUs on the given PON. It can be sent to all unregistered ONUs, or all registered ONUs, or several registered ONUs, or individual registered ONUs. Also, it does not announce all the elements of FEC-unprotected area (i.e., EBD).
 SuggestedRemedy
 Use the following description:
 "Announces burst synchronization patterns to all unregistered ONUs, multiple/all registered ONUs, or individual registered ONUs."
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 31A SC 31A P23 L18 # 295
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 CCP frames not listed in Table 31A-1.
 SuggestedRemedy
 Change reserved to be 00-19 through 00-1f. Add row for 00-20, CC_REQUEST, 144.4.2.1, Query or change the state of ONU channel(s), No. Add row for 00-21, CC_RESPONSE, 144.4.2.2, Report current channel(s) state and action result code, No. Add row for reserved 00-22 through 01-00.
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 45 SC 45.2.1 P24 L5 # 417
 Remein, Duane Huawei
 Comment Type TR Comment Status D C45
 Proposed material for Clause 45 PMA/PMD registers.
 SuggestedRemedy
 See remain_3ca_1_1901.pdf
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 45 SC 45.2.1.93a P24 L32 # 365
 Powell, William Nokia
 Comment Type ER Comment Status D C45
 writes ignored
 SuggestedRemedy
 write operations are ignored
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #417

Cl 45 SC 45.2.1.93a P24 L39 # 366
 Powell, William Nokia
 Comment Type ER Comment Status D C45
 Read only
 SuggestedRemedy
 Read-Only
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #417

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 141 SC 141.3.1.1 P40 L48 # 416
 Remein, Duane Huawei

Comment Type TR Comment Status D

Delay constraints. Comment 434 against Draft 1.2 suggested a mechanism to allocate delay constraints for Nx25G-EPON but there were objections to that proposal. This item has not been addressed yet and needs to be resolved in order for the draft to be technically complete and proceed to WG Ballot.

SuggestedRemedy

Work out a solution during the March meeting.

Proposed Response Response Status W

PROPOSED REJECT.

No proposed solution

Cl 141 SC 141.4 P44 L1 # 325
 Johnson, John Broadcom

Comment Type T Comment Status D

The column headings on Tables 141-11 and 141-12 are inconsistent.

SuggestedRemedy

Change the heading on the first column of Table 141-11 to "Wavelength Name".

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 141 SC 141.5.1 P45 L16 # 296
 Lynskey, Eric Broadcom

Comment Type T Comment Status D

Table 141-13 does not list the total average launch power for the single channel case. This is different than Table 141-14. Both tables should be consistent with each other.

SuggestedRemedy

Remove the 7.8 dBm total average launch power from Table 141-14.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

In Table 141-14, change "7.8" to "-"

Cl 141 SC 141.5.2 P47 L30 # 326
 Johnson, John Broadcom

Comment Type T Comment Status D

The line "Conditions of stressed receiver sensitivity test:" in Tables 141-15 and 16 is a section heading and has no associated parameter values. See example format in Table 141-19.

SuggestedRemedy

Remove TBD placeholders on this line in Tables 141-15 and 16 and merge the cells in the row on Table 141-16. Use formatting similar to Table 141-19.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 141 SC 141.5.2 P48 L27 # 297
 Lynskey, Eric Broadcom

Comment Type T Comment Status D

Table 141-16 references Table 75-7 for parameters related to 10G upstream. However, in the 2018 revision of IEEE 802.3, Table 75-7 refers to 10/1 power receive characteristics.

SuggestedRemedy

Replace reference with Table 75-6.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 141 SC 141.5.2 P52 L26 # 324
 Johnson, John Broadcom

Comment Type T Comment Status D

The same type of receiver technology will be used for Nx25G-EPON as for 10G-EPON (APD in TO-can). The same value of receiver reflectance (max) should be used.

SuggestedRemedy

Replace TBD values for Receiver reflectance (max) in Tables 141-19 and 141-20 with a value of -12 dB.

Proposed Response Response Status W

PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 141 SC 141.7.4 P54 L 32 # 304
Lynskey, Eric Broadcom

Comment Type T Comment Status D
Some tests call for "any valid encoded 256B/257B data stream". Some call for "valid Nx25G-EPON signal". Pick one and be consistent.

SuggestedRemedy
Replace 256B/257B data stream with valid Nx25G-EPON signal.

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 141 SC 141.7.5 P54 L 37 # 303
Lynskey, Eric Broadcom

Comment Type T Comment Status D
It seems like the I2 reference is a copy paste from 10G/1G EPON.

SuggestedRemedy
Remove "repeating pattern /I2/ ordered set (see 36.2.4.12) or".

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 141 SC 141.7.9 P55 L 1 # 327
Johnson, John Broadcom

Comment Type T Comment Status D
Section 141.7.9 is incomplete and requires additional text.

SuggestedRemedy
Use the text in johnson_3ca_1_0191.doc for section 141.7.9.

Proposed Response Response Status W
PROPOSED ACCEPT.

Cl 141 SC 141.7.13 P55 L 25 # 301
Lynskey, Eric Broadcom

Comment Type T Comment Status D laser_time
Max Ton value from Table 141-17 is 512ns, since it is inherited from Table 75-8.

SuggestedRemedy
Change 128ns to 512ns.

Proposed Response Response Status W
PROPOSED REJECT.

Table 141-17 shows value of 128ns for new PMDs. Likely, in Table 141-7, rows for Ton and Toff times should be expanded across all PMDs (overriding settings for last column), similar to what is done for Table 141-18

Cl 141 SC 141.7.13 P55 L 33 # 302
Lynskey, Eric Broadcom

Comment Type T Comment Status D laser_time
Max Toff value from Table 141-17 is 512ns, since it is inherited from Table 75-8.

SuggestedRemedy
Change 128ns to 512ns.

Proposed Response Response Status W
PROPOSED ACCEPT IN PRINCIPLE.

See comment #301

Cl 141 SC 141.8.5 P59 L 36 # 388
Kramer, Glen Broadcom

Comment Type T Comment Status D
There is no need to repeat a long list of all defined PMDs. All supported PMDs are already listed in Table 141-7 on page 38.

SuggestedRemedy
Replace the text "Defined PMDs are as follows: <list of PMDs>" with the following text:
"The list of all supported PMDs is shown in Table 141-7."

Proposed Response Response Status W
PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 141 SC 141.9.3 P61 L1 # 328
 Johnson, John Broadcom
 Comment Type T Comment Status D
 The downstream wavelength names in Table 141-21 are inconsistent with the definitions in Table 141-11.
 SuggestedRemedy
 Change the wavelength names in Table 141-21 to agree with Table 141-11. Change DW2 to DW0 and change the column order so that DW0 is to the left of DW1.
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.1.1 P64 L26 # 412
 Kramer, Glen Broadcom
 Comment Type T Comment Status D conventions
 We repeat the Conventions section in every clause (C142, C143, C144). There is a lot of duplicated material, but also some differences in how requirements are stated.
 SuggestedRemedy
 Use one Convention section in C142 and reference it from C143 and C144. Expand the convention section to cover timers, vector notations, and FIFO operations. Specific changes:
 1) Replace subclause 142.1.1 with the material shown in kramer_3ca_6_0119.pdf
 2) In 142.2.5.3, delete definitions of FIFO.Append(v), FIFO.Fill(v), FIFO.GetHead(), and FIFO.IsEmpty()
 3) Add this text to the last sentence of InputFifo definition: "and supports operations <i>Append()</i>, <i>IsEmpty()</i>, and <i>GetHead()</i> (see 142.1.1.5)."
 4) Add this sentence of TxFifo definition: "The TxFifo[] supports operations <i>Append()</i>, <i>Fill()</i>, and <i>GetHead()</i> (see 142.1.1.5)."
 5) Change title of 143.3.3.1 from "State diagram conventions" to "Conventions". Replace subclause text with "See 142.1.1."
 6) Replace subclause 143.3.4.1 text with "See 142.1.1."
 7) Change title of 144.1.6 from "State diagram conventions" to "Conventions". Replace subclause text with "See 142.1.1."
 8) In 144.3.6.3, change definition of EnvList as follows:
 8.a) Remove the sentence "Each EnvList[ch] list has several associated functions:".
 8.b) Remove the list of functions
 8.c) Insert text: "Each <i>EnvList[ch]</i> list supports operations <i>Append()</i>, <i>Clear()</i>, <i>IsEmpty()</i>, <i>GetHead()</i>, and <i>PeekHead()</i> (see 142.1.1.5)."
 9) In Figure 144-22, replace "RemoveHead()" with "GetHead()" (2 places)
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142 SC 142.1.1 P64 L28 # 377
 Powell, William Nokia
 Comment Type ER Comment Status D conventions
 It seems useful to start with the remark that code examples adhere to the C programming language. The subsequent notation ++, --, += and -= does not need to be explained.
 SuggestedRemedy
 The notation used in the state diagrams in this clause follows the conventions in Clause 21.5. Code examples provided in this clause adhere to the style of the "C" programming language. In particular, if the notation ++ or -- is used directly after a variable name representing an integer value, this integer value is incremented by 1 or decremented by 1, respectively. Similarly, if the notation += and -= are used after a variable, the corresponding value is to be incremented or decremented by the following value, respectively.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #412

Cl 142 SC 142.1.3 P64 L43 # 378
 Powell, William Nokia
 Comment Type ER Comment Status D
 The term "FEC-unprotected" is not common.
 SuggestedRemedy
 Suggest to rewrite this, e.g., An ONU burst transmission comprises two or three distinct synchronization pattern (SP) zones, followed by one or more FEC codewords, and ending with an end-of-burst (EBD) delimiter.
 Proposed Response Response Status W
 PROPOSED REJECT.
 It is a counterpart to FEC-protected.

Cl 142 SC 142.1.3 P66 L4 # 379
 Powell, William Nokia
 Comment Type T Comment Status D
 TXD[i]<31:0>, TXC[i]<3:0>, TXC, RXD[i]<31:0>, RXC<3:0>, RXC[i] in Figure 142-2 is first specified in Clause 143.3.1.1.
 SuggestedRemedy
 Propose to either introduce this notation in the text when describing Fig. 142-2, or to refer to Clause 143.3.1.1. BTW, should "TXC" be "TXC[i]"?
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 Transmit direction operates on a single clock signal TXC shared by all channels, so that is correct.
 Add the following statement on page 64, line 25: "See 143.3.1.1 for definition of TXD, TXC, TX_CLK, RXD, RXD, and RX_CLK. In figure 142-2, replace TCX (standalone) with TX_CLK. Same for RXC standalone to RX_CLK - it seems like an error.

Cl 142 SC 142.1.3 P66 L 52 # 380
Powell, William Nokia

Comment Type TR Comment Status D

sentence fragment: where the last codeword may be shortened to minimize the unused LDPC codeword payload ... This statement is inaccurate. First, the LDPC code used has wordlength 17,664, with a 14,592-bit payload and a 3,072-bit parity check segment. A transmitted codeword that comprises a maximum-size payload portion consists of 56 257-bit encoded and scrambled data blocks, i.e., a total of 14,392 bits, followed by 10 257-bit parity blocks carrying interleaved parity information and a codeword delimiter. As such, there is a 200-bit reduction in payload to allow for an integer-number of 256B257B blocks, and the last 512 parity check bits have been punctured to increase the code rate. Given that at this point, the LDPC encoder has not been introduced yet, and can also consider making a more general statement, that, depending on the number of bits to be transmitted during the burst transmission, one or several codewords will be formed, and that all codewords, except the last one, will be of full length.

SuggestedRemedy

Suggested change: The LDPC code used has wordlength 17,664, with a 14,592-bit payload and a 3,072-bit parity check segment. A transmitted codeword that comprises a maximum-size payload portion consists of 56 257-bit encoded and scrambled data blocks, i.e., a total of 14,392 bits, followed by 10 257-bit parity blocks carrying interleaved parity information and a codeword delimiter. As such, there is a 200-bit reduction in payload to allow for an integer-number of 256B257B blocks, and the last 512 parity check bits have been punctured to increase the code rate. The number of bits that are to be placed in the last transmitted codeword of a burst may be shorter than the maximum-size payload. In this case, only the information-carrying part of the payload is transmitted, followed by the entire 10 257-bit parity blocks.

Proposed Response Response Status W
PROPOSED REJECT.

That is a lot of information on FEC code injected way *before* the FEC code is defined. If such a text is needed, I would prefer to have it added in FEC section, even if we need to add a section on truncating FEC codeword in the upstream

Cl 142 SC 142.1.3 P67 L 2 # 381
Powell, William Nokia

Comment Type T Comment Status D

segment: ... data blocks and 10 of 257-bit blocks carrying LDPC parity and codeword delimiter. At this point, the LDPC encoder has not been introduced yet. Consider making a more general statement, that, depending on the number of bits to be transmitted, one or several codewords will be formed, and that all codewords, except the last one, will be of full length.

SuggestedRemedy

Alternative option: In normal operation, the SBD is followed by a number of FEC codewords, where the payload of all codewords, except for the last codeword, comprises 56 257-bit 256B/257B encoded and scrambled data blocks. The last data part of a burst is sent, followed by the full-length parity segment.

Proposed Response Response Status W
PROPOSED REJECT.

Discussion needed at TF. Text reads correctly as is.

Cl 142 SC 142.1.3 P67 L 19 # 382
Powell, William Nokia

Comment Type TR Comment Status D

Figure 142-3 - the parameter N is used in the figure to denote the number of codewords in the burst. This may lead to confusion, given that the codeword length is also denoted by N.

SuggestedRemedy

Suggest to replace N by, e.g., B.

Proposed Response Response Status W
PROPOSED REJECT.

I do not see where in this section N is used in the meaning of FEC codeword length / size.

Cl 142 SC 142.1.3 P67 L 20 # 393
Kramer, Glen Broadcom

Comment Type T Comment Status D

"TP Length" is shown in figures 142-3 and 142-4, but is not explained in text and is not used anywhere else.

SuggestedRemedy

Remove "TP Length" and the associated dimension arrows from both figures.

Proposed Response Response Status W
PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142 SC 142.1.3 P67 L 20 # 403
 Kramer, Glen Broadcom

Comment Type T Comment Status D

"The default number of Sync Pattern zones is two" But yet the diagram for 3 zones is shown immediately below and text after the figure only describes two zones.

SuggestedRemedy

Swap order of figures 142-3 and 142-4.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.2.2 P70 L 20 # 394
 Kramer, Glen Broadcom

Comment Type T Comment Status D

"Prior to being transcoded into 257-bit blocks the Nx25G PCS scrambles four aggregated 66-bit blocks."

This sentence states that PCS itself is being transcoded.

SuggestedRemedy

Change to "The Nx25G PCS scrambles the payload of each 66-bit block. It then accumulates 66-bits blocks into groups of four and transcodes each group into a single 257-bit block"

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.2.4 P70 L 32 # 383
 Powell, William Nokia

Comment Type TR Comment Status D

Original text: The Nx25G-EPON PCS shall encode the transmitted data stream using LDPC(16952,14392) FEC, defined in 142.2.4. Annex 142A gives an example of LDPC(16952,14392) FEC encoding and interleaving. The notation LDPC(16952,14392) FEC is not a common notation for an LDPC code, and it does not fully specify the code, unlike, e.g., a Reed Solomon code. It is suggested to provide a more general statement and refer to subsequent Clauses for further details.

SuggestedRemedy

The Nx25G-EPON PCS shall perform the FEC encoding operation using a quasi-cyclic low-density parity-check (QC-LDPC) code with blockwise interleaving as defined in Clause 142.2.4. Annex 142A provides examples of the blockwise interleaving and LDPC encoding operations.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.2.4 P70 L 33 # 384
 Powell, William Nokia

Comment Type T Comment Status D

Suggest to provide the details of the LDPC code in an Annex. The main advantage is that one can then first specify the full-length quasi-cyclic low-density parity-check code, using an mxn matrix that specifies the amount of cyclic rotation of a diagonal zxz sub-matrix.

SuggestedRemedy

Migrate portions of Clause 142.2.4 to an Annex.

Proposed Response Response Status W

PROPOSED REJECT.

Unclear what "portions of Clause 142.2.4" need to be migrated

Cl 142 SC 142.2.4 P70 L 34 # 385
 Powell, William Nokia

Comment Type T Comment Status D

Annex 142A has not been included yet.

SuggestedRemedy

Suggest to include examples as soon as possible. These may be modified/improved later on.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

Covered by comment #383

Cl 142 SC 142.2.4.1 P70 L 37 # 386
 Powell, William Nokia

Comment Type T Comment Status D

The bit sequence input for a given code block to the FEC Encoder ...

SuggestedRemedy

It may be best to state that during a burst transmission, an ONU is allocated sufficient time to transmit K_B bits. The number of codewords equals B = ceil(K_B/K_max).

Proposed Response Response Status W

PROPOSED REJECT.

Specific text would be welcome, please.

Proposed Responses

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Cl 142 SC 142.2.4.1 P70 L38 # 387
 Powell, William Nokia
 Comment Type T Comment Status D
 sentence: ... The parity check bit sequence produced by FEC Encoder ...
 SuggestedRemedy
 rewrite: ... Prior to encoding, the input bit sequence is grouped into K/z z-bit segments $u_i^{(j)}$
 Proposed Response Response Status W
 PROPOSED REJECT.
 "K/z z-bit segments $u_i^{(j)}$ " is not clear - seems like a sequence of rather random characters. Please present proposed notation in text editor

Cl 142 SC 142.2.4.1 P70 L38 # 336
 Powell, William Nokia
 Comment Type T Comment Status D
 The current text is convoluted; it would make most sense to write that a quasi-cyclic LDPC code was selected, specified by an $m \times n$ shift-matrix and a lifting factor $Z = 256$. This specifies the maximum word length: $N^* = nZ$ and the number of parity-check bits $M^* = mZ$. It is typically also useful to specify $k = n - m$, and $K^* = N - M$, the maximum number of systematic bits. After the definition of the code and its parameters, one can state that one uses K information bits, where $K \leq K_{max} \leq K^*$, and that the remaining $K^* - K$ bits are assumed to be zero, and not transmitted - this way, one also does not need a "zero-padding" module in the encoder. The first $M = M^* - 512$ parity-check bits are transmitted; this implies that the remaining parity-check bits do not have to be computed (one does not need a puncturing module in the encoder). Using this outline, one does not need the parameters P and S.
 SuggestedRemedy
 Proposal: specify the full-length LDPC code in 142.2.4.1. Avoid any discussion about puncturing and shortening here. Move this to 142.2.4.3. The description on p. 75, lines 5-18 is generally better than on p. 71, lines 3-25.
 Proposed Response Response Status W
 PROPOSED REJECT.
 Please propose specific text with changes.

Cl 142 SC 142.2.4.1 P70 L40 # 337
 Powell, William Nokia
 Comment Type TR Comment Status D
 sentence: ... where M is the number of parity check bits.
 SuggestedRemedy
 ... where M is the number of transmitted interleaved parity-check bits.
 Proposed Response Response Status W
 PROPOSED REJECT.
 Unclear what the purpose of the rewrite is.

Cl 142 SC 142.2.4.1 P70 L41 # 338
 Powell, William Nokia
 Comment Type ER Comment Status D
 sentence: ... where $N = K + M$ is the length of the encoder output sequence ... Issue: in Fig. 142-6, the FEC encoder only produces parity-bit segments.
 SuggestedRemedy
 rewrite this sentence to: ... where $N = K + M$ is the length of the transmitted codeword.
 Proposed Response Response Status W
 PROPOSED REJECT.
 The distinction between the existing text and proposed rewrite is unclear. Discussion needed.

Cl 142 SC 142.2.4.1 P70 L46 # 339
 Powell, William Nokia
 Comment Type T Comment Status X
 sentence: the LDPC parity check matrix is a 12-by-69 quasi-cyclic matrix - this is confusing. The matrix that specifies the H-matrix is a 12x69 matrix, but the matrix itself is 12Z x 69Z. It is suggested to move all text that defines a QC-LDPC code to 142.2.4.1 and the encoding details to 142.2.4.2.
 SuggestedRemedy
 the LDPC parity check matrix is specified by a 12 x 69 matrix H_c OR, move the paragraphs starting on p. 71, lines 29 up to p. 74, line 30 to the beginning of Clause 142.2.4.1. The Encoder-related material in 142.2.4.1 can then move to Clause 142.2.4.2.
 Proposed Response Response Status W
 Fixed subclause number to 142.2.4.1 (that is where the page/line points to)

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142 SC 142.2.4.1 P70 L51 # 404
 Kramer, Glen Broadcom

Comment Type T Comment Status D
 "— the number of shortened information bits, S (Smin = 200); "

We never use Smin anywhere else in text. And it is not clear that Smin is only used when we have Kmax information bits. It would be more informative to illustrate how value S is obtained.

SuggestedRemedy
 Replace "(Smin = 200)" with "(S = 14592 - K)"

Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.4.1 P70 L53 # 340
 Powell, William Nokia

Comment Type TR Comment Status D
 sentence: ... where M is the number of parity-check bits after puncturing

SuggestedRemedy
 rewrite: ... where M is the number of transmitted parity-check bits.

Proposed Response Response Status W
 PROPOSED REJECT.

Unclear what the purpose of the rewrite is.

Cl 142 SC 142.2.4.1 P70 L54 # 341
 Powell, William Nokia

Comment Type ER Comment Status D
 sentence: ... the number of parity-check bits after puncturing, M (M = 3072 - 512 = 2560);

SuggestedRemedy
 please note that M has already been defined on p. 70, line 5; it may not be necessary to redefine it here. Alternatively, rewrite: ... the number of transmitted interleaved parity-check bits, M (M = 2560).

Proposed Response Response Status W
 PROPOSED REJECT.

Unclear what the purpose of the rewrite is.

Cl 142 SC 142.2.4.1 P71 L1 # 343
 Powell, William Nokia

Comment Type TR Comment Status D
 a maximum number of information bits is specified, but can this be any number, or is it a multiple of 8, 16, ...? Should one also specify a minimum number of information bits? On p. 67, lines 1-4, it seems that the data granularity is 256 bits.

SuggestedRemedy
 Add information on the minimum payload length and the granularity. If there are no restrictions, then indicate that K can take any possible value, as long as $K \leq K_{max}$.

Proposed Response Response Status W
 PROPOSED REJECT.

Please propose specific text with changes.

Cl 142 SC 142.2.4.1 P71 L1 # 342
 Powell, William Nokia

Comment Type T Comment Status D
 sentence: the number of output bits ... it would be less ambiguous to refer to this as the transmitted sequence?

SuggestedRemedy
 the number of transmitted bits

Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.4.1 P71 L1 # 344
 Powell, William Nokia

Comment Type E Comment Status D
 sentence: ... on the burst length pattern to determine shortening length ... This sentence is ambiguous. The notion of "burst length" is mentioned in 141.3.5.2, p. 43, line 23. There is no notion of a burst length pattern prior to p. 71.

SuggestedRemedy
 Suggest to more specifically formulate the number of bits to be transmitted during a burst, in terms of information bits, and possibly introduce extra variables: the number of data bits, the number of input bits to the FEC encoder (256B/257B redundancy), ...

Proposed Response Response Status W
 PROPOSED REJECT.

Please propose specific text with changes.

Proposed Responses

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Cl 142 SC 142.2.4.1 P71 L3 # 345
 Powell, William Nokia
 Comment Type TR Comment Status D
 sentence: the code rate, $R = K/N$, defined as the code rate after puncturing and after shortening. Propose to use the standard definition.
 SuggestedRemedy
 Rewrite: the code rate, $R = K/N$, defined as the ratio between the number of information bits (K) and the number of transmitted bits (N).
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.4.1 P71 L5 # 346
 Powell, William Nokia
 Comment Type TR Comment Status D
 sentence: The encoder supports highest code rate $R_{max} = K_{max}/N_{max} = 0.849$.
 SuggestedRemedy
 Rewrite: The FEC Encoder supports an FEC code rate up to $R_{max} = K_{max}/N_{max} = 14392/16952 = 0.849$.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 The FEC Encoder supports the FEC code rate up to $R_{max} = K_{max}/N_{max} = 14392/16952 = 0.849$.

Cl 142 SC 142.2.4.1 P71 L8 # 320
 Laubach, Mark Broadcom
 Comment Type TR Comment Status D
 After producing and verifying the test vector addition to Annex 142A, these figures were updated to improved clarity, fix process flow, create symmetry and align with other PCS figures and state diagrams.
 SuggestedRemedy
 Replace both Figure 142-6 and 142-15 (page 87, line 34) with the respective figures in laubach_3ca_3_0119.pdf
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 Per laubach_3ca_3_0119.pdf but do not use capitalization in each and every word in text blocks (not needed)

Cl 142 SC 142.2.4.1 P74 L23 # 348
 Powell, William Nokia
 Comment Type T Comment Status D
 Fig. 142-7 - the labeling in this figure is ambiguous. If the systematic part of this "codeword" represents the input to the encoder, then the label "transmitted user bits" is inaccurate, as the encoder operates on an "bit-interleaved" sequence. The label "Transmitted Parity Bits" is also ambiguous, as the Parity Bits are interleaved prior to transmission. At the same time, this is also not a depiction of the transmitted sequence.
 SuggestedRemedy
 It is proposed to modify at least the labels, and possibly to introduce a second/third figure, or a combined figure. One could then show: block of K information bits; implicit zero-extension; 256-bit blockwise interleaving; encoding, i.e., determination of the first 10 256-bit parity-check segments; (de)interleaving of the parity segments; transmission of the K user bits, followed by 2560 interleaved parity-check bits.
 Proposed Response Response Status W
 PROPOSED REJECT.
 Please propose specific changes to text and/or figures.

Cl 142 SC 142.2.4.2 P71 L47 # 347
 Powell, William Nokia
 Comment Type T Comment Status D
 right column shifts
 SuggestedRemedy
 propose to introduce a shift-by-one $Z \times Z$ matrix B, or using a cyclic permutation. The matrix probably works best. The HC matrix would then specify the exponent of B (repeated shifts).
 Proposed Response Response Status W
 PROPOSED REJECT.
 Please provide the said matrix with text around it.

Proposed Responses

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Cl 142 SC 142.2.4.3 P74 L43 # 349
 Powell, William Nokia
 Comment Type TR Comment Status D
 Sentence: ... which is then interleaved ... To be consistent with other parts of the text, the term de-interleaved should be used; a better option seems to be to write that a reverse omega network is used.
 SuggestedRemedy
 Propose to write: the first 10 256-bit segments of computed parity bits $p^{(1)}$ $p^{(10)}$ are interleaved using an 8-stage reverse Omega network with seed value $s(i)$.
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.4.4 P75 L1 # 319
 Laubach, Mark Broadcom
 Comment Type TR Comment Status D 142.2.4.4
 After producing and verifying the test vector addition to Annex 142A, the interleaver text was reviewed. This update removes ambiguities, improves clarity, and reduces wording. Also provided is laubach_3ca_2_0119.pdf, a framemaker compare with the Draft 1.4 text.
 SuggestedRemedy
 Replace 142.2.4.4 with contents of laubach_3ca_1_0119.pdf.
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.4.4 P75 L1 # 350
 Powell, William Nokia
 Comment Type TR Comment Status D 142.2.4.4
 sub-clause title is confusing; the information part is transmitted in regular order (non-interleaved)
 SuggestedRemedy
 Proposed change: Interleaving operation of parity-bit segment
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #319

Cl 142 SC 142.2.4.4 P75 L3 # 351
 Powell, William Nokia
 Comment Type TR Comment Status D 142.2.4.4
 For the purposes here: ... it is hard to parse this sentence. The recommendation is to remove this paragraph.
 SuggestedRemedy
 Proposed change: remove this paragraph.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #319

Cl 142 SC 142.2.4.4 P75 L7 # 353
 Powell, William Nokia
 Comment Type T Comment Status D 142.2.4.4
 Sentence: ... reverse-omega networks. The term omega network is not all that common. It may be a better idea to introduce the omega network and the reverse network first, as for the LDPC code, and then describe the encoder and decoder operation.
 SuggestedRemedy
 Proposal: change the order of discussion - first the full-length LDPC code, the omega network and the reverse omega network, and then the FEC Encoder (and optionally, the FEC Decoder).
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #319

Proposed Responses

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Cl 142 SC 142.2.4.4 P75 L7 # 352
Powell, William Nokia

Comment Type TR Comment Status D 142.2.4.4

The term interleaving is generally used to describe the process of transforming a sequence that is in regular order into a sequence that is interleaved. Rather than turning this definition upside-down, it is proposed to discuss the 8-stage 256-input omega network and the 8-stage 256-input reverse omega network. One can then simply state that for the interleaver in the encoder, an 8-stage 256-input reverse omega network is used, and that, consequently, the decoder uses the 8-stage 256-bit omega network.

SuggestedRemedy

The FEC Encoder uses an 8-stage 256 x 256 reverse omega network.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See comment #319

Cl 142 SC 142.2.4.4 P75 L15 # 354
Powell, William Nokia

Comment Type T Comment Status D 142.2.4.4

The proposed de-interleaver/interleaver is a module that has 256 data inputs, 256 data outputs, a 128-bit seed, and a "fixed/pre-defined" cyclic rotation of this seed (shift factor: 17). Fig. 142-8 seems to imply that a massively parallel structure is needed with 57 * 256 inputs.

SuggestedRemedy

It seems more straightforward to present one de-interleaver unit and then associate the seeds with the segment indices.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See comment #319

Cl 142 SC 142.2.4.4 P75 L28 # 356
Powell, William Nokia

Comment Type TR Comment Status D 142.2.4.4

Sentence: The parity bit interleaver ... given that Fig. 142-8 show the information bit de-interleaver, it seems to make sense to first discuss the parity-check bit interleaver

SuggestedRemedy

Sentence: The parity-check bit de-interleaver ...

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See comment #319

Cl 142 SC 142.2.4.4 P75 L28 # 355
Powell, William Nokia

Comment Type TR Comment Status D 142.2.4.4

Sentence: The first ten ... These local interleavers are realized by 12 independent omega networks.

SuggestedRemedy

Proposed change: Change first "de-interleaved" to "interleaved"

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See comment #319

Cl 142 SC 142.2.4.4 P75 L30 # 357
Powell, William Nokia

Comment Type TR Comment Status D 142.2.4.4

Sentence: ... consists of 12 local interleavers ... not sure what local refers to; it seems to make more sense to state that the first 10 256-bit parity-check bit segments are de-interleaved using an 8-stage 256x256 reversed omega network, where each segment has its own seed.

SuggestedRemedy

Proposed: The first 10 256-bit parity-check bit segments are de-interleaved using an 8-stage 256x256 reversed omega network, where each segment has its own seed.

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

See comment #319

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142 SC 142.2.4.4 P75 L36 # 358
 Powell, William Nokia
 Comment Type TR Comment Status D 142.2.4.4
 The figure caption is misleading, as this is the Parity-Check Bit interleaver.
 SuggestedRemedy
 Revised caption: Parity-Check Bit interleaver.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 See comment #319

Cl 142 SC 142.2.4.5 P76 L31 # 359
 Powell, William Nokia
 Comment Type T Comment Status D
 Sentence: ... and i - 0, ..., 127 - the regular numbering thus far starts at 1. In the context of the permutation, an index starting at 0 can be useful, but it is not difficult to let this index also start at 1.
 SuggestedRemedy
 Rewrite: ... and i = 0, ..., 127.
 Proposed Response Response Status W
 PROPOSED ACCEPT.
 Changed E to T

Cl 142 SC 142.2.5.1 P80 L26 # 298
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 The lower 257-bits are no longer TBD, per 142.3.5.1. Also, it shouldn't be necessary to specify the 258-bit value here and 257-bit value elsewhere.
 SuggestedRemedy
 Value: {MSB = 0, EBD} as specified in 142.3.5.1
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 Value: MSB = 0, EBD as specified in 142.3.5.1

Cl 142 SC 142.2.5.2 P81 L25 # 278
 Hajduczenia, Marek Charter Communicatio
 Comment Type TR Comment Status D
 ClkOut and ClkXfr are defined in 142.2.5.2 and have the very same definition: "The clear on read variable ClkOut is set to true once for each 257-bits of data output by the PMD." - since the event happens at the specific moment of time (when 257 bits are transferred by the PMD), we could
 - combine definition into just one
 - rewrite it to set to true on bit 257 and false otherwise - this is sufficient to trigger transition in target SDs
 SuggestedRemedy
 Remove ClkXfr
 Change definition of ClkOut to read "The variable ClkOut is set to true once every 257-bits of data output by the PMD and set to false otherwise."
 Change the name of ClkOut to ClkOut257b. Update SDs (142-12, 142-13, and 142-14 accordingly)
 Change all instances of ClkXfr to ClkOut257b. Update SDs (142-12, 142-13, and 142-14 accordingly)
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 142 SC 142.2.5.3 P83 L11 # 360
 Powell, William Nokia
 Comment Type T Comment Status D
 FecParity() - would it make sense to provide a counter as argument?
 SuggestedRemedy
 FecParity(i)
 Proposed Response Response Status W
 PROPOSED REJECT.
 Not sure what counter would be passed and what the purpose of this passing would be.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142 SC 142.2.6.2 P81 L 38 # 390
 Kramer, Glen Broadcom

Comment Type T Comment Status D

When we define a variable with "[]" at the end, we always specify the type as "array of <units>". We create such array definitions only if we need to access individual elements using an index. The ParityStagingBuffer definition has "[]", but the type is defined as "block of 2570 bits".

This is inconsistent. We either need to define it as "array of 270 bits" or remove the brackets. In text or in the state diagrams, we never access individual elements of ParityStagingBuffer. We only use "<m:n>" notation as we do for blocks (vectors).

SuggestedRemedy

- 1) Make the type "2570-bit block"
- 2) Remove "[]" from the definition.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.3.1 P85 L 48 # 286
 Hajduczenia, Marek Charter Communicatio

Comment Type T Comment Status D

Remove current text from the subclause and insert red TBD

SuggestedRemedy

Per comment

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.3.5 P88 L 15 # 411
 Kramer, Glen Broadcom

Comment Type TR Comment Status D

There are multiple issues with the PCS receive data path state diagrams:

- 1) OLT and ONU synchronizers don't pass any aligned data to the rest of PCS receive path
- 2) Receive state diagram attempts to find perfect match for EBD and SBD values, which with input BER of 0.01 will happen only in 0.6% of bursts.
- 3) PMAUDI is a primitive. But it is used in the PCS receive state diagram as if it is a variable or a buffer.
- 4) In PCS Output SD, the variable OutEqCtr is used without being initialized
- 5) Non-mutually exclusive transitions from state NEXT_VECTOR

SuggestedRemedy

Replace subclause 142.3.5 with the material in kramer_3ca_2_0119.pdf.

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.3.5.1 P89 L 6 # 398
 Kramer, Glen Broadcom

Comment Type T Comment Status D

"parity delimiter" term is undefined. We use the term "FEC codeword delimiter"

SuggestedRemedy

Replace "parity delimitet" with "FEC codeword delimiter" in two places:
 page 89, line 6
 page 89, line 37

Proposed Response Response Status W

PROPOSED ACCEPT.

Cl 142 SC 142.3.5.2 P90 L 12 # 415
 Remein, Duane Huawei

Comment Type T Comment Status D

PMAUDI[i] Alias for PMA_UNITDATA[i]<256:0>.indication needs refinement

SuggestedRemedy

Change to: PMAUDI[i]
 Alias for PMA_UNITDATA[i](rx_code_group<256:0>)

Proposed Response Response Status W

PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 142A **SC 142A** **P97** **L37** # **321**
 Laubach, Mark Broadcom

Comment Type **TR** **Comment Status** **D**

Insert informational test vector text. Note: the five test vector files are also provided to the Editor in a zip file.

SuggestedRemedy
 Insert new text after 142A.1 as per laubach_3ca_4_0119.pdf

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT.

Cl 143 **SC 143.2.4.2** **P100** **L29** # **405**
 Kramer, Glen Broadcom

Comment Type **T** **Comment Status** **D**

Figure 143-3 may be confusing to readers, since it doesn't show the envelope headers at the beginning of each frame, as Figure 143-4 does. Probably just leaving Figure 143-4 is enough.

SuggestedRemedy
 Delete Figure 143-3 and its referemce in text.

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT.

Cl 143 **SC 143.2.6** **P106** **L23** # **401**
 Kramer, Glen Broadcom

Comment Type **T** **Comment Status** **D**

Section 143.2.6 "MCRS Time synchronization" is located in the generic part of MCRS clause, but it talks about EPON-specific concepts, such as OLT, ONU, LocalTime. At the same time, there is an empty section 143.4.2 "MCRS and MPCP clock synchronization" in the EPON-specific part of the clause.

SuggestedRemedy
 Move the subclause 143.2.6 into 143.4.2. Use the title "MCRS Time synchronization"

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT.

Cl 143 **SC 143.3.4.4** **P125** **L22** # **400**
 Kramer, Glen Broadcom

Comment Type **TR** **Comment Status** **D**

Definition of IsMisaligned function is wrong. The function is supposed to return true is the first xGMII transfer contains the second part of IBI EQ and the second transfer contains the first half of an envelope header.

SuggestedRemedy
 Replace the definition of IsMisaligned() function with the one shown in kramer_3ca_5_0119.pdf. Note the italics and indentation.

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT.

Comment line was fixed (was 12, should be 22)

Cl 144 **SC 144.1.2** **P138** **L3** # **279**
 Hajduczenia, Marek Charter Communicatio

Comment Type **T** **Comment Status** **D**

Text missing in 144.1.2 "Position of Multipoint MAC Control within the IEEE 802.3 hierarchy"

SuggestedRemedy
 Use the text per hajduczenia_3ca_1_0119.pdf

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT.

Cl 144 **SC 144.1.3** **P138** **L20** # **316**
 Lynskey, Eric Broadcom

Comment Type **T** **Comment Status** **D**

The CCP is missing from Figure 144-3.

SuggestedRemedy
 Add, similar to GATE generation process, to show that there are multiple instances.

Proposed Response **Response Status** **W**
 PROPOSED ACCEPT IN PRINCIPLE.

Update Figure 144-3 and Figure 144-4 to show CCPDU processing block in OLT and ONU within MPMC (outside of MPCP block shown already).

Proposed Responses

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Cl 144 SC 144.3.1 P182 L48 # 294
 Lynskey, Eric Broadcom

Comment Type T Comment Status D
 INVALID_COMMAND is not defined.

SuggestedRemedy

INVALID_COMMAND. This constant represents the value of ActionResultCode corresponding to "Invalid command", per Table 144-11. Value 0x4.

Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.2.2 P148 L6 # 293
 Lynskey, Eric Broadcom

Comment Type T Comment Status D
 MLID is also used to carry CCPDUs.

SuggestedRemedy

Add "and CCPDUs (see 144.4)" to end of first sentence.

Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.4 P149 L28 # 410
 Kramer, Glen Broadcom

Comment Type TR Comment Status D MPCPDU-rewrite

Action item from Bangkok meeting to update the MPCPDU description section to reflect the new approach of operand list structure in state diagrams

The main purpose of this update was to align field names and message structures with what we use in state diagrams and to ensure that all fields are defined only once in a single place.

SuggestedRemedy

Replace the current subclause 144.3.4 with the text and figures provided in kramer_3ca_3_0119.pdf.

In the new subclause, each MPCPDU has its operands grouped in a single structure called MsgName and every field can be accessed in any state diagram by using notation MsgName.FieldName. All state diagrams in C144 already use this notation.

(By definition, the operand list in a MAC Control message comprises all the fields following the opcode, but excluding Pad and FCS).

Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.4.1 P151 L39 # 306
 Lynskey, Eric Broadcom

Comment Type T Comment Status D MPCPDU-rewrite
 The bit positions of FR and F in Figure 144-10 are not clear.

SuggestedRemedy

Add some bit positions on the figure to show that F corresponds to bit [23] and FR corresponds to bit [22] and EnvLength corresponds to bits [21:0]. This would be similar to Figure 144-15.

Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.

See comment #410

Proposed Responses

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Cl 144 SC 144.3.5 P161 L15 # 307
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 The statement about aborting the registration attempt should be from the point of view of receiving the new SYNC_PATTERN_MPCPDU.
 SuggestedRemedy
 If a SYNC_PATTERN_MPCPDU is received prior to the transmission of a REGISTER_REQ MPCPDU of an ONU responding to a previous discovery window...
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.5 P161 L28 # 406
 Kramer, Glen Broadcom
 Comment Type T Comment Status D
 "Discovery windows are unique in that they are the only times when multiple ONUs can access the PON simultaneously, and transmission overlap can occur."
 This statement is not true in multi-channel PON.
 SuggestedRemedy
 Change the sentence to
 "Discovery windows are unique in that they are the only times when multiple ONUs can access the same upstream channel simultaneously, and transmission overlap can occur."
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 Change the sentence to
 "Discovery windows are unique in that they are the only times when multiple ONUs >>>may<<< access the same upstream channel simultaneously, and transmission overlap >>>may<<< occur."

Cl 144 SC 144.3.5 P161 L54 # 309
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 The final sentence that carries over to the next page is incorrect. The OLT no longer sends laser on/off back to the ONU.
 SuggestedRemedy
 Delete the final sentence on page 161 beginning with, "The OLT also..."
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.5 P162 L4 # 408
 Kramer, Glen Broadcom
 Comment Type T Comment Status D
 There are several problems with the following sentence:
 "The OLT at that time has enough information to schedule the ONU for access to the PON and transmits a standard GATE MPCPDU allowing the ONU to transmit a REGISTER_ACK MPCPDU."
 1) Not clear at what time the OLT has the information.
 2) "transmits a standad GATE" implies that 802.3ca standard will also describe a non-standard GATE.

SuggestedRemedy
 Change the sentence to
 "After processing the REGISTER_REQ MPCPDU received from a given ONU, the OLT has enough information to schedule that ONU for access to the PON. The OLT transmits a GATE MPCPDU allowing the ONU to transmit a REGISTER_ACK MPCPDU."
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.3.5.1 P164 L15 # 310
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 Does a constant need a default value?
 SuggestedRemedy
 Remove "default value".
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 144 SC 144.3.5.1 P164 L29 # 311
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 A constant shouldn't have an unknown value.
 SuggestedRemedy
 Move GRANT_MARGIN to 144.3.5.3 Variables.
 Proposed Response Response Status W
 PROPOSED ACCEPT IN PRINCIPLE.
 Per comment + change "constant" to "variable". Change name from GRANT_MARGIN to GrantMargin + update SDs

Cl 144 SC 144.3.6.8. P172 L39 # 396
 Kramer, Glen Broadcom
 Comment Type TR Comment Status D
 In Figure 144-22, "=" shall be "<=". The originally accepted state diagram had the correct symbol.
 SuggestedRemedy
 Replace "MsgGate.StartTime - LocalTime = MPCP_PROCESS_DLY"
 with
 "MsgGate.StartTime - LocalTime <= MPCP_PROCESS_DLY"
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.4.1.1 P177 L9 # 314
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D
 It doesn't seem quite right to have the ONU send a unicast CC_RESPONSE. In 144.4.2, it says the destination address of the CCPDU can have either the multicast address or a unicast address associated with a PLID. It seems that the ONU should be able to use the multicast DA here.
 SuggestedRemedy
 Remove "unicast" in all five instances of "sends a unicast CC_RESPONSE".
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.4.3.1 P182 L7 # 397
 Kramer, Glen Broadcom
 Comment Type T Comment Status D
 No units are needed for CCP_TIMEOUT, since this interval applies to a timer, not a counter. We do not specify time resolution units for timers.
 SuggestedRemedy
 Strike ",expressed in units of EQT."
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Cl 144 SC 144.4.3.1 P182 L11 # 317
 Lynskey, Eric Broadcom
 Comment Type T Comment Status D delete_retry
 Setting a 100ms timeout and retry limit of 3 appears to be taking control away from the client. In the current draft, the client could immediately issue the same CCP message again after 300ms.
 SuggestedRemedy
 Delete these two constants.
 Proposed Response Response Status W
 PROPOSED REJECT.
 The purpose is to enforce a retry mechanism up to 3 times, with 100ms max wait time for ONU response.

Cl 144 SC 144.4.3.3 P184 L7 # 399
 Kramer, Glen Broadcom
 Comment Type T Comment Status D
 No return value is needed in the definition of function UpdateChState(int chIndex, int NewState)
 SuggestedRemedy
 Delete "int4"
 Proposed Response Response Status W
 PROPOSED ACCEPT.

Proposed Responses

IEEE P802.3ca D1.4 25/50G-EPON Task Force 5th Task Force review comments

Cl 144 **SC 144.4.3.6** **P185** **L1** # **318**
 Lynskey, Eric Broadcom

Comment Type **T** **Comment Status** **D** *delete_retry*

In a different comment, I suggested removing the timeout and retry limits. If that is accepted, changes will also be needed in figure 144-29.

SuggestedRemedy

Remove all state transitions leaving FORWARD_REQUEST. Add a new UCT transition from FORWARD_REQUEST to WAIT_FOR_CCPDU. Also remove the CcpRetry action in WAIT_FOR_CCPDU.

Proposed Response *Response Status* **W**

PROPOSED REJECT.

See comment #317

Cl 144 **SC 144.4.3.6** **P186** **L8** # **315**
 Lynskey, Eric Broadcom

Comment Type **T** **Comment Status** **D**

There is no enforced priority if the MCSR and MCII happen at the same time.

SuggestedRemedy

Change so that MCII(MsgChRequest) has priority when leaving WAIT_FOR_CCPDU and FORWARD_CC_REQUEST states.

Proposed Response *Response Status* **W**

PROPOSED ACCEPT IN PRINCIPLE.

Change "MCII(MsgChRequest)" to "MCII(MsgChRequest) * !MCRS (MsgChState)"

Change "MCRS (MsgChState)" to "MCRS (MsgChState) * !MCII(MsgChRequest)"

This way, they are both mutually exclusive.