

Figure 143–13—MCRS Transmit Function, Transmit Process state diagram

1) Corrected bit positions for the 1st and 2nd 25GMII transfers within an EQ

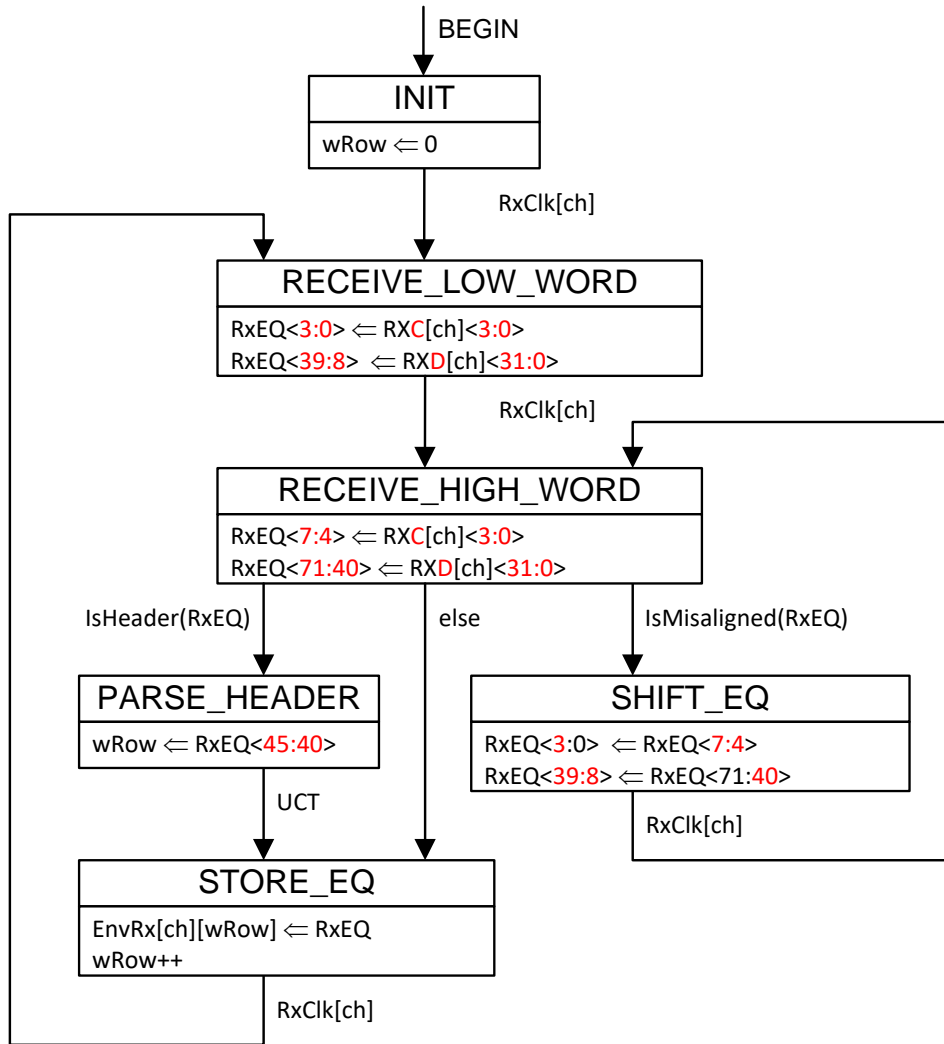


Figure 143–15—MCRS Receive Function, Receive Process state diagram

- 2) Corrected bit positions for the 1st and 2nd 25GMII transfers within an EQ
- 3) Moved reading TXC bits to be in front of reading TXD bits to match the MCRS Transmit state diagram
- 4) Fixed bit positions for EPAM (in PARSE_HEADER state)