Summary of ERL, SNR\_ISI, and RL comment recommendations (comments #34-#49 other ERL comments #24, #25, #26, #27, #29, #65, #67, #68, #89, #90, #93, #94, #96, #96)

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# ToC

- ☐ ERL in Draft
- ☐ History
- ☐ Documents suggesting ERL parameters
- ☐ Overview of Proposal

#### ERL in Draft 3.1

- ☐ In Draft 3.1 ERL (effective return loss) is proposed to replace
  - SNR<sub>ISI</sub> specifications
  - Frequency domain return loss (RL) mask specifications
- ☐ The above addresses some of these reported Issues with SNR<sub>ISI</sub> and RL
  - measuring SNDR and SNR<sub>ISI</sub>
  - test fixture variability impacting measurements
  - Return loss tracking COM (i.e. performance)
    - Short packages perform better than long packages but have worst return loss
  - Frequency domain not representative of time domain PHY requirements
    - Strict RL masks may make the link work
    - But also may be overly prohibitive

## **History**

- ☐ ERL limits and parameters were presented at the January Interim
  - mellitz 3cd 01a 0118
- ☐ A proposal for add ERL to Annex 93A was given by Adee Ran
  - 8023cd-anx93A-ERL-proposal
- ☐ ERL limits and parameters were summarized at the January Interim
  - ran\_3cd\_01\_0118
- □ Draft 3.1 contains data from 8023cd-anx93A-ERL-proposal and 01\_0118
- ☐ Example COM code to compute ERL was posted at subsequent ad hoc
  - mellitz\_3cd\_020718\_adhoc-v2 (COM v2.24)
  - Reflects slight difference between mellitz\_3cd\_01a\_0118 and D3.1
- ☐ ERL data for real produce was requested at the Task Force so support or change the ELR limits.
- ☐ Since the interim thre has been somework to suststanciate the ERL values
- ☐ ERL has been shown to correlate to COM and SNR<sub>isi</sub>
  - mellitz\_022118\_3cd\_adhoc & heck\_022118\_3cd\_adhoc

### **Documents suggesting ERL parameters**

- ☐ Presented in an ad hoc by Richard Melitz
  - mellitz\_022118\_3cd\_adhoc
- □ Reflects slight differences between mellitz\_3cd\_01a\_0118 and D3.1
- Measured product data has considered in ERL limit proposal

# **Overview of Proposal**

mellitz\_022118\_3cd\_adhoc

| Clause                | ERL Min<br>(dB)<br>Proposal | ρ <sub>x</sub><br>Proposal | N<br>Proposal        | ERL Min<br>(dB)<br>D3.1 | ρ <sub>x</sub><br>D3.1 | N<br>D3.1 |
|-----------------------|-----------------------------|----------------------------|----------------------|-------------------------|------------------------|-----------|
| 136 Tx Host           | 14.5                        | 0.44                       | 300 UI               | 9                       | .3                     | 200       |
| 136 Rx Host           | 14.5                        | 0.44                       | 300 UI               | 9                       | .3                     | 200       |
| 136 Cable<br>Assembly | 14                          | 0.44                       | t <sub>s</sub> *10 s | 10.5                    | .35                    | 200       |
| 137 Tx Device         | 16.1                        | 0.44                       | 100 UI               | 19.5                    | 0.33                   | 200       |
| 137 Rx Device         | 16.1                        | 0.44                       | 100 UI               | 19.5                    | 0.33                   | 200       |
| 137 Channel           | 11                          | 0.44                       | t <sub>s</sub> *10 s | 95                      | 0.11                   | 200       |

heck\_022118\_3cd\_adhoc

sakai\_3cd\_01\_0318

# Thank You!