

100GBASE-DR Link Budget Considerations

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Background and Motivation

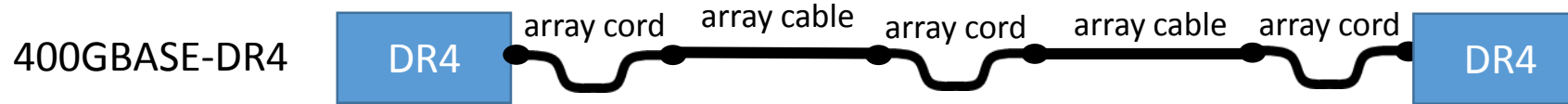
- One of IEEE 802.3cd objectives is to *‘define a single lane 100 Gb/s PHY for operation over duplex SMF with lengths up to at least 500 m, consistent with IEEE P802.3bs Clause 124’* (400GBASE-DR4).
- In the adopted 100GBASE-DR baseline of [traverso 3cd 03a 0916](#), it was stated: “with penalties consistent with a channel allowing up to 4 discrete reflectances of -35 dB” – This means an additional ~0.2 dB MPI penalty needs to be included in the link budget.
- No consensus has been reached as how to allocate this ~0.2 dB between Tx and Rx.
- As the additional MPI penalty is primarily due to the high reflectance LC connectors in 100GBASE-DR links, which also have lower insertion loss, high MPI penalty and high link loss do not occur at the same time.
- In this presentation, we propose to use the total of link loss and MPI penalty for 100GBASE-DR link budget (vs. consider them separately), show this would support more usage cases with the same total budget as 400GBASE-DR4, and recommend changes to the Draft 1.0.

Link Budget Based on Total Link Loss + MPI Penalty

- 1) Methodologies used
 - Referencing [kolesar 3bs 01 0514](#) for MPO/LC loss statistics and model
 - Assume MPO mean loss = 0.35 dB (stdev = 0.25 dB), and
LC mean loss = 0.2 dB (stdev = 0.15 dB).
 - Referencing [anslow 03 1107.xls](#) for SMF attenuation
 - Referencing [king 01a 0116 smf](#) for MPI calculation

- 2) Compare the usage cases covered by 3 dB link loss and 0.3 dB MPI penalty vs. the usage cases covered by 3.1 dB total budget

400GBASE-DR4 Reference



500m Double link with 4 MPO connectors

Connector Losses = 2.65 dB,

Fiber loss = 0.25dB,

-> Link Loss = 2.9 dB (rounded to 3 dB)

MPI Penalty = 0.1 dB

Agreed Budget = 3.1 dB

500m Link Loss & MPI Penalties vs. # of LC/MPO Connectors

Link Loss	0 MPO	1 MPO	2 MPO	3 MPO	4 MPO	5 MPO
0 LC	0.25	1.23	1.83	2.38	2.9	3.4
1 LC	0.83	1.53	2.11	2.65	3.16	n.c.
2 LC	1.18	1.82	2.38	2.91	3.41	n.c.
3 LC	1.5	2.1	2.65	3.16	n.c.	n.c.
4 LC	1.8	2.38	2.91	3.42	3.91	n.c.
5 LC	2.09	2.65	3.17	n.c.	n.c.	n.c.
6 LC	2.37	2.91	3.42	n.c.	n.c.	n.c.
7 LC	2.64	3.17	n.c.	n.c.	n.c.	n.c.
8 LC	2.91	n.c.	n.c.	n.c.	n.c.	n.c.

MPI penalty	0 MPO	1 MPO	2 MPO	3 MPO	4 MPO	5 MPO
0 LC	n.c.	n.c.	n.c.	n.c.	0.1	n.c.
1 LC	n.c.	n.c.	n.c.	0.07	0.06	n.c.
2 LC	n.c.	n.c.	0.13	0.11	0.12	n.c.
3 LC	n.c.	0.2	0.19	0.18	n.c.	n.c.
4 LC	n.c.	0.33	0.26	n.c.	n.c.	n.c.
5 LC	n.c.	0.36	0.35	n.c.	n.c.	n.c.
6 LC	0.55	0.43	n.c.	n.c.	n.c.	n.c.
7 LC	0.55	n.c.	n.c.	n.c.	n.c.	n.c.
8 LC	0.55	n.c.	n.c.	n.c.	n.c.	n.c.

Green cells = Margins w/respect 3.0 dB link loss

Yellow cells = Less than 0.2dB margin w/respect 3.0dB Link loss

Orange cells = Not supported by 3.0dB link loss

Blue = 400GBASE-DR4 link loss

Red = considered main topology for 100GBASE-DR double link

Green cells = MPI penalty < 0.3dB

Orange cells = MPI penalty > 0.3dB

Blue = 400GBASE-DR4 agreed MPI penalty (calculated is < 0.1dB)

- High link loss and high MPI penalty not always occur at the same time

Independent Link Loss & MPI Penalty vs. Total Budget

3.3 dB Total Budget (3 dB Loss + 0.3 dB MPI Penalty)

Loss only	0 MPO	1 MPO	2 MPO	3 MPO	4 MPO
0 LC	-	-	n.c.	n.c.	2.90
1 LC	-	n.c.	n.c.	2.65	3.16
2 LC	n.c.	n.c.	2.38	2.91	3.41
3 LC	n.c.	2.10	2.65	3.16	n.c.
4 LC	1.80	2.38	2.91	n.c.	n.c.
5 LC	2.09	2.65	3.17	n.c.	n.c.
6 LC	2.37	2.91	n.c.	n.c.	n.c.
7 LC	2.64	3.17	n.c.	n.c.	n.c.
8 LC	2.91	n.c.	n.c.	n.c.	n.c.

Green Cells: Meet both 3dB link loss and 0.3dB MPI Penalty

Orange cells: OK with 3dB link loss but MPI penalty > 0.3 dB

3.1 dB Total Budget

Loss+MPI	0 MPO	1 MPO	2 MPO	3 MPO	4 MPO
0 LC	-	-	n.c.	n.c.	3.00
1 LC	-	n.c.	n.c.	2.72	3.22
2 LC	n.c.	n.c.	2.51	3.02	3.53
3 LC	n.c.	2.30	2.84	3.34	n.c.
4 LC	n.c.	2.71	3.17	n.c.	n.c.
5 LC	n.c.	3.01	3.52	n.c.	n.c.
6 LC	2.92	3.34	n.c.	n.c.	n.c.
7 LC	3.19	n.c.	n.c.	n.c.	n.c.
8 LC	n.c.	n.c.	n.c.	n.c.	n.c.

Green Cells: Meeting 3.1 dB total budget

Yellow Cells: Can be covered with an extra 0.2 dB

- Consistent with previous methods
- Less usage cases covered
- Different Tx OMA_(min) and Rx sensitivity for DR (4xDR) and DR4

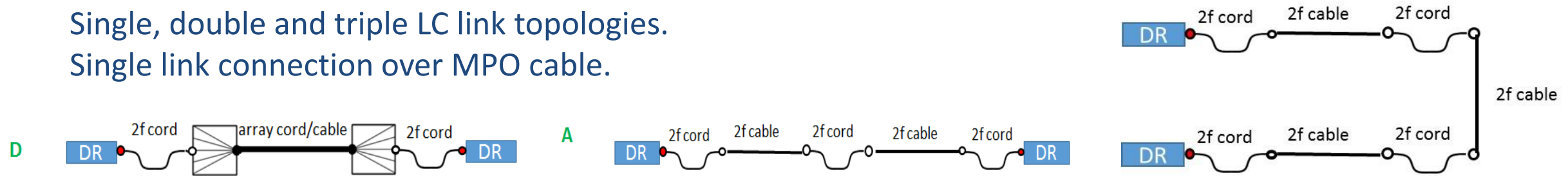
- Consider the total budget of (loss + MPI penalty)
- More usage cases covered
- Same Tx OMA_(min) and Rx sensitivity for DR (4xDR) and DR4

Main Link Topologies Supported by 3.1 dB Total Budget

Assuming 3.1dB total budget for 100GBASE-DR, we can include below topologies:

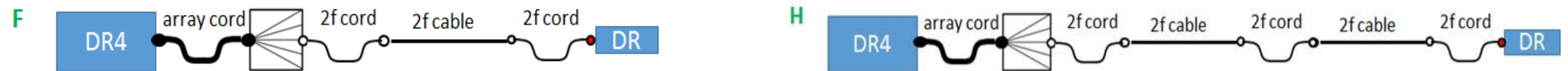
100GBASE-DR over 100GBASE-DR

Single, double and triple LC link topologies.
Single link connection over MPO cable.



Breakout: 4x100G-DR (DR4-like) over 100GBASE-DR

Single, double links connection for most representative break-out cases.



100GBASE-DR (4xDR) optical interface specifications identical to 400GBASE-DR4 specifications

Optical Return Loss

- Field additions of all possible reflections including Rx without link loss

# of -35 dB Discrete Reflectance	# -45 dB Reflectance MPO allowed	Optical Return Loss (dB)
0	4	22.8
1	3	21.4
2	3	19.8
3	2	18.8
4	1	17.9
5	1	16.8
6	0	16.1

Recommend to use 16.1 dB for the worst case as the optical return loss specification

Recommend Changes to Draft 1.0

Table 140–6—100GBASE-DR transmit characteristics

Description	Value	Unit
Signaling rate (range)	53.125 ± 100 ppm	GBd
Modulation format	PAM4	—
Wavelength (range)	1304.5 to 1317.5	nm
Side-mode suppression ratio (SMSR), (min)	30	dB
Average launch power (max)	4	dBm
Average launch power ^a (min)	-2.4	dBm
Outer Optical Modulation Amplitude (OMA _{outer}) (max)	4.2	dBm
Outer Optical Modulation Amplitude (OMA _{outer}) (min) ^b	-0.3	dBm
Launch power in OMA _{outer} minus TDECQ (min)	-1.3	dBm
Transmitter and dispersion eye closure for PAM4 (TDECQ) (max)	2.5	dB
Average launch power of OFF transmitter (max)	-20	dBm
Extinction ratio (min)	5	dB
RIN _{21.4} OMA (max)	-142	dB/Hz
Optical return loss tolerance (max)	21.4	dB
Transmitter reflectance ^c (max)	-26	dB

→ Keep unchanged, same as in DR4

→ Keep unchanged, same as in DR4

→ Change to RIN_{16.1}OMA

→ Change to 16.1 dB

Recommended Changes to Draft 1.0 (II)

Table 140-7—100GBASE-DR receive characteristics

Description	Value	Unit
Signaling rate (range)	53.125 ± 100 ppm	GBd
Modulation format	PAM4	—
Wavelengths (range)	1304.5 to 1317.5	nm
Damage threshold ^a	6.5	dBm
Average receive power (max)	4	dBm
Average receive power ^b (min)	-5.4	dBm
Receive power (OMA _{outer}) (max)	4.2	dBm
Receiver reflectance (max)	-26	dB
Receiver sensitivity (OMA _{outer}) ^c (max)	-4.4	dBm
Stressed receiver sensitivity (OMA _{outer}) ^d (max)	-1.9	dBm
Conditions of stressed receiver sensitivity test: ^e		
Stressed eye closure for PAM4 (SECQ)	2.5	dB

→ Keep unchanged

→ Keep unchanged

Table 140-8—100GBASE-DR illustrative link power budget

Parameter	Value	Unit
Power budget (for max TDECQ)	5.6	dB
Operating distance	500	m
Channel insertion loss ^a	3	dB
Maximum discrete reflectance	See 140.10.2.2	dB
Allocation for penalties ^b (for max TDECQ)	2.6	dB
Additional insertion loss allowed	0	dB

→ 5.6 dB as in 400GBASE-DR4

→ 2.6 dB as in 400GBASE-DR4

Summary

- Proposed to use the total of link loss and MPI penalty in 100GBASE-DR link budget consideration, which would
 - resolve the issue of additional ~ 0.2 dB MPI penalty in the link budget,
 - support more usage cases, and
 - keep the same Tx OMA(min) and Rx sensitivity specifications as those in 400GBASE-DR4.
- Calculated optical return loss for 100GBASE-DR usage cases which gave 16.1 dB as the worst case ORL.
- Recommended changes in 100GBASE-DR link budget.