

Component Vendor Perspective on PMDs for 100G/ λ

IEEE802.3 Interim Meeting

P802.3cd task force

14 September 2016

Fort Worth

kimber_3cd_01A_0916.pdf

Authors

- Mark Kimber – Semtech
- Atul Gupta - Macom

Supporters

- Sudeep Bhoja – Inphi
- Kohichi Tomura – Oclaro
- Sacha Corbeil – Lumentum
- Michael Vitic – Lumentum
- David lewis – Lumentum
- Ed Ulrichs – Source Photonics
- Hai-Feng Liu – Intel
- Hanan Leizerovich – Multiphy
- Bharat Tailor - Semtech

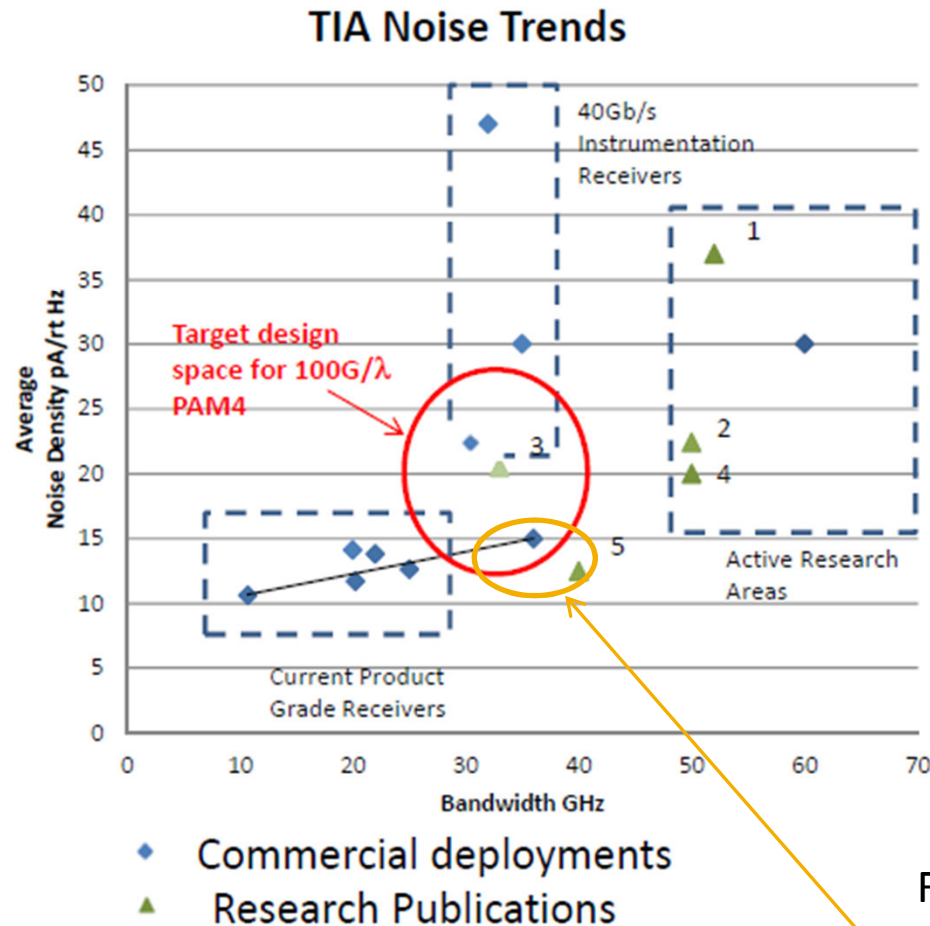
Background

- Availability of PMD components critical to deployment of 100G/ λ modules and systems
 - TIAs
 - EML Drivers
 - MZ Drivers
- Earlier generation components had performance but had high power dissipation, larger size and higher noise

TIAAs

- Key parameter requirements for link budgets
 - Typically 4k transimpedance
 - Typically 30-35GHz bandwidth (with PD)
 - Typically 15pA/√Hz noise
 - Typically 2mA max input current
 - Low power dissipation to enable

TIA



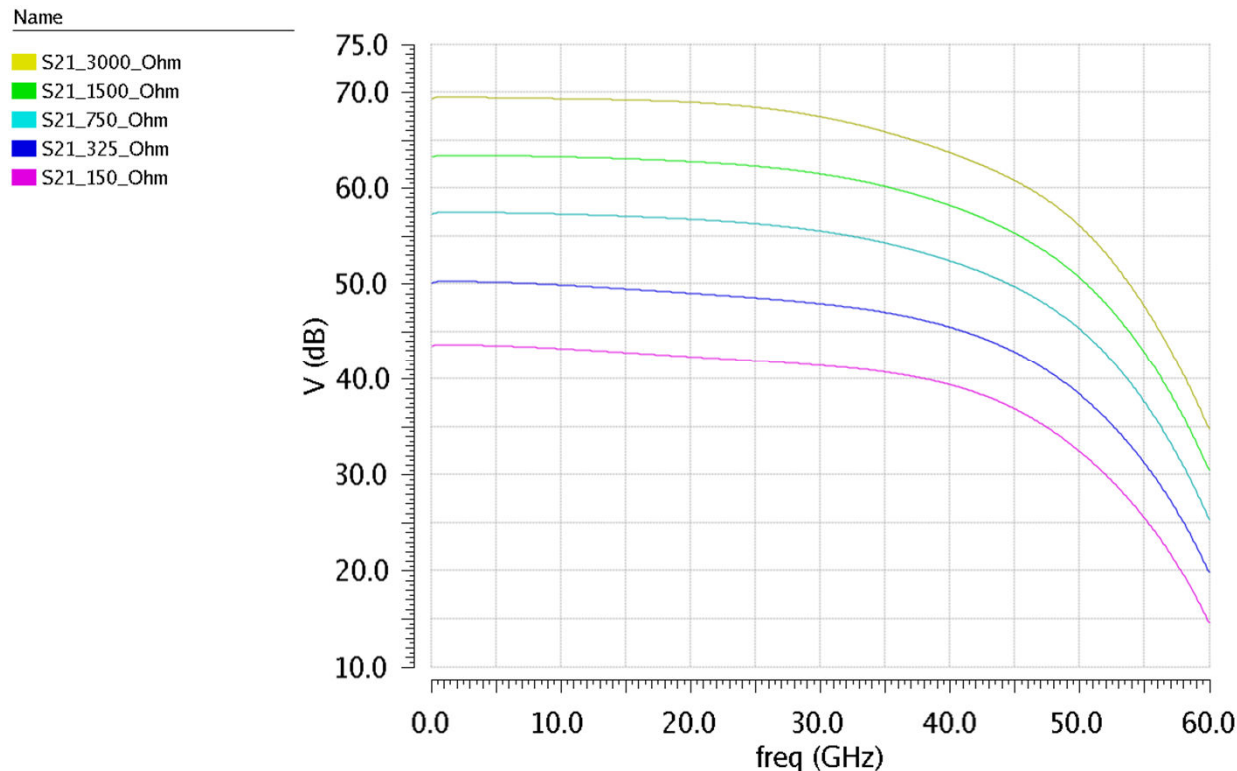
1. Monolithic Photoreceivers for 60 Gbits/s and Beyond
H.-G. Bach
OFC 2003 ThZ1
2. A 50-Gb/s Differential Transimpedance Amplifier in 65nm CMOS Technology
Sang Gyun Kim, Seung Hwan Jung, Yun Seong
Taiwan Asian Solid-State Circuits Conference
IEEE November 10 - 12, 2014/Kaohsiung,
3. A 40-Gb/s Optical Transceiver Front-End in 45 nm SOI CMOS
Joohwa Kim, and James F. Buckwalter
IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 47, NO. 3, MARCH 2012
4. SiGe Differential Transimpedance Amplifier With 50-GHz Bandwidth
Joseph S. Weiner, Andreas Leven, Vincent Houtsma, Yves Baeyens, Young-Kai Chen, Peter Paschke, Yang Yang, John Frackowiak, Wei-Jer Sung, Alaric Tate, Roberto Reyes, Rose F. Kopf, and Nils G. Weimann
IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 38, NO. 9, SEPTEMBER 2003
5. A 40-GHz Bandwidth Transimpedance Amplifier with Adjustable Gain-Peaking in 65-nm CMOS
Ran Ding, y Zhe Xuan, Tom Baehr-Jones, Michael Hochberg
2014 IEEE 57th International Midwest Symposium on Circuits and Systems (MWSCAS),

From tipper_01a_3bs_0315

Current (2016) product developments

Current simulated performance

S21_3000_Ohm:S21_1500_Ohm:S21_750_Ohm:S21_325_Ohm:S21_ Thu Jun 2 10:53:20 2016 1



PIN capacitance = 70fF

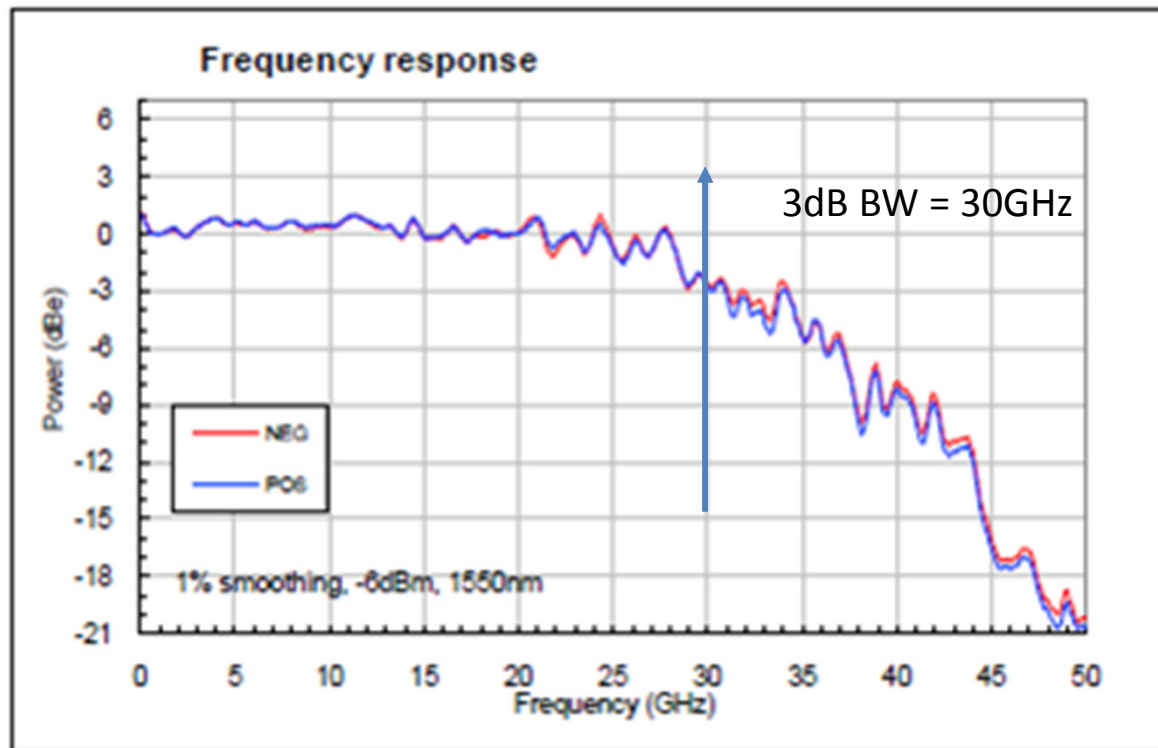
Transit time = 14ps

Input bondwire loop inductance (including PIN parasitics)= 220pH

Output bondwire inductances = 200pH

Semtech

Current TIA measured performance: Bandwidth



MACOM

Current TIA measured performance

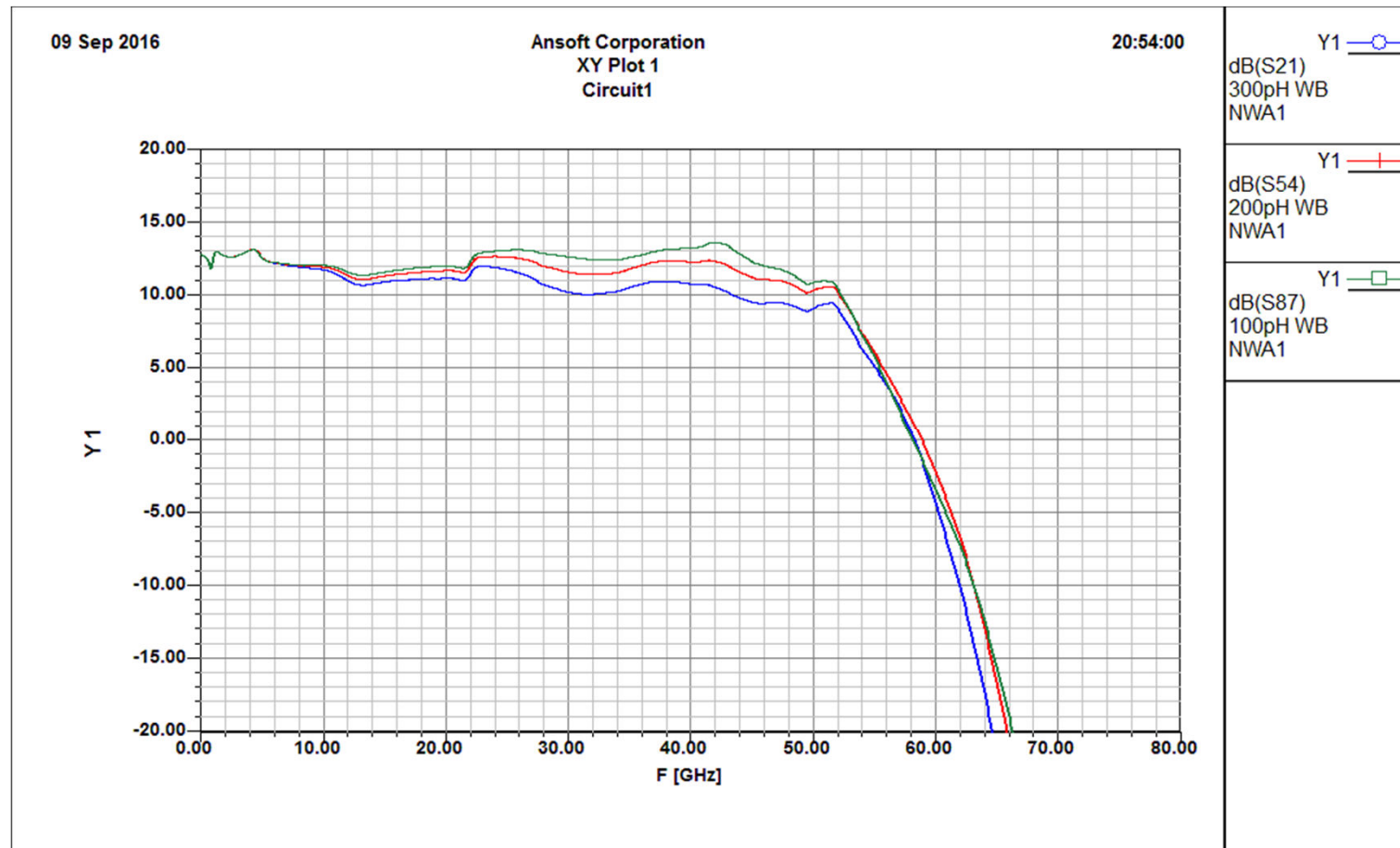
Noise

- Differential output referred PD+TIA noise which includes loading of the PD and any dark current noise of the PD = 6.4mVrms
- TIA gain = 2.5K ohm
- TIA BW = 30GHz
- Input referred noise =
$$6.4\text{E-}3 / (2.5\text{k} * \text{sqrt}(30\text{G}))$$
$$= 14.78\text{pA/sqrt(Hz)}$$

EML Drivers – Single-ended

- Key parameter requirements to enable link budgets
 - Typically 10-15dB gain
 - Typically 30-35GHz bandwidth
 - Typically <10dB noise figure
 - Typically <5% THD
 - Low power dissipation for 2VppSE output swing

Current simulated EML Driver performance

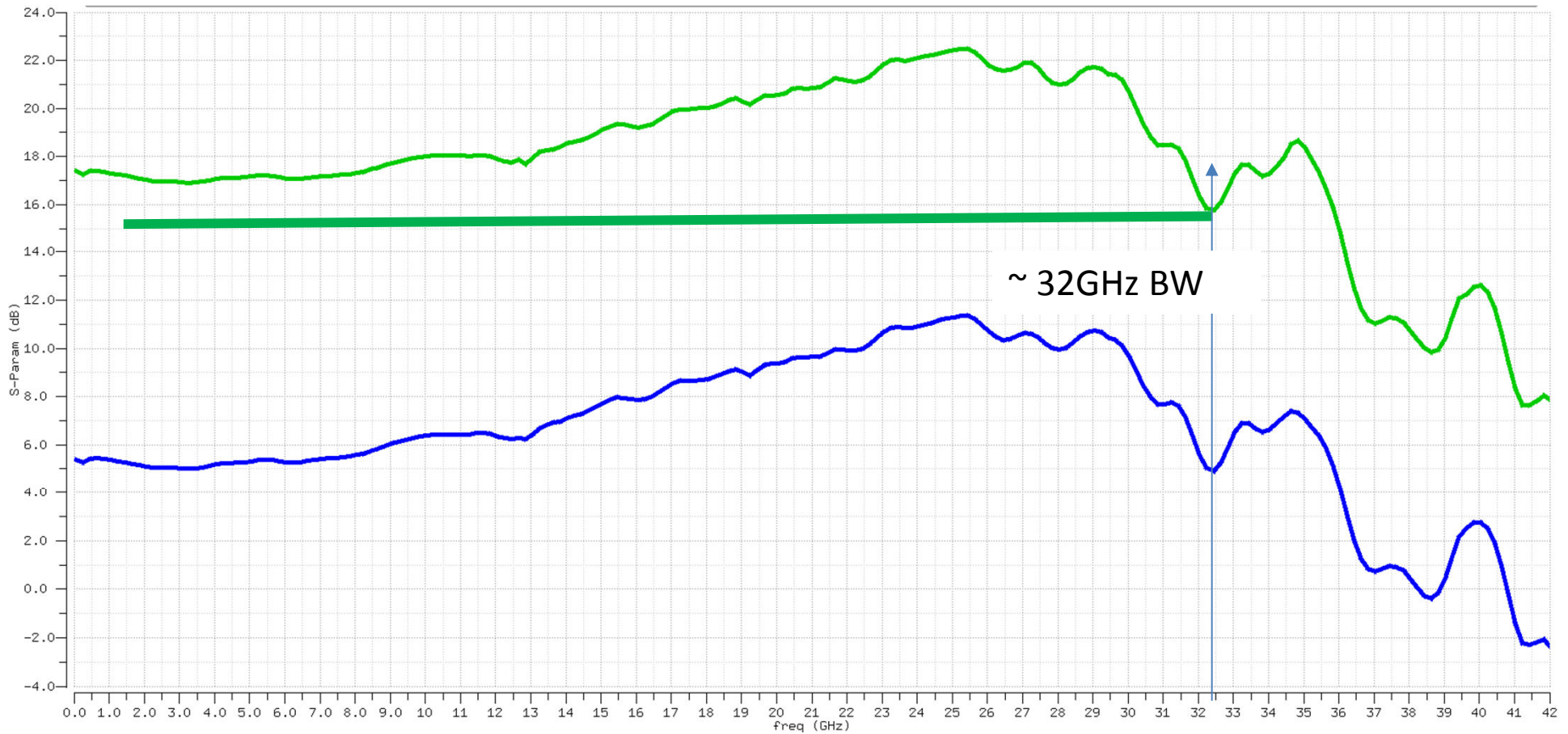


Semtech

MZ Drivers – Differential

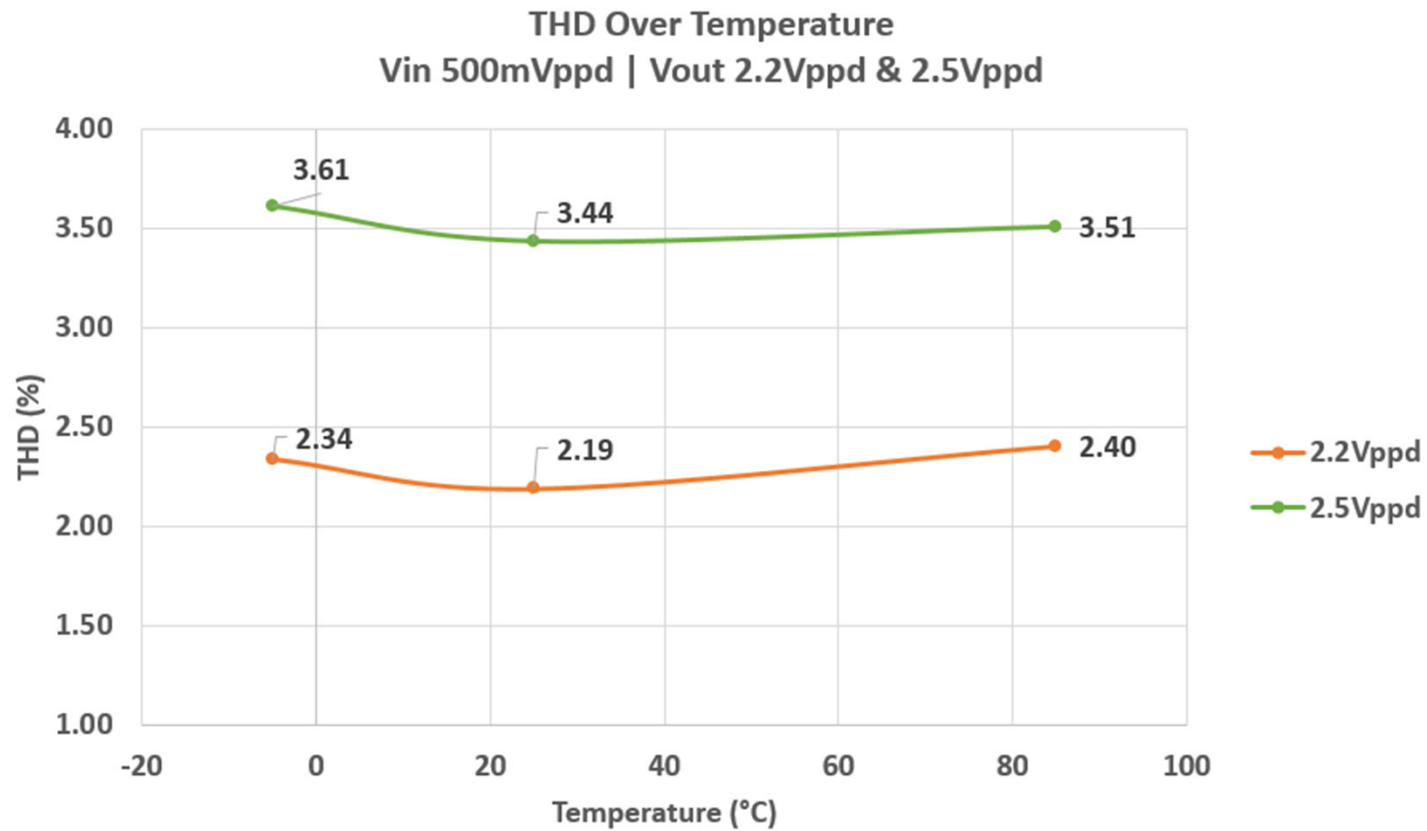
- Key parameter requirements to enable link budgets
 - Typically 10-15dB gain
 - Typically 30-35GHz bandwidth
 - Typically <10dB noise figure
 - Typically <5% THD
 - Low power dissipation for 2Vppd output swing

Current MZ Driver measured performance: BW



MACOM

Linearity Measurement of MZ Driver

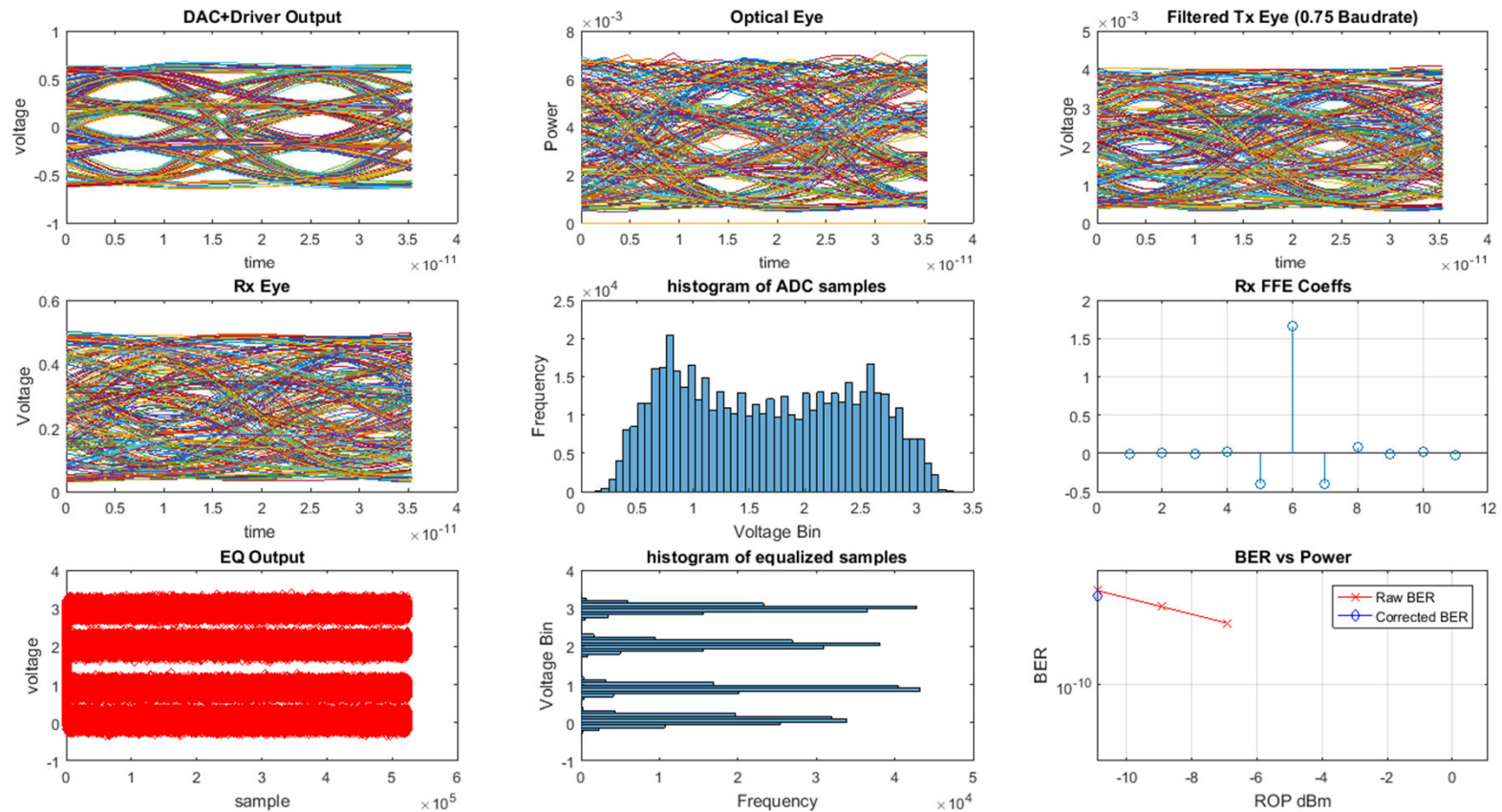


MACOM

Simulated DR Link

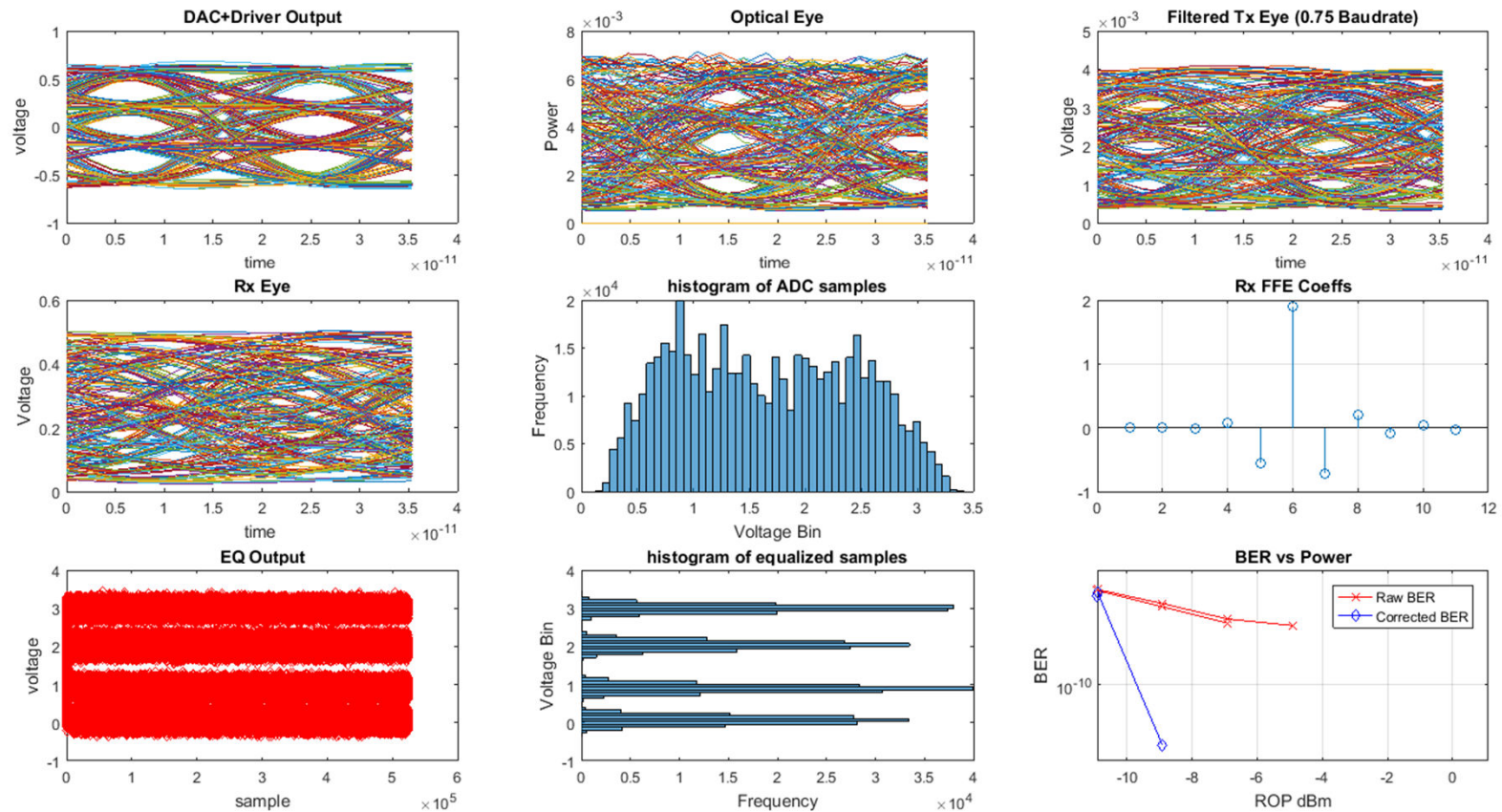
- 53GBd PAM4 (Gray coded) over 500m SMF
- -26dB ORL for Tx, Rx, -45dB ORL for connectors
- 30GHz ADC and DAC bandwidths modelled
- Simulated EML response with level offsetting
- ~7-8dB Tx ER
- 3dB Tx CTLE
- -142dB/Hz RIN
- Simulated Rx response + 3% THD
- 0.6A/W coupled responsivity
- 20pA/rtHz Rx noise
- -40dB electrical crosstalk
- Simple interconnect included
- 11 tap FFE equaliser

Simulated DR Link Performance



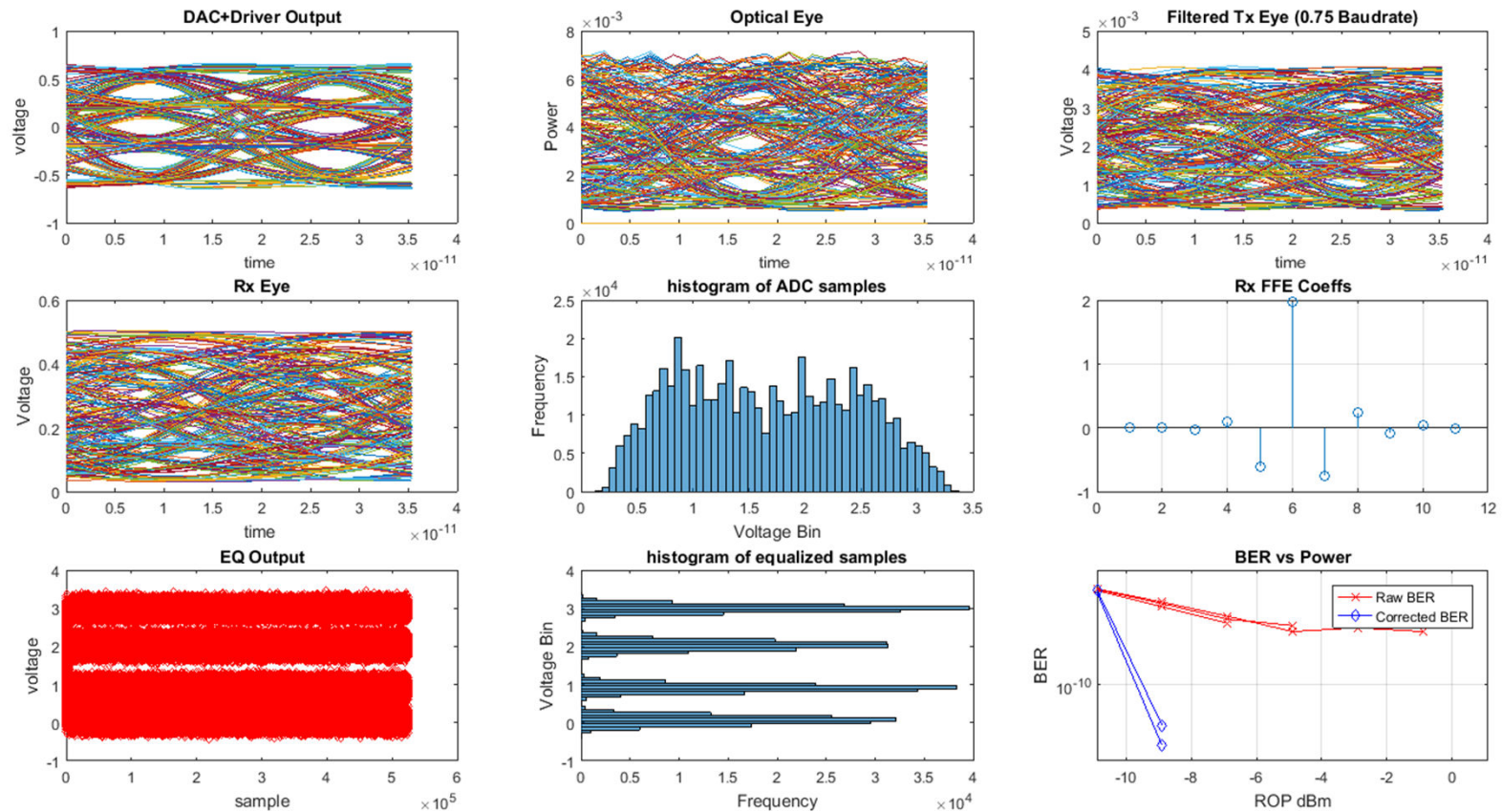
35GHz Ideal Driver, 30GHz Ideal Rx

Simulated DR Link Performance



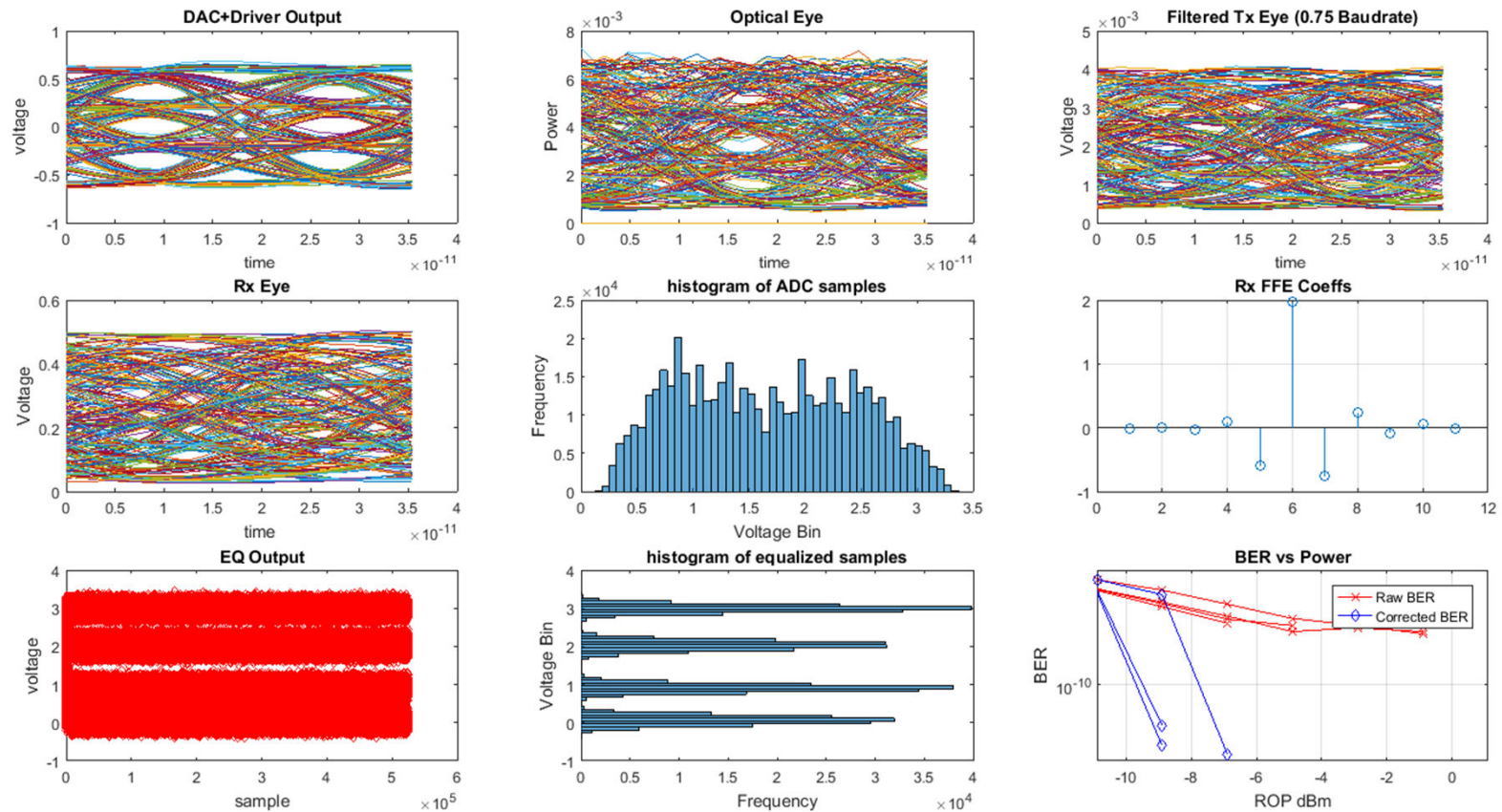
35GHz Ideal Driver, 26GHz simulated wc Rx (slow)

Simulated DR Link Performance



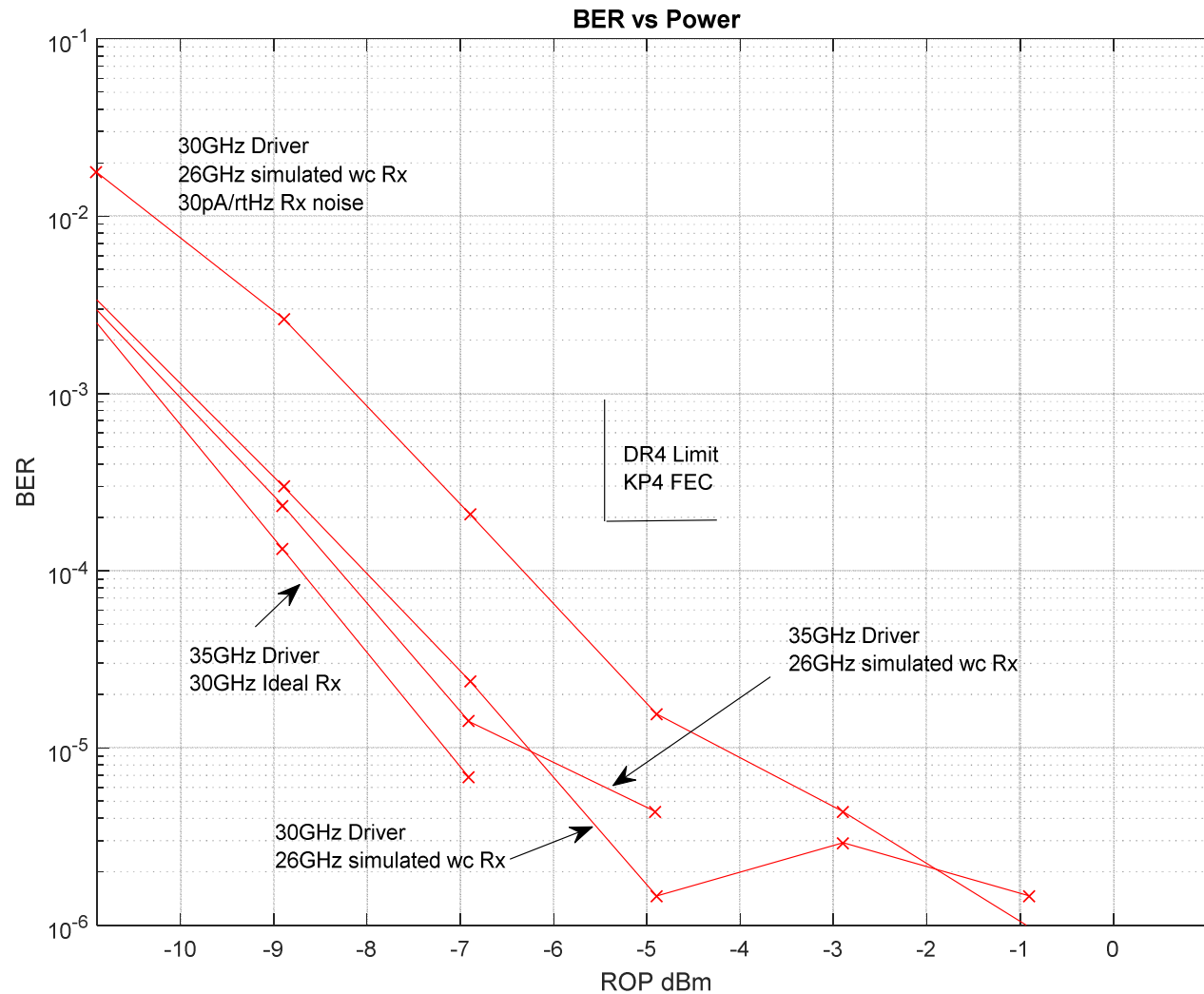
30GHz Ideal Driver, 26GHz simulated wc Rx (slow)

Simulated DR Link Performance



30GHz Ideal Driver, 26GHz simulated wc Rx (slow), 30pA/rtHz

Simulated DR Link Performance



Expected availability

- Several vendors indicate product availability within the next 6 months
 - Some companies have prototypes available

Summary

- PMD performance to support the link budgets for 100G/ λ are in development
 - Some prototypes are already available
- PMD component availability to support the link budgets for 100G/ λ is expected within 6 months
- Multiple vendors supporting this