

Agenda for the P802.3cd TF ad hoc meeting, October 26, 2016

P802.3cd Ethernet TF

Meeting

- Attendance
 - Attendee names and affiliations for these ad hoc meetings will be taken from the WebEx participants list.
 - Please ensure that your full name and employer/affiliation are indicated correctly.
 - If the WebEx participants list does not correctly indicate your name and employer/affiliation, please send an e-mail to the ad hoc recording secretary:
Kent Lusted (kent dot c dot lusted at intel dot com).
- WebEx
 - Please mute your lines if you are not speaking
 - *6 to mute/unmute or click on mute button in the WebEx window
 - Noisy, unmuted lines will be muted by the WebEx organizer
 - *6 to unmute if this happens to you
- Participants wishing to contact the presenter can contact the Chair for their contact info

P802.3cd Task Force ad hoc communications

- The TF ad hoc web page is here:
 - <http://www.ieee802.org/3/cd/public/adhoc/index.html>
 - Includes ad hoc meeting schedule and WebEx information.
- All e-mail communications are via the TF reflector
 - archive and subscription links are here:
<http://www.ieee802.org/3/cd/reflector.html>

Proposed agenda

- Approval of the Agenda
- Approval of the October 19 minutes
- IEEE patent policy reminder:
 - <http://www.ieee802.org/3/patent.html>
- P802.3cd TF Ad Hoc –
 - Task Force Update, Kent Lusted (10 min)
 - “100GBASE-DR Link Budget Considerations”, Hai-Feng Liu (20 mins)
 - “FEC based AM unlock”, Mark Gustlin (20 mins)
 - “PMD BER requirements”, Pete Anslow (45 mins)
 - “Effect of Package Trace and Termination Resistance on Tx Specification”, Yasuo Hidaka (20 mins)