



## **Can ERL replace SNRisi for 50GBASE-CR?**

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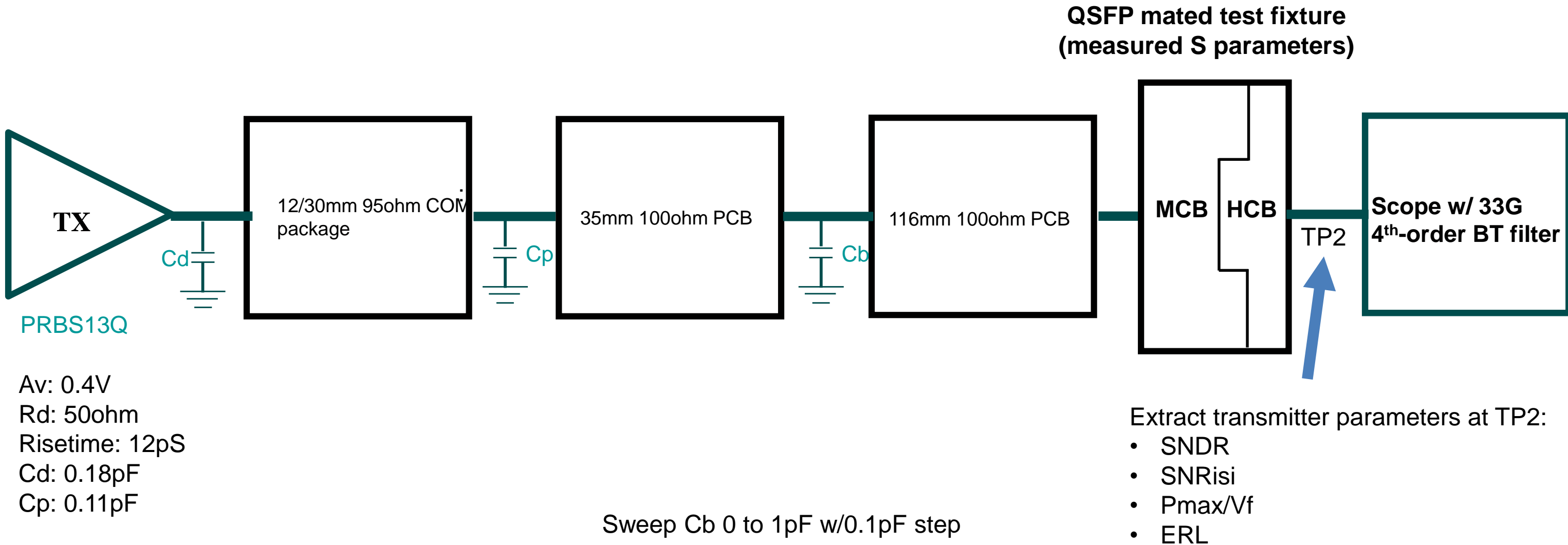
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- **It has been suggested that ERL could replace both Return loss specifications and SNRisi.**
- **The replacement of Return loss would appear to be a significant improvement as this measurement takes into account the time domain effects of reflections, the effects of the DFE equalizer and allows for removing some of the effects of the test fixtures. The Frequency domain masks of the existing return loss specifications are a very blunt instrument.**
- **The suggested replacement of SNRisi is a different matter as SNRisi is already a time domain measurement and includes the effect of the DFE equalizer. Also it is measuring the through response of multiple reflections which is what mainly matters to the receiver whereas ERL measures the return response.**
- **This presentation investigates the correlation between SNRisi, ERL and system performance as measured by COM for 50GBASE-CR.**

- **TX parameters including ERL are simulated at TP2 for the long package and short package with a 100 Ohm standard host trace as now used in the Cable COM calculation.**
- **With the short package these Tx parameters were re-simulated while sweeping Cb, a capacitor added part way along the host board trace. This represents one particular potential host system impairment.**
- **The predicted system performance as a function of Cb was simulated for a representative cable by running COM for 50GBASE-CR while using the same host Tx as simulated above. The Rx used is the standard COM receiver configuration with the long package.**
- **Some additional host configurations were investigated to see what ERL, SNR<sub>isi</sub> and COM they created. In particular ones with the host PCB much shorter.**

# Transmitter parameters at TP2



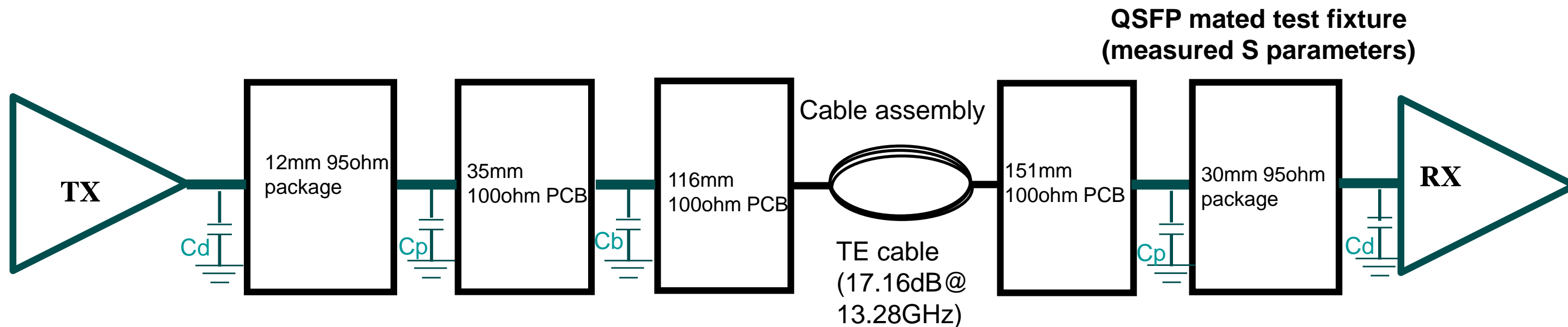
# TX parameters vs. Cb

TX package length(mm)	Cb(pF)	R <sub>lm</sub>	V <sub>f</sub> (V)	P <sub>max</sub> (V)	P <sub>max</sub> /V <sub>f</sub>	SNR <sub>isi</sub>	SNDR(TX_SNR =32.5dB)(dB)	ERL22(dB)
30	0	0.997	0.36	0.174	0.484	32.229	32.498	11.731
12	0	0.997	0.368	0.188	0.512	34.273	32.498	11.611
12	0.1	0.997	0.369	0.187	0.508	32.756	32.498	11.28
12	0.2	0.997	0.37	0.184	0.498	30.129	32.498	10.458
12	0.3	0.996	0.37	0.179	0.484	28.091	32.498	9.635
12	0.4	0.996	0.371	0.174	0.469	26.393	32.498	8.93
12	0.5	0.996	0.371	0.168	0.454	25.32	32.498	8.309
12	0.6	0.996	0.371	0.163	0.438	24.606	32.497	7.8
12	0.7	0.996	0.372	0.157	0.423	24.083	32.497	7.379
12	0.8	0.995	0.372	0.152	0.408	23.671	32.497	7.017

Notes:

- Cb is only significantly affecting P<sub>max</sub> (and P<sub>max</sub>/V<sub>f</sub>), SNR<sub>isi</sub> and ERL.
- For Cb<=0.3pF the configurations pass all specs except SNR<sub>isi</sub> and a very marginal fail for P<sub>max</sub>/V<sub>f</sub> that matches the marginal fail for the COM Tx.
- ERL also passes the recommended 9dB for Cb<=0.3pF
- SNR<sub>isi</sub> fails for Cb>0.1pF





$A_v$ : 0.415V  
 $A_{fe}$ : 0.415V  
 $A_{ne}$ : 0.604V  
 $R_d$ : 50ohm  
 $Z_{c\_pkg}$ =95ohm  
 $Z_{c\_brd}$  =100ohm

Thru channel includes  $C_b$  on TX host trace. XTALK channels don't include  $C_b$ .  
 Run COM by sweeping  $C_b$  0 to 0.7pF w/0.1pF step  
 Other parameters refer to table 136-15  
 COM revision: 221

# COM spreadsheet

Table 93A-1 parameters				I/O control			Table 93A-3 parameters		
Parameter	Setting	Units	Information				Parameter	Setting	Units
f_b	26.5625	GBd		DIAGNOSTICS	1	logical	package_tl_gamma0_a1_a2	[0 1.734e-3 1.455e-4]	
f_min	0.05	GHz		DISPLAY_WINDOW	0	logical	package_tl_tau	6.141E-03	ns/mm
Delta_f	0.01	GHz		Display frequency domain	1	logical	package_Z_c	95	Ohm (tdr sel)
C_d	[1.8e-4 1.8e-4]	nF	[TX RX]	CSV_REPORT	1	logical	Table 92-12 parameters		
z_p select	[ 1 ]		[test cases to run]	RESULT_DIR	.\results\CR_50G_{date}\		Parameter	Setting	
z_p (TX)	[12 30]	mm	[test cases]	SAVE_FIGURES	0	logical	board_tl_gamma0_a1_a2	[0 4.114e-4 2.547e-4]	
z_p (NEXT)	[12 12]	mm	[test cases]	Port Order	[1 3 2 4]		board_tl_tau	6.191E-03	ns/mm
z_p (FEXT)	[12 30]	mm	[test cases]	RUNTAG	CR_50G_PAM4		board_Z_c	100	Ohm
z_p (RX)	[30]	mm	[test cases]	Receiver testing			z_bp (TX)	151	mm
C_p	[1.1e-4 1.1e-4]	nF	[TX RX]	RX_CALIBRATION	0	logical	z_bp (NEXT)	110	mm
R_0	50	Ohm		Sigma BBN step	5.00E-03	V	z_bp (FEXT)	110	mm
R_d	[ 50 50 ]	Ohm	[TX RX] or selected	IDEAL_TX_TERM	0	logical	z_bp (RX)	151	mm
f_r	0.75	*fb		T_r	0.012	ns			
c(0)	0.6		min	FORCE_TR	1	logical			
c(-1)	[-0.25:0.05:0]		[min:step:max]	Non standard control options					
c(-2)	[0:0.025:0.1]		[min:step:max]	COM_CONTRIBUTION	0	logical			
c(1)	[-0.25:0.05:0]		[min:step:max]	TDR	1	logical			
g_DC	[-20:1:0]	dB	[min:step:max]	ERL	1	logical			
f_z	10.625	GHz		Z_t	50	ohms			
f_p1	10.625	GHz		ERL_ONLY	0	logical			
f_p2	53.125	GHz		TR_TDR	0.0189	ns			
A_v	0.415	V	tdr selected	TDR_duration	10				
A_fe	0.415	V	tdr selected	TDR_f_BT_3db	19.921875	GHz			
A_ne	0.604	V	tdr selected	TDR_Butterworth	1	logical			
L	4			beta_x	10700000000				
M	32			rho_x	0.44				
N_b	12	UI		fixture delay time	1.10E-09		set to zero for no fixture. For a CR cable this is determined outside of this program		
b_max(1)	0.7			Grr_limit	0				
b_max(2..N_b)	0.2			ERL_FOM	0				
sigma_RJ	0.01	UI							
A_DD	0.02	UI							
eta_0	1.64E-08	V <sup>2</sup> /GHz							
SNR_TX	32.5	dB	tdr selected						
R_LM	0.95								
DER_0	1.00E-04								
Operational control									
COM Pass threshold	3	dB							
Include PCB	1	Value	0, 1, 2						
g_DC_HP	[-6:1:0]		[min:step:max]						
f_HP_PZ	0.6640625	GHz							

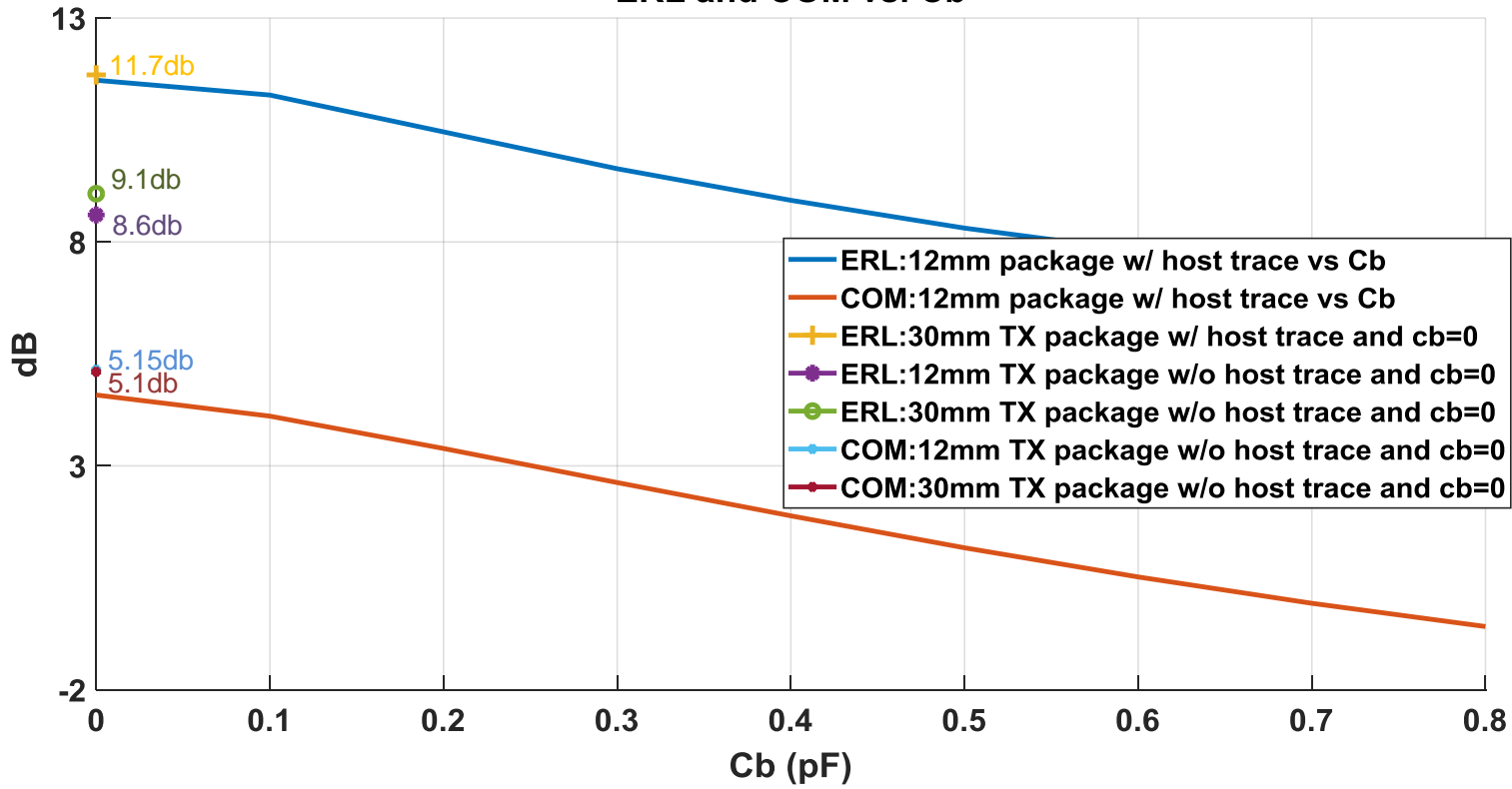
# COM vs. Cb w/ XTALK

TX package length(mm)	Cb(pF)	COM(dB)
30	0	4.194
12	0	4.583
12	0.1	4.11
12	0.2	3.388
12	0.3	2.627
12	0.4	1.884
12	0.5	1.17
12	0.6	0.519
12	0.7	-0.069
12	0.8	-0.588
12	0.9	-1.046
12	1	-1.526

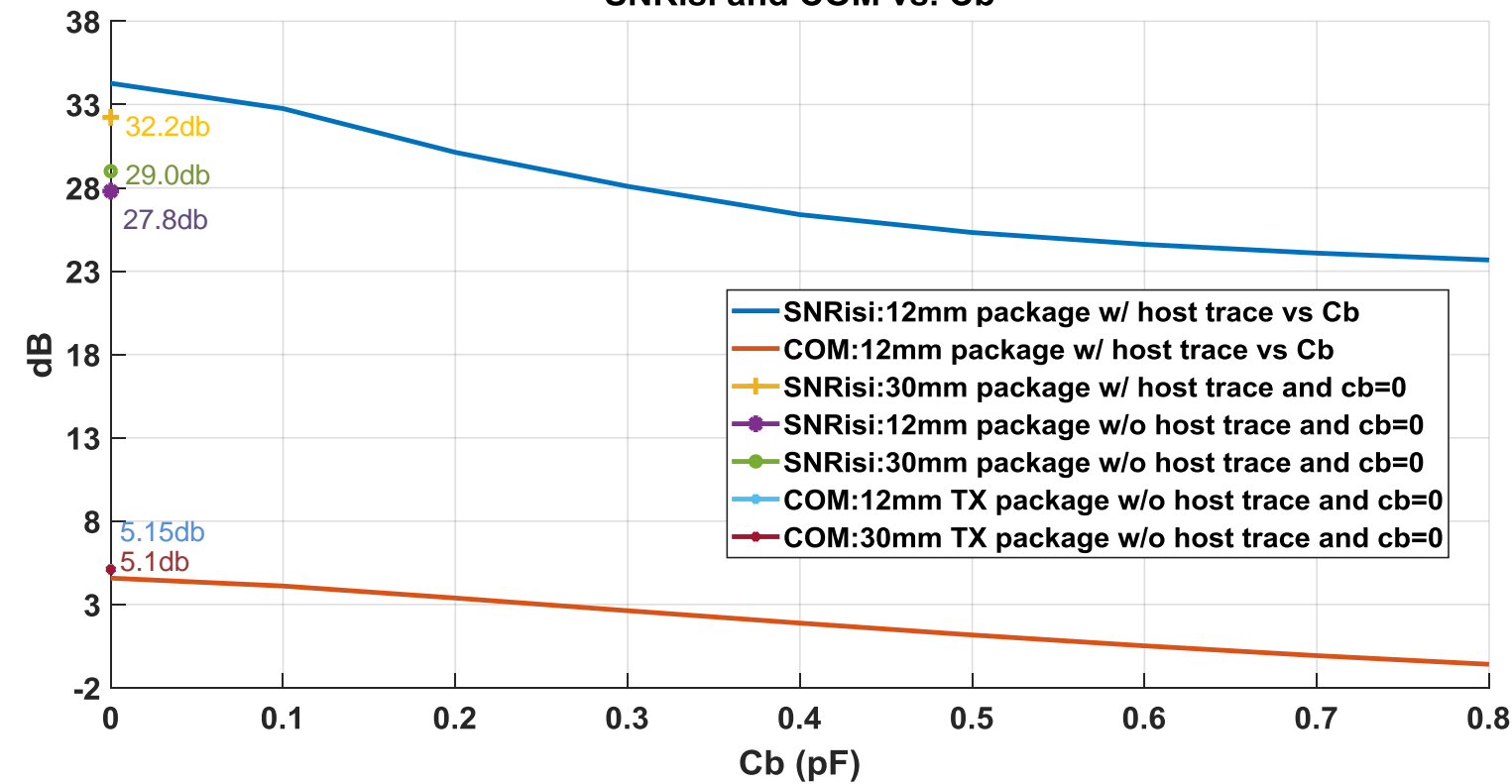


# ERL/SNRisi/COM vs. Cb

ERL and COM vs. Cb



SNRisi and COM vs. Cb



# Conclusions.

- **System performance (as measured by COM) is worse than the Cable test value for  $C_b > 0.1 \text{ pF}$ . It is a definite fail for  $C_b = 0.3 \text{ pF}$**
- **SNR<sub>Risi</sub> is correlating with this system performance and would fail the hosts with  $C_b > 0.1 \text{ pF}$ .**
- **The proposed ERL specification would have to be significantly tightened (to 11dB) to fail these hosts, and this would fail hosts that have good system performance. However SNR<sub>Risi</sub> has some similar but not as bad issues. We definitely have an issue that with our existing parameters including ERL we do not appear to be able to discriminate between good hosts (No additional host PCB giving very good COM) and bad hosts (with  $C_b = 0.3$  that has bad COM)**
- **We should retain the SNR<sub>Risi</sub> specification, and investigate further whether a better metric can be developed.**

