

# ERL with Host Backplane Channels

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2018/02/21

# Summary

- Results for host backplane channels support ERL as a more useful metric than RL.

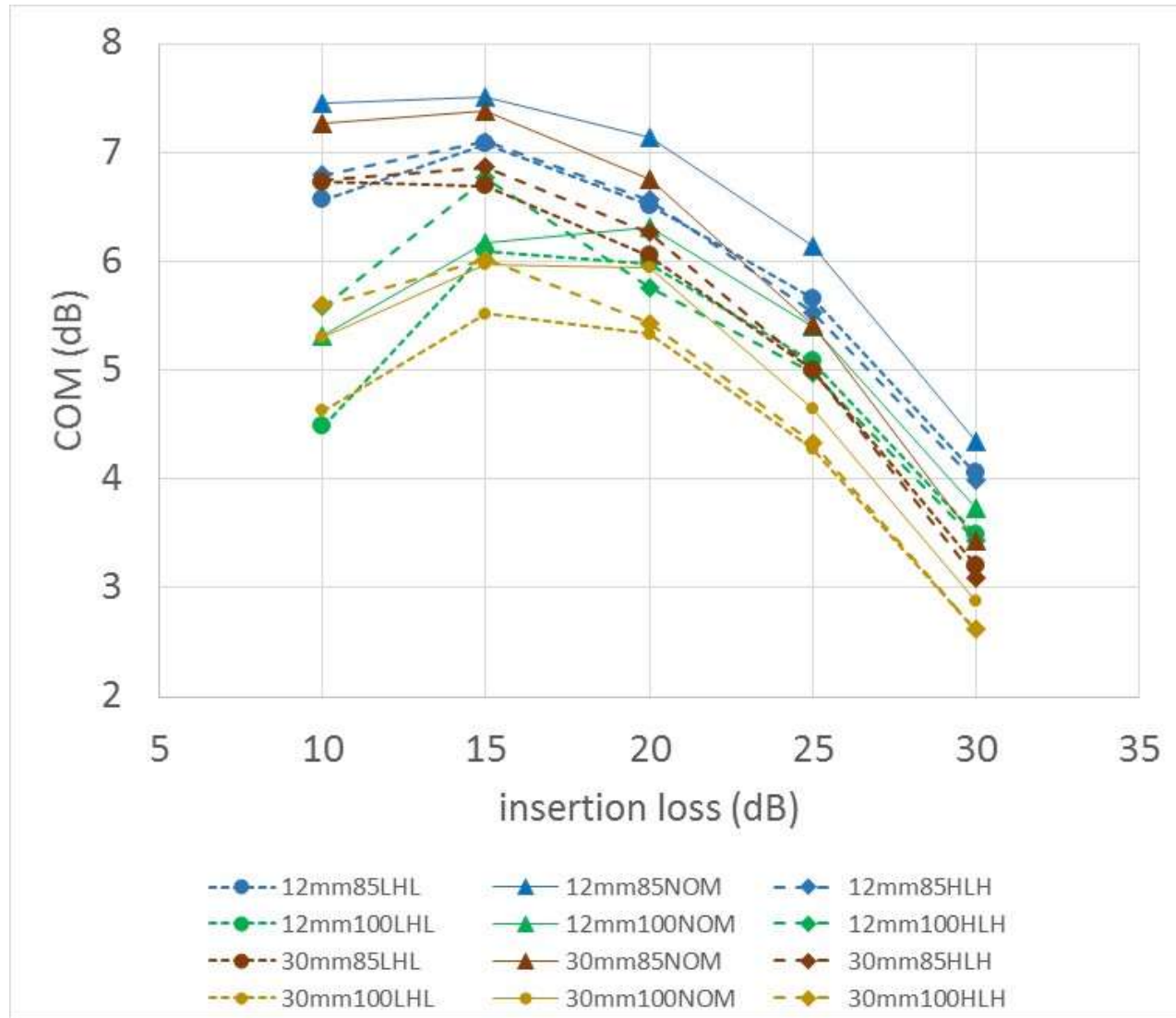
Metric	Result
COM	PASS - all except 30dB, 100ohm, 30mmpkg
ERL	PASS - all
RL	FAIL - all

- Results for the channels studied here support the proposed values for  $\rho_x$ ,  $\beta_x$ , and ERL limit.
  - Probably some leeway here. Recommend studying more channels.

# Channels

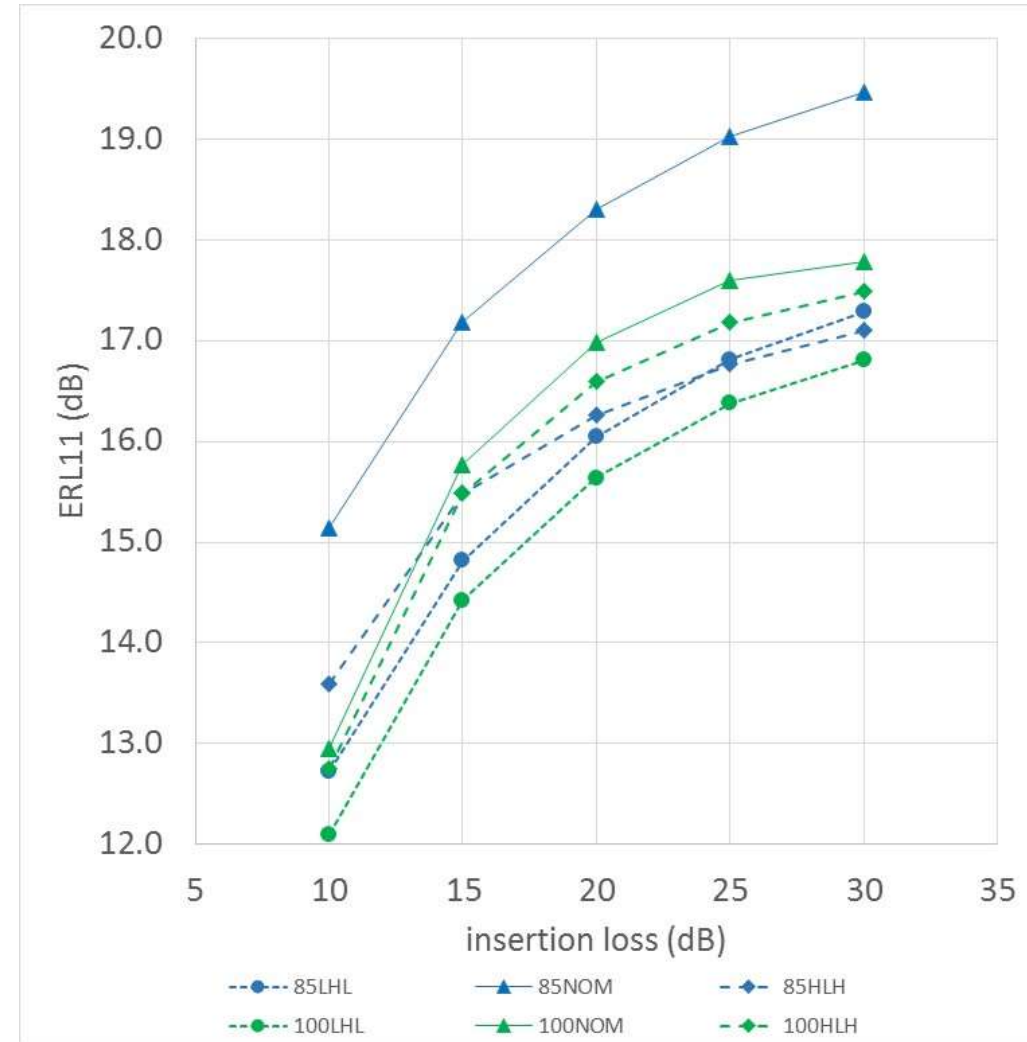
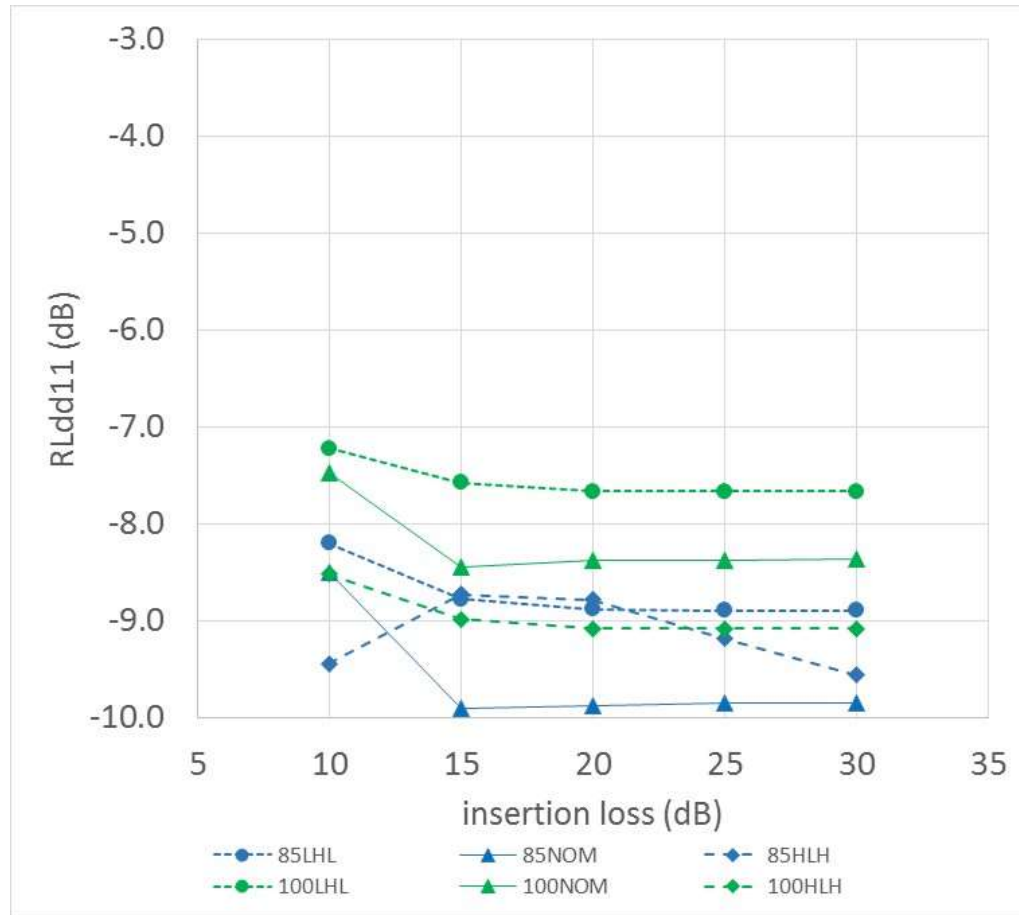
- Files – previously contributed
  - Mellitz\_01\_021716\_10dB\_6\_channels.zip
  - Mellitz\_01\_021716\_15dB\_6\_channels.zip
  - Mellitz\_01\_021716\_20dB\_6\_channels.zip
  - Mellitz\_01\_021716\_25dB\_6\_channels.zip
  - Mellitz\_01\_021716\_30dB\_6\_channels.zip
- 6 channel models for each loss point:
  - Impedance: 85ohm, 100ohm
  - Impedance variation: nominal, low/high/low, high/low/high (compute card/backplane/line card)
- 30 channels total

# COM vs IL



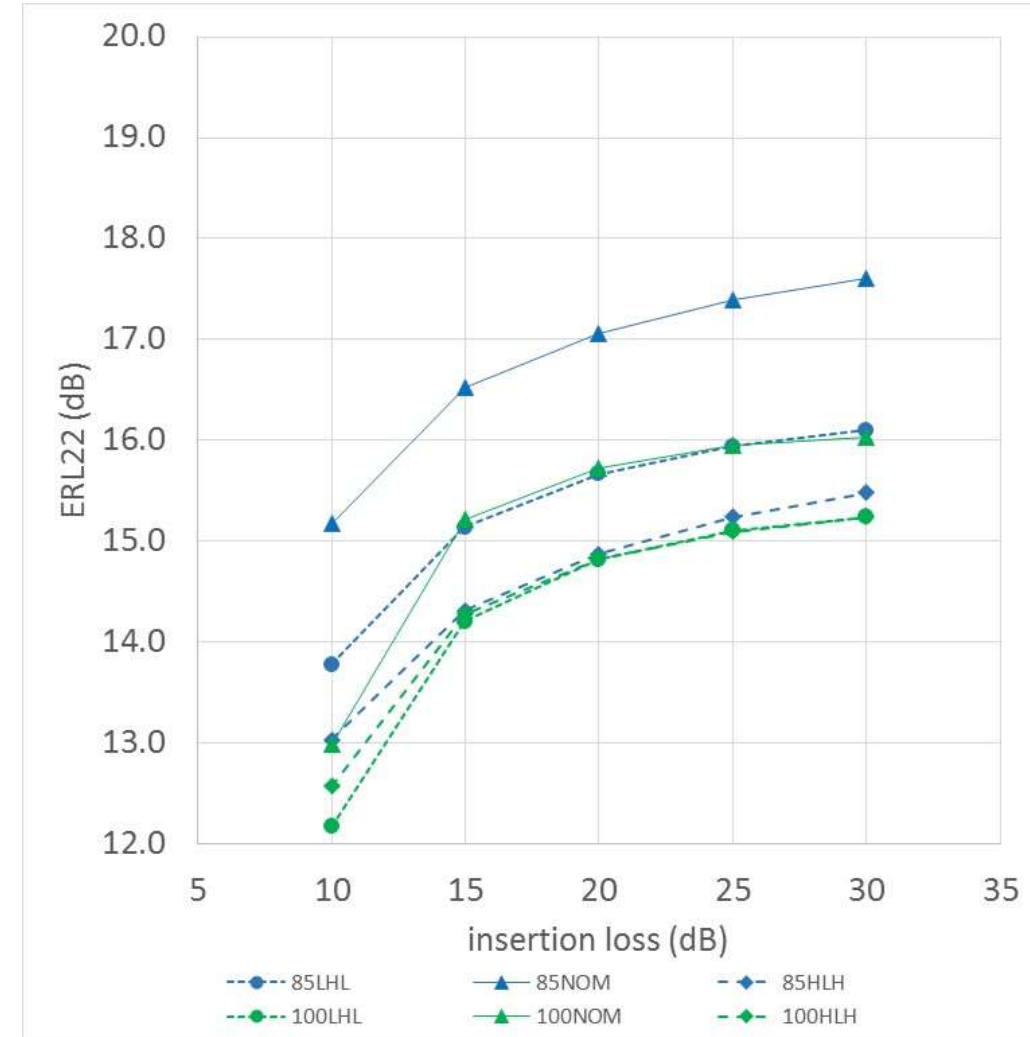
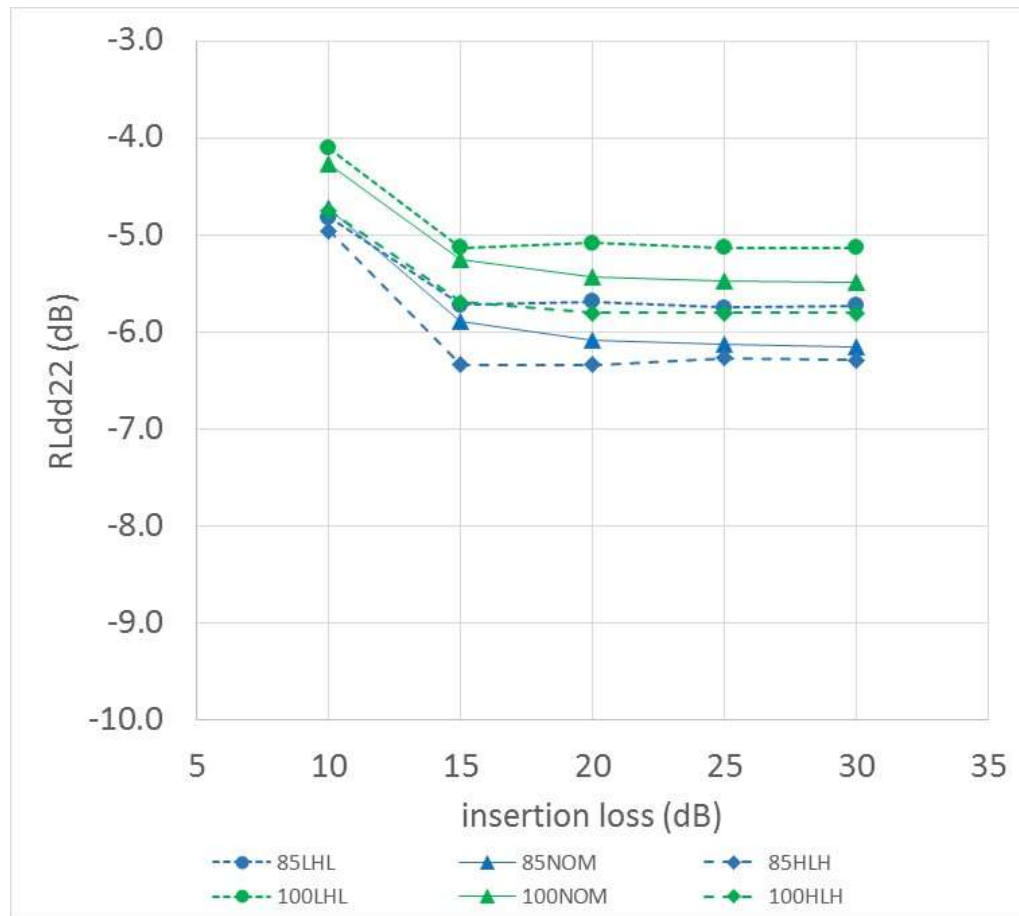
# RL11, ERL11 vs IL

RL is peak value



# RL22, ERL22 vs IL

RL is peak value



# Summary

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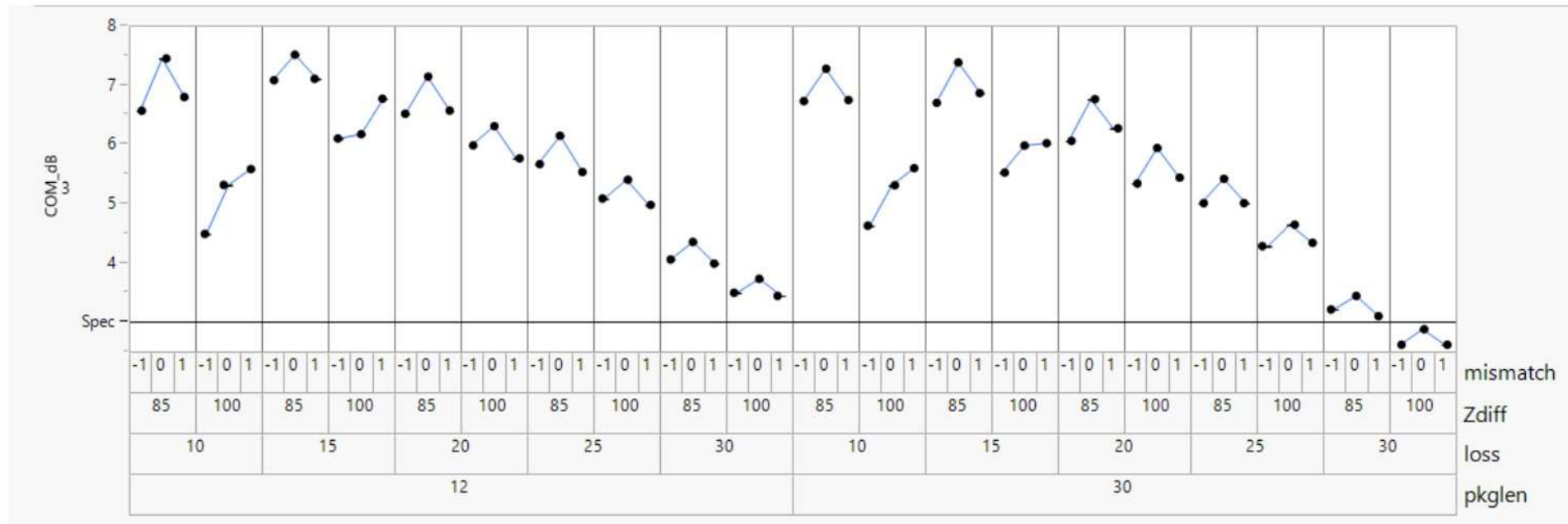
# Additional info



# Simulation Setup

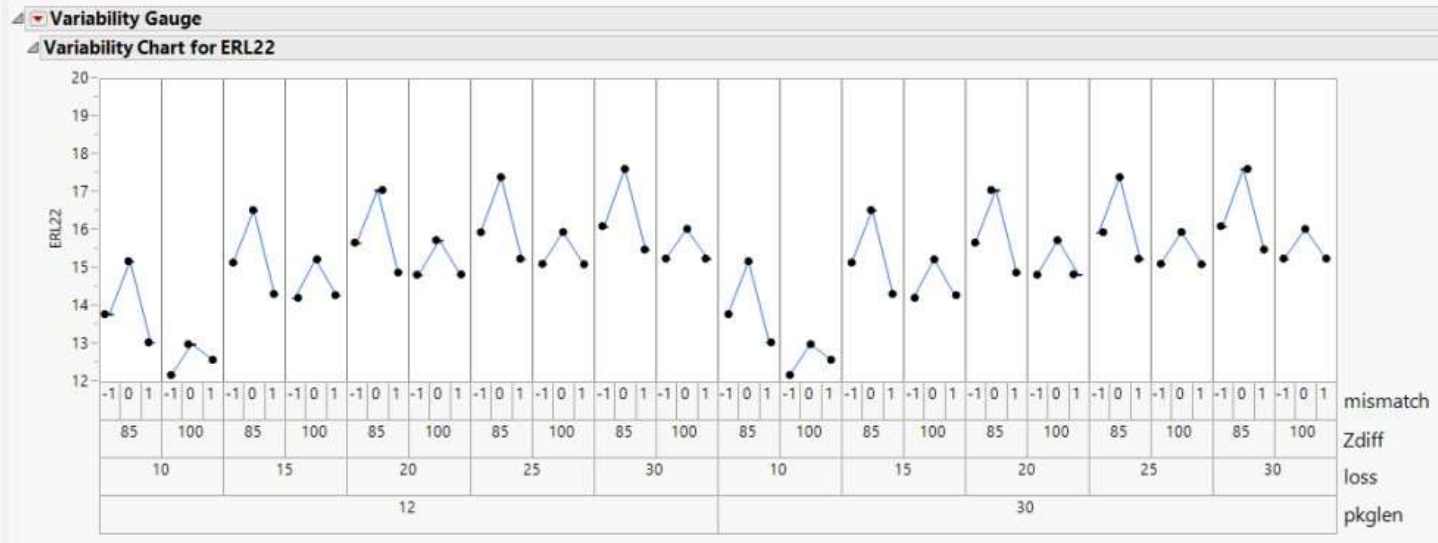
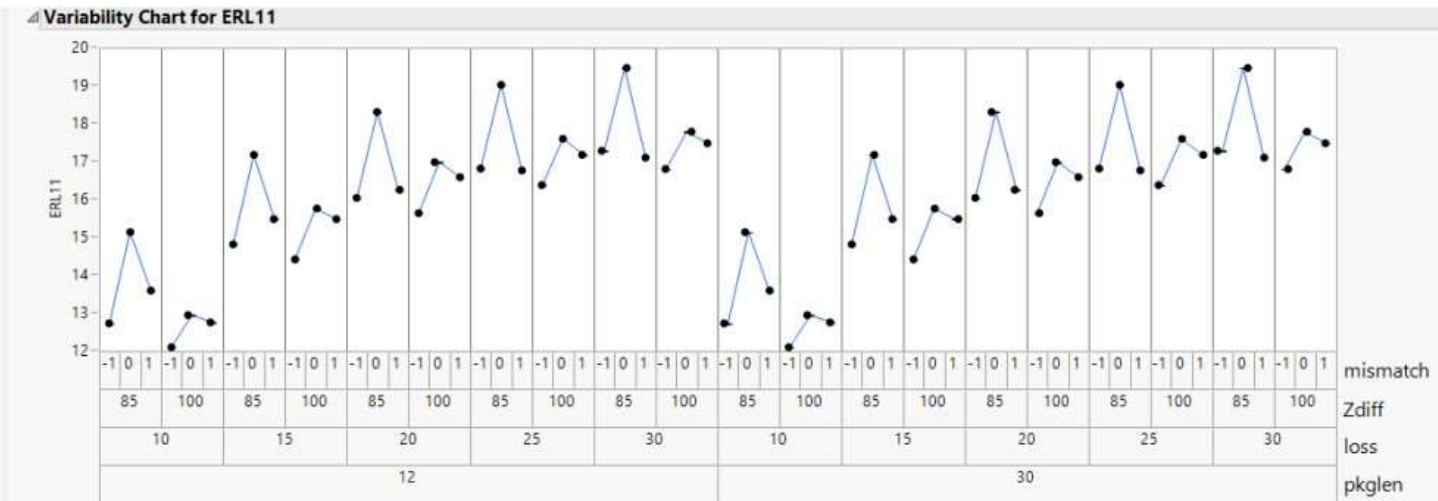
Table 93A-1 parameters				I/O control			Table 93A-3 parameters		
Parameter	Setting	Units	Information	Parameter	Setting	Units	Parameter	Setting	Units
f_b	26.5625	GBd		DIAGNOSTICS	1	logical	package_tl_gamma0_a1_a2	[0 1.734e-3 1.455e-4]	
f_min	0.05	GHz		DISPLAY_WINDOW	0	logical	package_tl_tau	6.141E-03	ns/mm
Delta_f	0.01	GHz		Display frequency domain	1	logical	package_Z_c	95	Ohm (tdr sel)
C_d	[1.8e-4 1.8e-4]	nF	[TX RX]	CSV_REPORT	1	logical	Table 92-12 parameters		
z_p select	[ 1 2 ]		[test cases to run]	RESULT_DIR	.\results\Com2p1\		Parameter	Setting	
z_p (TX)	[12 30]	mm	[test cases]	SAVE_FIGURES	0	logical	board_tl_gamma0_a1_a2	[0 4.114e-4 2.547e-4]	
z_p (NEXT)	[12 12]	mm	[test cases]	Port Order	[1 3 2 4]		board_tl_tau	6.191E-03	ns/mm
z_p (FEXT)	[12 30]	mm	[test cases]	RUNTAG	COM2p1_KR50G		board_Z_c	110	Ohm
z_p (RX)	[12 30]	mm	[test cases]	Receiver testing			z_bp (TX)	151	mm
C_p	[1.1e-4 1.1e-4]	nF	[TX RX]	RX_CALIBRATION	0	logical	z_bp (NEXT)	72	mm
R_0	50	Ohm		Sigma BBN step	5.00E-03	V	z_bp (FEXT)	72	mm
R_d	[ 50 50 ]	Ohm	[TX RX] or selected	IDEAL_TX_TERM	0	logical	z_bp (RX)	151	mm
f_r	0.75	*fb		T_r	0.012	ns			
c(0)	0.6		min	FORCE_TR	1	logical			
c(-1)	[-0.25:0.05:0]		[min:step:max]	Non standard control options					
c(-2)	[0:0.025:0.1]		[min:step:max]	COM_CONTRIBUTION	0	logical			
c(1)	[-0.25:0.05:0]		[min:step:max]	TDR	1	logical			
g_DC	[-20:1:0]	dB	[min:step:max]	ERL	1	logical			
f_z	10.625	GHz		Z_t	50	ohms			
f_p1	10.625	GHz		ERL_ONLY	0	logical			
f_p2	53.125	GHz		TR_TDR	0.0189	ns			
A_v	0.415	V	tdr selected	TDR_duration	10				
A_fe	0.415	V	tdr selected	TDR_f_BT_3db	19.921875	GHz			
A_ne	0.604	V	tdr selected	TDR_Butterworth	1	logical			
L	4			beta_x	10700000000				
M	32			rho_x	0.44				
N_b	12	UI		fixture delay time	0.00E+00				set to zero for no fixture for KR channel
b_max(1)	0.7			Grr_limit	0				
b_max(2..N_b)	0.2			ERL_FOM	0				
sigma_RJ	0.01	UI							
A_DD	0.02	UI							
eta_0	1.64E-08	V^2/GHz							
SNR_TX	32.5	dB	tdr selected						
R_LM	0.95								
DER_0	1.00E-04								
Operational control									
COM Pass threshold	3	dB							
Include PCB	0	Value	0, 1, 2						
g_DC_HP	[-6:1:0]		[min:step:max]						
f_HP_PZ	0.6640625	GHz							

# COM Results



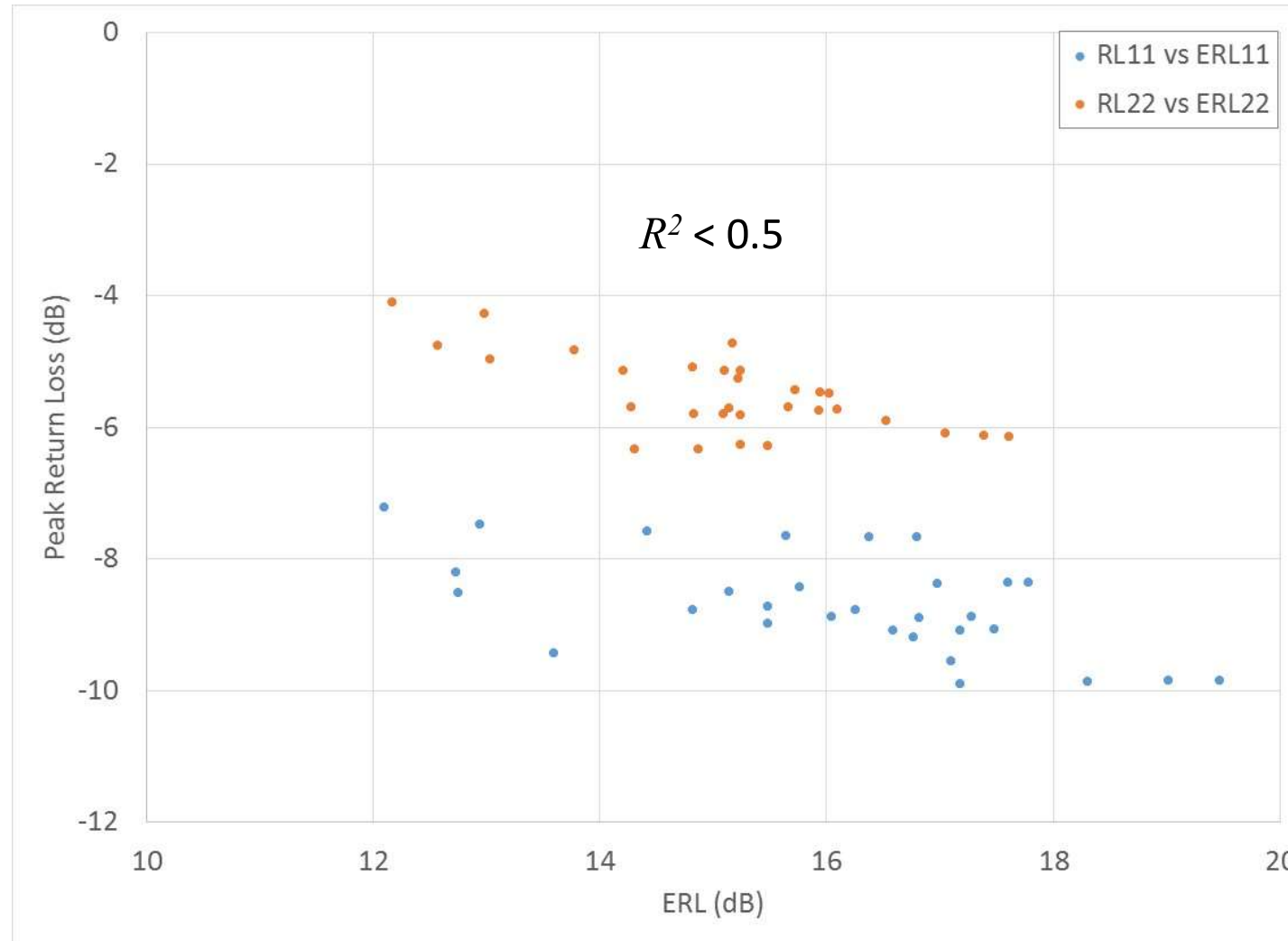
- All channels pass COM except for 30db, 100ohm, 30mm package.
- All channels fail return loss spec.

# ERL Results

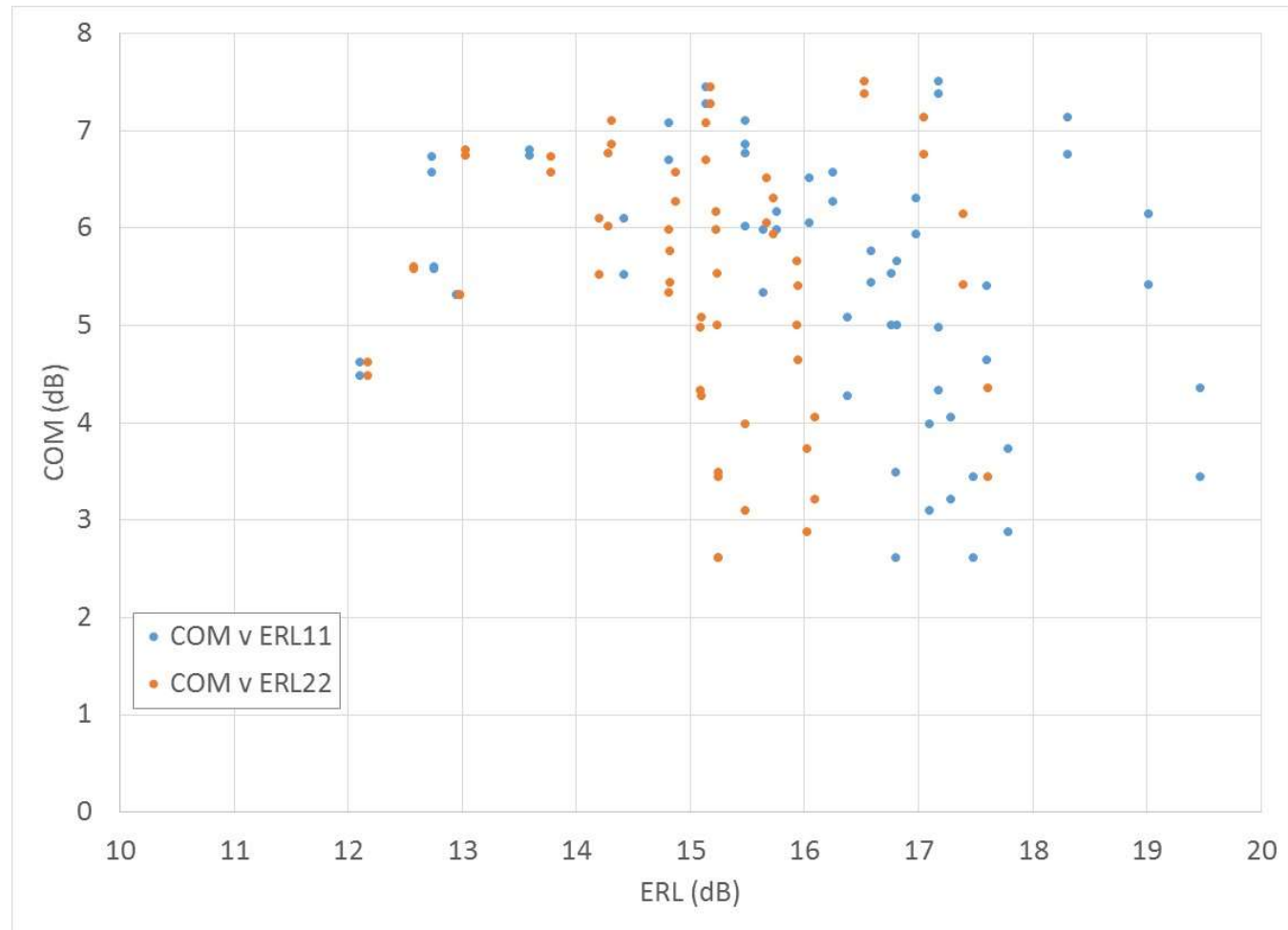


- All channels meet the propose ERL spec.
- ERL increases with loss, as expected.

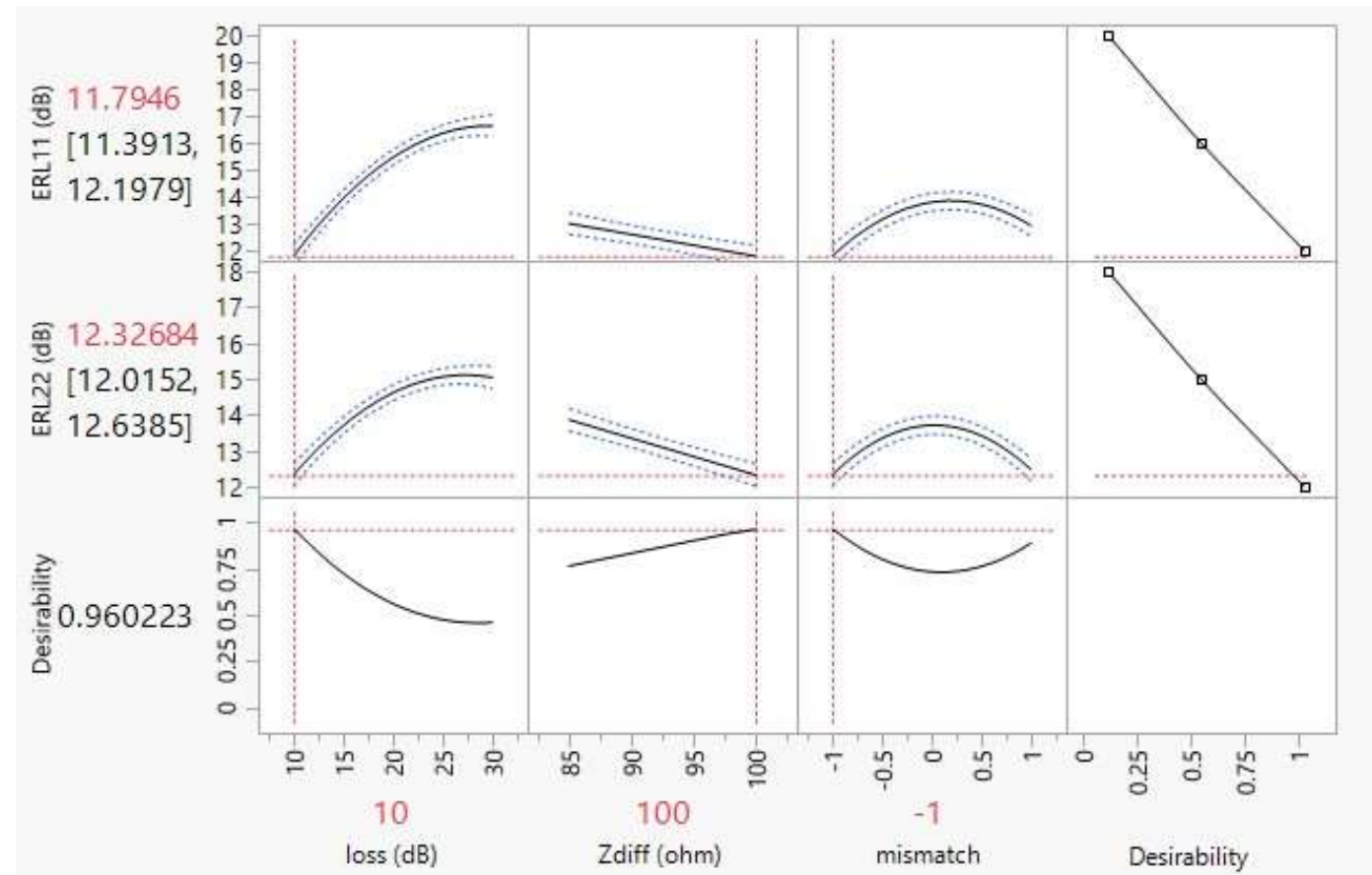
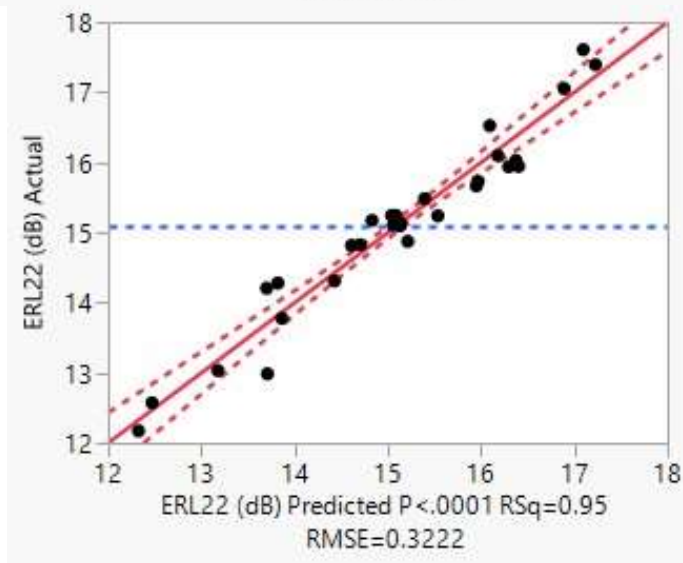
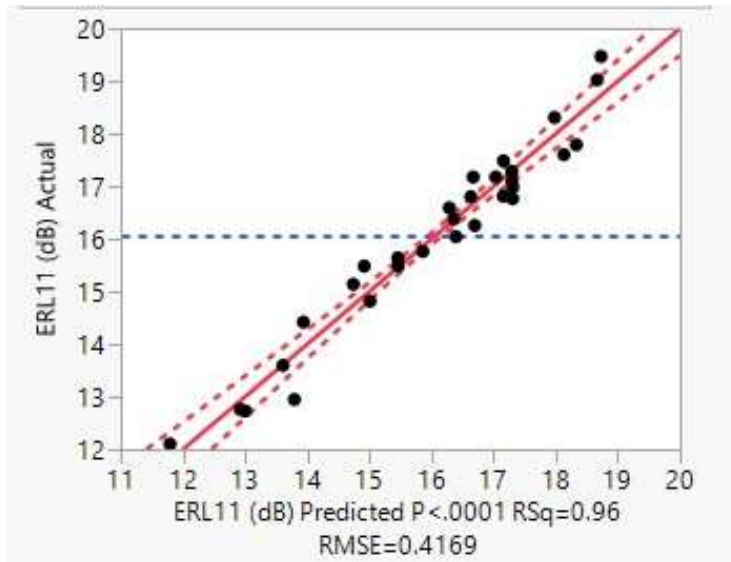
# Return Loss vs. ERL



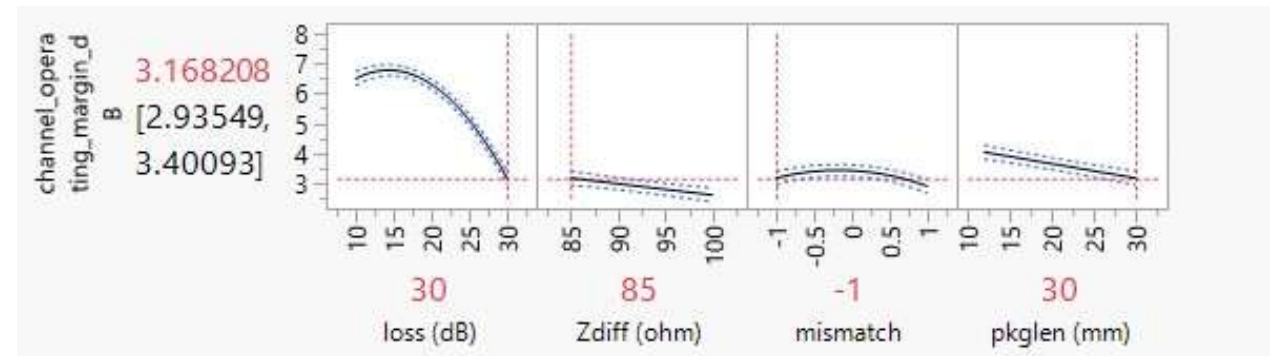
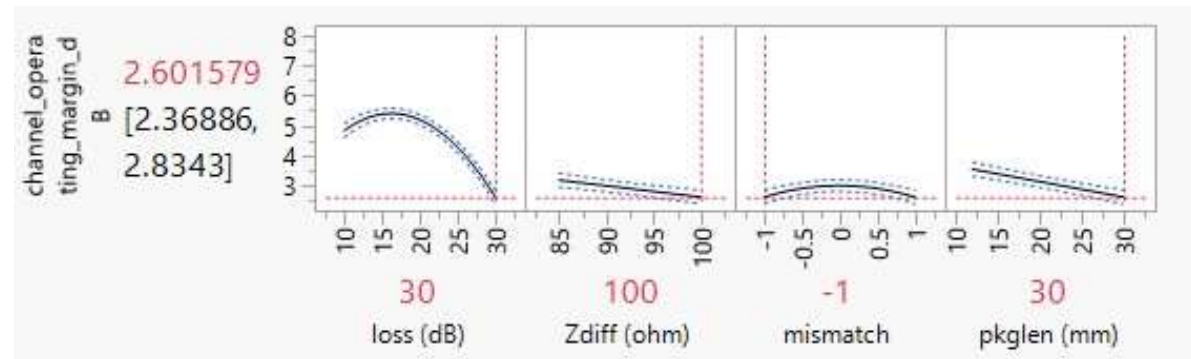
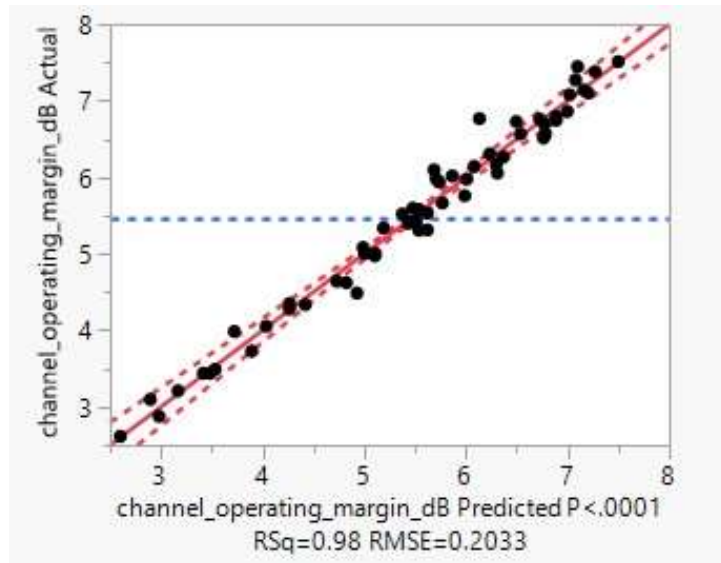
# COM vs ERL



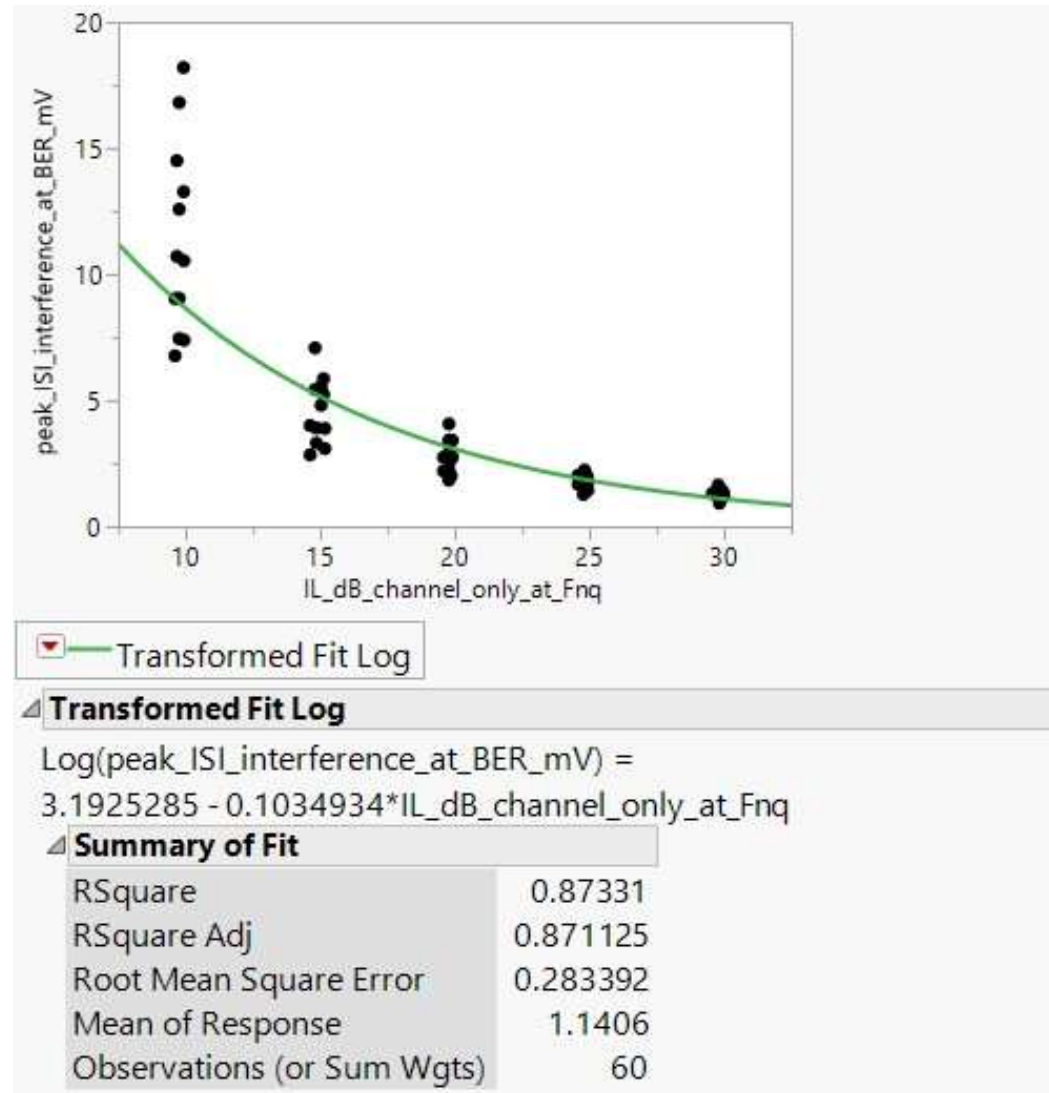
# ERL RSM Model



# COM RSM Model



# Peak ISI vs Channel IL





# ERL vs Peak ISI

