IEEE P802.3cd Ad Hoc meeting – January 17, 2018

Prepared by Kent Lusted

Proposed Agenda:

- Approval of the Agenda
- Approval of the Jan 10 minutes
- IEEE patent policy reminder:
 - http://www.ieee802.org/3/patent.html
- IEEE Participation Requirements reminder
- P802.3cd TF Ad Hoc
 - P802.3cd Task Force Update, Mark Nowell (5 min)
 - "Proposal to modify the SRS test for 100GBASE-DR", David Lewis (15 mins)
 - "TDECQ : RLM and threshold adjust", Jonathan King (20 mins)
 - "SECQ", Scott Schube (20 mins)
 - "Jitter considerations for 100GAUI-2 with 100GBASE-DR, comments i-61 and i-115", Adee Ran (45 mins)

Presentations posted at: http://www.ieee802.org/3/cd/public/adhoc/archive/index.html

Meeting began at ~8:05 a.m. Pacific by Kent Lusted.

Meeting began with the agenda presentation:

http://www.ieee802.org/3/cd/public/adhoc/archive/agenda 011718 3cd adhoc.pdf

The ad hoc chair reminded participants to indicate full names and employer/affiliation correctly for the meeting minutes. He reminded participants to mute lines when not speaking and reviewed the steps to unmute.

Showed the links to the IEEE P802.3cd Task Force ad hoc page and the email reflector.

Presented the proposed agenda and asked if there was objection as written. The agenda was approved by the ad hoc.

Asked if there were comments regarding the posted minutes of the last ad hoc meeting. No one responded.

Reminded participants of the IEEE patent policy. He asked if anyone was unfamiliar with the IEEE patent policy. No one responded.

Reminded participants of the IEEE Participation Requirements and showed the slide with the Participation requirements. He asked if anyone was unfamiliar with the IEEE Participation Requirements. No one responded.

Agenda Items

P802.3cd Task Force update, Mark Nowell

- There are 17 presentation requests for the interim meeting.
- Two tracks on Monday and Tuesday at the meeting.
- Monday morning ITU registration desk opens at 8:30 a.m., not 8:00 a.m. as previously communicated. Please pre-register to expedite the process.
- Meeting at January 2018 interim, week of Jan 22. Plan to address comments and generate D3.1.
- Chair received several liaison letters that need response at the interim meeting. The liaisons are all available in the private area: http://www.ieee802.org/3/cd/private/index.html Chair asked if people could take a moment to review the MEF liaison and the draft they shared. Chair does not think the technical details are highly relevant to this group but there are a number of references to the IEEE specifications that the TF may want to review and check for accuracy.
- Proposed comment responses are posted on the website. (See: http://www.ieee802.org/3/cd/comments/index.html)
- Early registration for the March plenary is Friday, January 26, 2018.

Presentation #1:

"Proposal to modify the SRS test for 100GBASE-DR", David Lewis

See: http://www.ieee802.org/3/cd/public/adhoc/archive/lewis_011718_3cd_adhoc.pdf

- The presentation references comment i-78.
- Discussed the SRS curves on slide 8.
- Author is waiting for permission to share data that illustrates the problem.
- It was recommended by the editor to provide that the IEEE text such as an equation.

Presentation #2:

"TDECQ: RLM and threshold adjust", Jonathan King

See: http://www.ieee802.org/3/cd/public/adhoc/archive/king_011018_3cd_adhoc.pdf

- Discussed the graphs of Q penalty vs. RLM on slide 8 as it relates to eye compression.
- Continued discussing the relationship of Q penalty vs. RLM to the data shared by other participants.

Presentation #3:

"SECQ Test Method and Calibration Improvements", Scott Schube

See: http://www.ieee802.org/3/cd/public/adhoc/archive/schube_011718_3cd_adhoc.pdf

- The eye diagrams on slide 3 are from simulation.
- Author indicated that some DSP vendors expressed concern with the noise amounts in the current specification.

- There was a request to get the waveforms from the presentation for offline analysis. Scott will check it.
- It was noted that the authors should review the Frank Chang contribution TDECQ analysis.

Presentation #4:

"Jitter considerations for 100GAUI-2 with 100GBASE-DR, comments i-61 and i-115", Adee Ran See: http://www.ieee802.org/3/cd/public/adhoc/archive/ran_011718_3cd_adhoc.pdf

- Discussed the benefits and impacts of a cleaning PLL.
- It was noted that the jitter will be broadband, not a specific frequency, as assumed on slide 5.

*** A free drink to the first person to let the Task Force Chair know they've read this ***

The ad hoc meeting ended at ~10 a.m. Pacific.

List of attendees (captured from Webex tool)

Name Company

Adam Healey broadcom.com
Adee Ran intel.com
Akinori Hayakawa jp.fujitsu.com

Ali Ghiasi n/a

maxlinear.com Anand A (Mxl) Andrew Kim (Spectra7) spectra7.com cadence.com **Arthur Marris** cisco.com Beth Kochuparambil bill Kirkland semtech.com semtech.com C Samoila semtech.com Chad Erven chengson iuniper.net intel.com ChienPing Kao (Intel) Dave marvell.com Dave Lewis (Lumentum) lumentum.com David Malicoat (Senko) senko.com

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Frank Chang inphi.com
Gary Nicholl cisco.com
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salvatore rotolo st.com intel.com Scott Steve Sekel keysight.com T.SAKAI socionext.com Vittal Balasubramanian innovium.com Will Bliss broadcom.com Xianyun Meng juniper.net Yang Zhiwei zte.com.cn independent Yasuo Hidaka (Independent)