

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 01 SC 1.3 P 28 L 22 # i-4

Hajduczenia, Marek Charter Communications

Comment Type ER Comment Status A MDI

So what happens when IEC 61076-3-125 is not published by the time this draft is done?
Do you wait for its completion?

SuggestedRemedy

I am not sure what the strategy is in case of IEC specifications in flight, but we cannot (I believe) have a reference to an unpublished IEC draft. It seems that the spec would have to wait for iEC publication to become available?
The same comment / question about IEC 63171-1 under development

Response Response Status W

ACCEPT IN PRINCIPLE.

Replace, "Editor's Note (to be removed prior to publication): IEC 61076-3-125 is still in development. The publication date will need to be inserted and the document title and number confirmed."

with, "Editor's Note (to be removed prior to final recirculation): IEC 63171-6 is still in development. The publication date will need to be inserted and the document title and number confirmed. If IEC 63171-6 is not referenceable by final circulation, then the entry for IEC 63171-6, this Editor's Note, and references to IEC 63171-6 in this draft will be removed."

CI 45 SC 45.2 P 42 L 1 # i-8

Rannow, R K self

Comment Type GR Comment Status R Editorial

verbose and confusing wording throughout Subclause 45.2

SuggestedRemedy

Response Response Status W

REJECT.

The CRG disagrees with the commenter. The commenter did not provide a proposed resolution in sufficient detail to readily determine the specific wording of changes that will cause him to change his vote to approve (see SASB Ops Manual clause 5.4.3.2,b).

CI 01 SC 1.3 P 28 L 24 # i-12

Anslow, Peter Ciena

Comment Type TR Comment Status A MDI

The editor's note says "IEC 61076-3-125 is still in development. The publication date will need to be inserted and the document title and number confirmed."
However, the IEC web site does not contain any status information on IEC 61076-3-125. This suggests that the document number is incorrect or it will not be published by the expected approval date for the P802.3cg amendment of September 2019.
Since any normative reference has to be available at the time of approval of the draft, this issue has to be corrected prior to the draft being suitable for RevCom submittal.

SuggestedRemedy

Either:
Change the reference to a document that will be published by the expected date of RevCom submittal or remove this reference.

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodated by i-4. The resolution to i-4 is:

ACCEPT IN PRINCIPLE.

Replace, "Editor's Note (to be removed prior to publication): IEC 61076-3-125 is still in development. The publication date will need to be inserted and the document title and number confirmed."

with, "Editor's Note (to be removed prior to final recirculation): IEC 63171-6 is still in development. The publication date will need to be inserted and the document title and number confirmed. If IEC 63171-6 is not referenceable by final circulation, then the entry for IEC 63171-6, this Editor's Note, and references to IEC 63171-6 in this draft will be removed."

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CI 01 SC 1.3 P 28 L 39 # i-13

Anslow, Peter

Ciena

Comment Type TR Comment Status A MDI

The editor's note says "IEC 63171-1 is still in development. The publication date will need to be inserted."

However, the IEC web site shows an expected publication date for IEC 63171-1 of May 2020.

Also, the title shown on the IEC web site is "IEC 63171-1, Connectors for Electrical and Electronic Components--Product Requirements--Part 1: Detail specification for 2-way, shielded or unshielded, free and fixed connectors: mechanical mating information, pin assignment and additional requirements for TYPE 1 / Copper LC Style"

Since any normative reference has to be available at the time of approval of the draft, this issue has to be corrected prior to the draft being suitable for RevCom submittal.

SuggestedRemedy

Either:

Change the reference to a document that will be published by the expected date of RevCom submittal or remove this reference.

Response Response Status W

ACCEPT IN PRINCIPLE.

Replace, "Editor's Note (to be removed prior to publication): IEC 63171-1 is still in development. The publication date will need to be inserted."

with, "Editor's Note (to be removed prior to final recirculation): IEC 63171-1 is still in development. The publication date will need to be inserted. If IEC 63171-1 is not referenceable by final circulation, then the entry for IEC 63171-1, this Editor's Note, and references to IEC 63171-1 in this draft will be removed."

CI 104 SC 104.7.1.4 P 97 L 26 # i-19

Anslow, Peter

Ciena

Comment Type ER Comment Status A EZ

As pointed out in Comment #11 against D2.3:

In Equation (104-5) "min" is a function not a variable, so should not be italic font.

Same issue for Equation (104-6)

SuggestedRemedy

Change "min" to be in upright font in both Equation (104-5) and Equation (104-6)

Response Response Status W

ACCEPT.

CI 146 SC 146.7.1 P 147 L 28 # i-20

Anslow, Peter

Ciena

Comment Type ER Comment Status A EZ

This editor's note just describes work going on in another standards body. This is not appropriate in a draft that is suitable for submission to RevCom

SuggestedRemedy

Delete the editor's note.

Response Response Status W

ACCEPT.

CI 146 SC 146.9.1 P 156 L 28 # i-21

Anslow, Peter

Ciena

Comment Type ER Comment Status A Safety

This editor's note is not appropriate in a draft that is suitable for submission to RevCom

SuggestedRemedy

Change the text as appropriate and delete the editor's note.

Response Response Status W

ACCEPT IN PRINCIPLE.

Delete the editor's note.

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 00 SC 0 P 1 L # i-27

Robinson, Gary RETIRED/unemployed

Comment Type TR Comment Status R PLCA

This standard is well written for its intended purpose but I do not believe it belongs as an amendment to 802.3 series.

This standard does not conform to the layer 1, 2, or 3 rules as the rest of 802.3.

Physical Layer Collision Avoidance (PLCA) when combined with CSMA/CD (which remains as an error handling function) constitutes a new Media Access Control (MAC) function and as such belongs in the MAC sublayer, not in the Physical Sublayer. Where such a function is appropriately placed is a matter of architecture, not implementation per clause 1.1.3 of the standard.

I would be satisfied if it was moved out of 802.3 and into 802.n or another series all together.

As the original contributor of CSMA/CD, 802.3 I have argued this issue before and I am sure it is not the last time.

SuggestedRemedy

I would be satisfied if it was moved out of 802.3 and into 802.n or another series all together.

Response Response Status W

REJECT.

The CRG disagrees with the commenter. The specification of PLCA is appropriately placed in the physical layer and carries out the operations delegated to the physical layer in the 802.3 architecture, providing mapping of PLS primitives to signalling for the PHY, and aligning the MAC data with the needs of the PHY. Nodes implementing the PLCA RS are interoperable on the same mixing segment with nodes without the PLCA RS implemented or enabled. The functions are located in the physical layer according to the definitions in ISO 7894-1:1994, which states that the physical layer provides "functional and procedural means to activate, maintain, and de-activate physical-connections for bit transmission between data-link-entities." (7.7.2), and that "functions may be provided by the (N)-layer to enhance the facilities offered to, and the quality of service seen by the (N+1)-entities over those which are offered to the (N)-layer by the (N-1)-layer" (5.3.3.1.2). The PLCA RS conforms to the Physical layer service specifications in IEEE 802.3 by interfacing with the MAC at the existing PLS_CARRIER, PLS_DATA_VALID, and PLS_SIGNAL primitives and providing the information necessary for the local MAC sublayer entity to perform media access functions. (IEEE Std 802.3-2018 6.2.3). The augmentation of the physical layer is consistent with prior augmentation of these primitives in IEEE Std 802.3 over its lifetime, but particularly the last 20 years. For further information, please see http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf

Move to accept the above proposed reject response to comment i-27:

M: C. Jones

S: V. Maguire

Y: 27

N: 4

A: 8

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-27

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CI 147 SC 147.3.3.5 P 181 L 27 # i-32

Marris, Arthur Cadence Design Systems, Inc.

Comment Type **TR** Comment Status **A** PCS

Definition of RSCD is not adequate

SuggestedRemedy

"RSCD indicates a new symbol has been decoded and is available for processing in the state diagram."

Response Response Status **W**

ACCEPT IN PRINCIPLE.

Accommodated by comment i-423. Response to comment i-423 is:

ACCEPT IN PRINCIPLE.

Multiple editorial changes are required in both the PCS Receive and PCS Transmit functions subclauses to address the lack of clarity pointed out by the commenter.

Editorial license to align other comments with the variable name changes in this comment response, and to re-alphabetize variable names in lists as necessary.

Throughout the whole subclause 147.3.2 (including figures) apply the following changes:

- replace all occurrences of "pcs_txen" with "TX_EN"
- replace all occurrences of "pcs_txer" with "TX_ER"
- replace all occurrences of "pcs_txd" with "TXD"

Throughout the whole subclause 147.3.3 (including figures) apply the following changes:

- replace all occurrences of "pcs_rxdv" with "RX_DV"
- replace all occurrences of "pcs_rxr" with "RX_ER"
- replace all occurrences of "pcs_rxd" with "RXD"

At page 170, line 42 replace "with every PCS transmit clock cycle" to
"with every symb_timer expiration. The symb_timer is defined in 147.3.2.7."

At page 177, line 33 change the description of the "tx_sym" variable to:
"5B symbol to be conveyed to the PMA Transmit function by the means of the PMA_UNITDATA.request primitive specified in 147.2.2."

At page 178, line 24 change the description of the "ENCODE" function to:
"This function takes a 4 bit input parameter Scn<3:0> and returns a 5B symbol according to the following procedure:
1. Convert Scn<3:0> into Sdn<3:0> as specified in 147.3.2.6.
2. Convert Sdn<3:0> (4B symbol) into the corresponding 5B symbol defined in Table 147-1."

At page 179, line 24 change the description of the "STD" abbreviation to:
"Alias for symbol timer done."

At page 179, line 32, change the second paragraph (starting with "An implementation of ...")

to read:

"An implementation of a self-synchronizing scrambler by a linear-feedback shift register is shown in Figure 147-6. The bits stored in the shift register delay line at time n are denoted by Scn<16:0>. The '+' symbol denotes the exclusive OR logical operation. When Scn<3:0> is presented at the input of the scrambler, Sdn<3:0> is produced by shifting in each bit of Scn<3:0> as Scn<i>, with i ranging from 0 to 3 (i.e., LSB first). The scrambler is reset upon execution of the PCS Reset function. If the PCS Reset is executed, all bits of the 17-bit vector representing the self-synchronizing scrambler state are arbitrarily set. The initialization of the scrambler state is left to the implementer. In no case shall the scrambler state be initialized to all zeroes. At every STD, if no data is presented at the scrambler input via Scn<3:0>, the scrambler may be fed with arbitrary inputs."

At page 180, line 8, append the following text to subclause 147.3.2.7:

"symb_timer

A continuous free-running timer. PMA_UNITDATA.request messages are issued by the PCS concurrently with symb_timer_done (see 147.2.2). TX_CLK (see 22.2.2.1) shall be generated from 5B_symb_timer with the rising edge of TX_TCLK generated synchronously with 5B_symb_timer_done.

Continuous timer: The condition symb_timer_done becomes true upon timer expiration.

Restart time: Immediately after expiration.

Duration: 400 ns ± 100 ppm (see 22.2.2.1)"

At page 179 in Figure 147-6 perform the following changes:

- replace "TXDn[i]" with "Scn<i>". Please note the 'n' is a subscript
- replace all square brackets '[]' with angular brackets '<>'

At page 180, line 9, change the description of the "RXn" variable to read:

"The rx_sym parameter of the PMA_UNITADATA.indication primitive defined in 147.2.1.

The 'n' subscript denotes the rx_sym conveyed in the most recent recv_symb_conv_timer cycle.

The 'n-x' subscript indicates the rx_sym conveyed 'x' cycles behind the most recent one."

At page 181, line 18, change the description of the "DECODE" function to read:

"This function takes a 5B symbol input parameter and returns a 4 bit value Dcn<3:0> value according to the following procedure:

1. Convert the 5B input symbol into Drn<3:0> by performing a reverse lookup of Table 147-1. If no 4B value is associated to the given 5B symbol, the PCS Receive function shall assert RX_ER for at least one symbol period and Drn<3:0> may be set arbitrarily.
2. Convert Drn<3:0> to Dcn<3:0> as specified in 147.3.3.7."

Please not that the 'n' in the Dcn and Drn variables name is a subscript.

At page 181, line 26, change the description of the "RSCD" abbreviation to read:

"Alias for recv_symb_conv_timer_done."

At page 183, line 48, insert a new subclause 147.3.3.x with name "Timers" between existing subclauses 147.3.3.7 and 147.3.3.8.

Add the following text to the newly created subclause:

"recv_symb_conv_timer

A continuous timer which expires when the PMA_UNITDATA.indication message is

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID **i-32**

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generated (see 147.2.1).

Continuous timer: The condition `recv_symb_conv_timer_done` becomes true upon timer expiration.

Restart time: Immediately after expiration.

Duration: timed by the `PMA_UNITDATA.indication` message generation."

Perform renumbering of the subclauses accordingly.

At page 183, line 28 change the whole paragraph starting with "The PCS receive function shall ..." to read:

"The PCS Receive function descrambles the 5B/4B decoded data stream and returns the value of `RXD<3:0>` to the MII. The descrambler shall employ the polynomial defined in 147.3.2.6. The implementation of the self-synchronizing descrambler by linear-feedback shift register is shown in Figure 147–9. The bits stored in the shift register delay line at time *n* are denoted by `Dcrn<16:0>`. The '+' symbol denotes the exclusive OR logical operation. When `Drn<3:0>` is presented at the input of the descrambler, `Dcn<3:0>` is produced by shifting in each bit of `Drn<3:0>` as `Drn<i>`, with *i* ranging from 0 to 3 (i.e., LSB first). The descrambler is reset upon execution of the PCS Reset function. If PCS Reset is executed, all the bits of the 17-bit vector representing the self-synchronizing descrambler state are arbitrarily set. The initialization of the descrambler state is left to the implementer. At every RSCD, if no data is presented at the descrambler input via `Drn<3:0>`, the descrambler may be fed with arbitrary inputs."

Please note that the 'n' in the `Dcn` and `Drn` variables name is a subscript.

At page 183, in figure 147-9, perform the following changes:

- replace "`RXDn[i]`" with "`DCn[i]`"
- replace all square brackets '[' with angular brackets '<>'

At page 191, line 52, add the following text after "DME encoded stream received at the MDI.":

"The clock recovery provides a synchronous clock for sampling the signal on the pair. While it may not drive the MII directly, the clock recovery function is the underlying source of `RX_CLK`."

CI 104	SC 104	P 86	L 1	# i-37
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Yseboodt, Lennart

Signify

Comment Type	TR	Comment Status	A	PoDL
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After reviewing 146.8.4 I realized that PoDL's PSE spec does not include a voltage polarity requirement.

The PD section does not specify whether PDs need to be polarity insensitive, or what the expected pinout is either.

Suggested Remedy

Add a subsection with appropriate requirements for the PSE and PD that specifies output/input voltage polarity (possibly linked only to 10SPE and/or the listed IEC connectors there).

Response	Response Status	W
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ACCEPT IN PRINCIPLE.

Bring Figure 104-3 in the document and replace MDI+ with BI_DA+ and replace MDI- with BI_DA- in the figure.

Add sub-clause 104.4.1

"104.4.1 PI pin assignments

A PSE provides power via a single two wire connection. Table 104-1a in conjunction with Figure 104-3 illustrates the PSE pinout.

A PSE shall implement the PSE pinout in Table 104-1a.

Table 104-1a - PSE Pinout

```
{
{ {Contact} {PI} }
{ {1} {PI+} }
{ {2} {PI-} }
}
```

Add sub-clause 104.5.1

"104.5.1 PD PI

A PD may receive power in two modes, Mode A and Mode B. Table 104-4a in conjunction with Figure 104-3 illustrates the PD pinout.

Table 104-4a - PD Pinout

```
{
{ { Contact } {Mode A} {Mode B} }
{ {1} {PI+} {PI-} }
{ {2} {PI-} {PI+} }
}
```

Class 0 to class 9 PDs shall be able to operate per the Mode A column in Table 104-4a.

Class 10 to class 15 PDs shall be implemented to be insensitive to the polarity of the

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power supply and shall be able to operate per the Mode A column and the Mode B column in Table 104-4a.

Editorial license granted to craft necessary Editing Instruction text in accordance with IEEE style.

Cl 104 **SC 104.7.1.4** **P 97** **L 22** # **i-38**

Yseboodt, Lennart

Signify

Comment Type **ER** **Comment Status** **A** **PoDL**

104.7.1.4 is the subclause that specifies how a PoDL system can determine the actual cable resistance between the PIs.

The measured value is named "RCable_initial". This value is then increased with a margining factor and the result is called RAutoclass.

Autoclass is a specific term used in Clause 145 to denote a classification mechanism. The parameter naming here is confusing, as this is about a cable resistance measurement method.

SuggestedRemedy

Rename RAutoclass to RCable.

Response **Response Status** **W**

ACCEPT IN PRINCIPLE.

Replace "RAutoclass" with "RCable" in sub-clause 104.7.4.1 and in Equation 104-5 (Page 97)

Cl 104 **SC 104.7.2.6** **P 100** **L 40** # **i-40**

Yseboodt, Lennart

Signify

Comment Type **TR** **Comment Status** **A** **PoDL**

Bits 13:8 in the VOLT_POWER_INFO register (Table 104-10) denote the power the PD is asking.

The table says "Power requested by PD, 0.3125 W per LSB".

With the 6 available bits, we can express power up to $(2^6)-1 * 0.3125W = 19.69W$.

This is less than the amount of power supported by PoDL.

SuggestedRemedy

Suggest to:

- use bits 15:8 and make the LSB count for 400mW, resulting in max 102W.

Make sure to align solution with similar comment on Table 104-11.

Response **Response Status** **W**

ACCEPT IN PRINCIPLE.

Make the following changes:

- 1) In Table 104-10, first Row, first Column, change from "b[15:14]" to "b[31:20]"; second Row, first Column, change from "b[13:8]" to "b[19:8]"; second Row, third Column, change from "Power requested by PD, 0.3125 W per LSB" to "Power requested by PD, 0.025 W per LSB"
- 2) In 104.7.2.6, line 29 change from "shall respond with a 16-bit VOLT_POWER_INFO read payload" to "shall respond with a 32-bit VOLT_POWER_INFO read payload"
- 3) In 104.7.2.5, line 43, change from "contents of the preceding 16-bit Read/Write payload" to "contents of the preceding Read/Write payload"
- 4) In 104.7.1.5 line 49, change from "is the PD Requested Power as reported in b[13:8] of VOLT_POWER_INFO" to "is the PD Requested Power as reported in b[19:8] of VOLT_POWER_INFO"
- 5) In 104.7.1.5 line 36, change from "via the PD Requested Power, PPD_req, field of the VOLT_POWER_INFO Register b[13:8]" to "via the PD Requested Power, PPD_req, field of the VOLT_POWER_INFO Register b[19:8]"

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CI **104** SC **104.7.2.7** P **101** L **16** # **i-41**

Yseboodt, Lennart

Signify

Comment Type **TR** Comment Status **A** PoDL

Bits 5:0 in the POWER_ASSIGN register (Table 104-11) denote the power assigned to the PD.

Like in the other Table, 6 bits with 0.3125W/bit only get us to just under 20W

Suggested Remedy

Implement solution consistent as with fix VOLT_POWER_INFO.

Response Response Status **W**

ACCEPT IN PRINCIPLE.

Make the following changes:

- 1) In Table 104-11, first Row, first Column, change from "b[15:6]" to "b[31:12]" ; second Row, first Column, change from "b[5:0]" to "b[11:0]" ; second Row, third Column, change from "PD assigned power, 0.3125 W per LSB" to "PD assigned power, 0.025 W per LSB"
- 2) In 104.7.2.7 line 5, change from "the PSE shall transmit a 16-bit POWER_ASSIGN write payload" to "the PSE shall transmit a 32-bit POWER_ASSIGN write payload"
- 3) In 104.7.2.8 line 25, change from "the PD shall respond with a 16-bit POWER_ASSIGN read payload" to "the PD shall respond with a 32-bit POWER_ASSIGN read payload"
- 4) In 104.7.1.5 line 52, change from "is the PD Assigned Power by PSE as assigned in b[5:0] of POWER_ASSIGN" to "is the PD Assigned Power by PSE as assigned in b[11:0] of POWER_ASSIGN"
- 5) In 104.7.1.5 line 2 on page 98, change from "the PSE determines PPD_assign, as assigned in b[5:0] of POWER_ASSIGN" to "the PSE determines PPD_assign, as assigned in b[11:0] of POWER_ASSIGN"

Add MDIO registers to accommodate the larger number of bits in the PD power fields as shown in http://www.ieee802.org/3/cg/public/May2019/stewart_3cg_02_0519_v1.pdf, by making the following changes: (references to "Stewart comment i-40_41 presentation" below are to this URL).

Change the edit to clause 45.2.9 Power Unit Registers with editing instructions: "modify Table 45-211p Power Unit MMD Registers to add two rows for registers 13.3 and 13.4 below the existing row for register 13.2 as shown on slide 3 of Stewart comment i-40_41 presentation"

And add new subclause 45.2.9.4 PoDL PSE Status 3 Register (Register 13.3) and 45.2.9.4.1 PD Assigned Power (13.3.11:0) to the draft, with editing instruction: "Insert New subclause 45.2.9.4 PoDL PSE Status 3 Register (Register 13.3) after 45.2.9.3 and add text under subclause 45.2.9.4 as below:"
 "The PoDL PSE Status 3 Register is defined if cable resistance measurement is supported"
 "And Insert New subclause 45.2.9.4.1 PD Assigned Power (13.3.11:0) after 45.2.9.4 and insert text under subclause 45.2.9.4.1 PD Extended Class (13.3.11:0) as below:
 "The PD Assigned Power is the maximum average available power at the PD PI."
 Add Table 45-342 PoDL PSE Status 3 Register (Register 13.3) under clause 45.2.9.4 to

denote PD Assigned Power as shown on slide 4 of Stewart comment i-40_41 presentation.

And add new subclause 45.2.9.5 PoDL PSE Status 4 Register (Register 13.4) and 45.2.9.5.1 PD Requested Power (13.4.11:0) to the draft, with editing instruction: "Insert New subclause 45.2.9.5 PoDL PSE Status 4 Register (Register 13.4) after 45.2.9.4 and add text under subclause 45.2.9.5 as below:"
 "The PoDL PSE Status 4 Register is defined if cable resistance measurement is supported"
 "And Insert New subclause 45.2.9.5.1 PD Requested Power (13.4.11:0) after 45.2.9.5"
 "Insert text under subclause 45.2.9.5.1 PD Extended Class (13.4.11:0) as below:
 "The PD Requested Power is the requested average available power at the PD PI."
 Add Table 45-343 PoDL PSE Status 4 Register (Register 13.4) under clause 45.2.9.5 to denote PD Assigned Power as shown on slide 5 of Stewart comment i-40_41 presentation.

Editorial license granted to craft necessary Editing Instruction text in accordance with IEEE style.

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CI 146 SC 146.8.4 P 155 L 34 # i-42

Yseboodt, Lennart

Signify

Comment Type TR Comment Status A PoDL

146.8.4:

"The wire pair of the MDI shall withstand without damage the application of positive voltages of up to 60 V dc with the source current limited to 2000 mA, under all operating conditions, for an indefinite period of time."

146.8.5:

"The wire pair of the MDI shall withstand without damage the application of short circuits of any wire to the other wire of the same pair or ground potential, as per Table 146-9, under all operating conditions, for an indefinite period of time."

- Why does 146.8.4 only cover positive voltages ?
- ... and 146.8.5 covers both polarities ?
- why is the subject of the sentence 'the wire pair of the MDI' when it should be the device itself ?

SuggestedRemedy

Change the quoted text in 146.8.4 to read:

"The device shall withstand without damage the application of any voltages between 0 V dc and 60 V dc with the source current limited to 2000 mA, applied across BI_DA+ and BI_DA-, in either polarity, under all operating conditions, for an indefinite period of time."

Response Response Status W

ACCEPT IN PRINCIPLE.

On page 155, line 34:

Replace, "The wire pair of the MDI shall withstand without damage the application of positive voltages of up to 60 V dc with the source current limited to 2000 mA, under all operating conditions, for an indefinite period of time."

with, "The DTE shall withstand without damage the application of any voltages between 0 V DC and 60 V DC with the source current limited to 2000 mA, applied across BI_DA+ and BI_DA-, in either polarity, under all operating conditions, for an indefinite period of time."

(Editor's note: Make sure the "-" in BI_DA- is an en-dash)

CI 147 SC 147.9.3 P 203 L 38 # i-44

Yseboodt, Lennart

Signify

Comment Type TR Comment Status A MDI

* Similar comment filed against Clause 146. Make sure to make changes consistently.

147.9.3:

"The wire pair of the MDI shall withstand without damage the application of positive voltages of up to 60 V dc with the source current limited to 2000 mA, under all operating conditions, for an indefinite period of time."

147.9.4:

"The wire pair of the MDI shall withstand without damage the application of short circuits of any wire to the other wire of the same pair or ground potential, as per Table 147-5, under all operating conditions, for an indefinite period of time."

- Why does 147.9.3 only cover positive voltages ?
- ... and 147.9.4 covers both polarities ?
- why is the subject of the sentence 'the wire pair of the MDI' when it should be the device itself ?

SuggestedRemedy

Change the quoted text in 146.9.3 to read:

"The device shall withstand without damage the application of any voltages between 0 V dc and 60 V dc with the source current limited to 2000 mA, applied across BI_DA+ and BI_DA-, in either polarity, under all operating conditions, for an indefinite period of time."

Response Response Status W

ACCEPT IN PRINCIPLE.

On page, 203, lines 38-39:

Replace,

"The wire pair of the MDI shall withstand without damage the application of positive voltages of up to 60 V DC with the source current limited to 2000 mA, under all operating conditions indefinitely."

with,

"The DTE shall withstand without damage the application of any voltages between 0 V DC and 60 V DC with the source current limited to 2000 mA, applied across BI_DA+ and BI_DA<en-dash>, in either polarity, under all operating conditions, for an indefinite period of time."

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 148 SC 148 P 214 L 1 # i-47

Grow, Robert

RMG Con

Comment Type TR Comment Status R PLCA_SCOPE

The PLCA protocol is a MAC protocol. It is virtually identical to a token bus protocol (shared medium) I specified years ago. This clause violates 802.3 layering, and though considerable effort has been made to place this in the Reconciliation Sublayer, it doesn't change the fact that the functions are medium access control.

SuggestedRemedy

Delete Clause 148 and related text.

Response Response Status U

REJECT.

The CRG disagrees with the commenter's description of layering and the proper placement of PLCA in the layering model. PLCA performs the functions delegated by the 802.3 layer model to the physical layer - carrier sense and collision detection. Commenter seems to posit an implementation which is not described in the amendment, where the PLCA sublayer interfaces to the MAC via an MII. (a "top MII" per the commenter), whereas PLCA maintains the layering and communicates to the MAC via the primitives PLS_CARRIER and PLS_SIGNAL defined in IEEE Std 802.3, and communicates with the remainder of the physical layer through the MII interface. For more detail on how PLCA relates to OSI layering please see http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf.

Additionally, the fact that PLCA-enabled half-duplex CSMA/CD stations may operate with and coexist with non-PLCA enabled half-duplex CSMA/CD stations on the same mixing segment is evidence that the PLCA RS is located beneath the CSMA/CD MAC and not a new MAC function in itself. See http://www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf and http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf

The PLCA working principle is to detect collisions (concurrent transmission of multiple stations on a shared network segment) in a logical sense. As an example, 10BASE-2 and 10BASE-5 detect concurrent transmissions by checking the DC voltage level on the shared media, that is detecting the superposition of multiple (not decodable) signals on the line. PLCA detects the very same concurrent transmissions by aligning the data conveyed by the local MAC to the unique transmit opportunity of the node and checking for concurrent reception of a packet. In such a way the collision does not result in "corrupting" the signal on the media. That is, the packet currently being transmitted is not interrupted, thus yielding the advertised network performance enhancement.

This is also in line with the ISO/OSI principle by which a layer may enhance the service it provides to the upper layer.

See http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf

Moreover the commenter is unclear as PLCA + CSMA/CD is obviously not identical to 802.4 Token Bus, and it is unclear what specification the commenter is referring to. For example, PLCA does not define any handshake protocol between nodes, it does not

generate packets and there is no concept of arbitration of the media. Additionally, CSMA/CD nodes with PLCA enabled interoperate properly with non-PLCA enabled nodes on the same network segment (without yielding the advertised gain in performance in this case). That would not be possible if nodes with PLCA enabled were not, in fact, using the CSMA/CD MAC protocol. See http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf.

CI 148 SC 148 P 214 L 1 # i-48

Grow, Robert

RMG Con

Comment Type GR Comment Status R PLCA_SCOPE

This clause specifies functionality that is outside the scope of the PAR. The result of out of scope content is that all interested parties may not have been aware of actual content and as a result enticed to join the ballot group.

SuggestedRemedy

Either delete the clause and related content, or revise the PAR, reform the ballot group, and restart Standards Association ballot.

Response Response Status U

REJECT.

The CRG disagrees with the commenter, and believes the draft is within the PAR scope. A key responsibility of the ballot pool is to evaluate whether the scope of the draft is within the scope of the PAR, and an affirmative vote indicates your agreement that the work does not exceed the scope of the PAR. The ballot pool has voted in the affirmative.

CI 98 SC 98B.3 P 235 L 11 # i-154

Marris, Arthur

Cadence Design Systems, Inc.

Comment Type TR Comment Status A Editorial

Put the two unchanged rows into Table 98B-1 it will make things clearer.

SuggestedRemedy

Delete "(unchanged rows not shown)" on line 11

Add the following to Table 98B-1:

A0 100BASE-T1 ability

A2 1000BASE-T1 ability

Response Response Status W

ACCEPT.

nd Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

Cl 30	SC 30.2.2.2.1	P 0	L 0	# i-205
Thompson, Geoffrey		Independent Consultant		
Comment Type	TR	Comment Status	R	PLCA
As I think I understand PLCA the occurrence of collision at any point during reception is an error. If that is the case, then collision (in the presence of PLCA operation) should be added to the list of error statistics in this clause.				
SuggestedRemedy				
See comment.				
Response	Response Status U			
REJECT.				
The CRG disagrees with the commenter. Collisions on the media in the presence of PLCA operation are already counted by the bits in register 3.2294.15:0 (see 45.2.3.68f.1). No change is required.				

Cl 00	SC 0	P 11	L 15	# i-207
Thompson, Geoffrey		Independent Consultant		
Comment Type	ER	Comment Status	R	Editorial
The following statement in the introductory material is not true: "Ethernet at 10 Mb/s was approved as an IEEE standard by the IEEE Standards Board in 1983 and subsequently published in 1985 as IEEE Std 802.3-1985." What was initially approved and published by the IEEE was not identified as Ethernet. The only mention of the word "Ethernet" in the first 802.3 standard is in an acknowledgement on page 7 of the front matter between the Working Group member listing and the Standards Board membership roster. "The IEEE 802.3 Working Group acknowledges and appreciates that many concepts embodied in this standard are based largely upon the CSMA/CD access method earlier described in The Ethernet specification as written jointly by individuals from Xerox Corporation, Digital Equipment Corporation, and Intel Corporation. Appreciation is also expressed to Robert M. Metcalfe and David R. Boggs for their pioneering work in establishing the original concepts." IEEE Std 802.3-1985				
SuggestedRemedy				
Change the sentence to read: The derivative at 10 Mb/s was approved as an IEEE standard by the IEEE Standards Board in 1983 and subsequently published in 1985 as IEEE Std 802.3-1985 titled Information technology-- Telecommunications and information exchange between systems-- Local and metropolitan area networks-- Specific requirements-- Part 3: Carrier sense multiple access with collision detection (CSMA/CD) access method and physical layer specifications.				
Response	Response Status U			
REJECT.				
The Front Matter is included in the draft, but is not open for ballot. Therefore the "Must Be Satisfied" aspect of the comment is not applicable.				
The CRG disagrees with the commenter. The text in the introductory material is exactly as provided in the introduction to IEEE Std 802.3-2018.				

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

Cl 9 SC 9.1 P 30 L 8 # i-212

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R Multidrop

Correction text is incorrect and baseline text is (now) incomplete.

SuggestedRemedy

Change text to read: "This clause specifies a repeater for use with half duplex IEEE 802.3 10 Mb/s baseband networks, with the exceptions of 10BASE-T1S (Clause 147). A repeater for any other IEEE 802.3 network type is beyond the scope of this clause."

Response Response Status U

REJECT.

The CRG disagrees with the commenter. The commenter's suggested remedy goes beyond the scope of this amendment and potentially excludes PHYs beyond the project's scope.

Cl 22 SC 22.2.2.4 P 31 L 17 # i-213

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R PLCA

BIG TICKET ITEM: Remove the changes you have here for PLCA. IFF you are going to insist that the PLCA lives in the Physical Layer then you don't get to change the layer interface to the MAC to accommodate a PLCA.

SuggestedRemedy

Remove the changes here and document them in clause 148. This is appropriate for a) keeping PLCA identified as being in the Physical Layer, b) Placing PLCA as a new supplementary MAC sublayer below the CSMA/CD sublayer or c) moving PLCA to a new standard for a MAC sublayer shim to Ethernet to convert CSMA/CD to CSMA/CA. This is my preferred solution which I would label "Standard for DeterminNet".

Response Response Status U

REJECT.

The CRG disagrees with the commenter. The layer interface to the MAC is not modified. The interface to the MAC, according to Clause 6 are the Physical Layer service specifications. These primitives are unmodified. Only the interface within the physical layer, the MII, which is between the RS and the PHY, is modified.

Cl 30 SC 30.3 P 37 L 31 # i-215

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R PLCA

I believe that the BEHAVIOUR of each of the following MAC attributes may need additional text to describe how it behaves (differently) when used in a PLCA network: 30.3.1.1.3 aSingleCollisionFrames; 30.3.1.1.4 aMultipleCollisionFrames; 30.3.1.1.9 aFramesWithDeferredXmissions; 30.3.1.1.10 aLateCollisions; 30.3.1.1.20 aFramesWithExcessiveDeferral; 30.3.1.1.30 aCollisionFrames; 30.3.1.1.31 aMACCapabilities; 30.3.1.1.32 aDuplexStatus

SuggestedRemedy

Examine each BEHAVIOUR for each of the listed attributes in the context of PLCA operation and augment the text definition of each BEHAVIOUR to cover operation in PLCA mode. This should explicitly cover whether an occurrence is an error in PLCA operation when such is not the case in CSMA/CD.

Response Response Status U

REJECT.

The CRG disagrees with the commenter. PLCA does not to change the behavior of these attributes.

Cl 45 SC 45.2.1.185 P 43 L 12 # i-220

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R Management

See my comment for 30.3.9.2.2. I believe there needs to be an entry here for each of the two types. 10BASE-T1S Type no longer should exist in this context.

SuggestedRemedy

Replace "10BASE-T1S" text with: "10BASE-T1SHD" AND "10BASE-T1SFD" as two separate entries, each with their own bit

Response Response Status U

REJECT.

The CRG disagrees with the commenter. Unlike clause 30 MAU Type, the PMA/PMD type in clause 45 PMA/PMD control registers does not specify modes of the PHY separately.

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CI 45 SC 45.2.1.185.2 P 43 L 27 # i-221

Thompson, Geoffrey Independent Consultant

Comment Type **TR** Comment Status **R** Management

See my comment for 30.3.9.2.2. I believe there needs to be discussion text here for each of the two 10BASE-T1S types. 10BASE-T1S Type no longer should exist in this context.

SuggestedRemedy

Replace "10BASE-T1S" text with: "10BASE-T1SHD" AND "10BASE-T1SFD" as two separate entries, each with their own bit

Response Response Status **U**

REJECT.

The CRG disagrees with the commenter. Unlike clause 30 MAU Type, the PMA/PMD type in clause 45 PMA/PMD control registers does not specify modes of the PHY separately.

CI 45 SC 45.2.3.68c P 55 L 5 # i-225

Thompson, Geoffrey Independent Consultant

Comment Type **TR** Comment Status **R** Registers

The text here says that "The default value for each bit of the ... control register should be chosen so that the initial state of the device upon power up or reset is a normal operational state without management intervention." It is not well placed or a requirement nor is it reflected in the table definitions.

SuggestedRemedy

There needs a) to be a "shall" statement b) so that there will be a corresponding entry in the PICS Pro Forma, c) placed so the text applies to the entire device below the MII and d) reflected with a default value declaration for each bit in each control register.

Response Response Status **U**

REJECT.

The CRG disagrees with the commenter. The statement is consistent in placement and wording with similar statement in similar control registers in Clause 45 of IEEE Std 802.3-2018. Like these registers, the 10BASE-T1S PCS control register controls test modes, and the statement is advisory. IEEE Std 802.3-2018 does not require this with a 'shall' statement, as the commenter suggests.

CI 45 SC 45.5.3.3 P 64 L 17 # i-231

Thompson, Geoffrey Independent Consultant

Comment Type **TR** Comment Status **R** Registers

The default states do not appear in the table for the referenced items. The PICS entries Y and N/A do not appear to me reference the univers of possibilities.

SuggestedRemedy

Expand answer table and indicate default values in the relevant register tables.

Response Response Status **U**

REJECT.

The CRG disagrees with the commenter. PICS entry for referenced text is consistent with practice in IEEE Std 802.3 clause 45. Reporting of default values is consistent with practice and style of Clause 45. Default values are generally not listed in PICS tables, but are in text of clause 45 subclauses.

The commenter did not provide a proposed resolution in sufficient detail to readily determine the specific wording of changes that will cause him to change his vote to approve (see SASB Ops Manual clause 5.4.3.2,b).

CI 146 SC 146.11.4.3 P 165 L 9 # i-241

Thompson, Geoffrey Independent Consultant

Comment Type **ER** Comment Status **A** AutoNeg

The statement is about a 2 DTE end-to-end system. The PICS is for a single DTE. The text here addresses a pair.

SuggestedRemedy

The text and result need to be restated for an appropriate test and result for a single transceiver.

Response Response Status **U**

ACCEPT IN PRINCIPLE.

The basic requirement (146.6.2) is a requirement on the user, and inappropriate.

Change "shall be configured" to "should be configured" in two places on P145 L46.

Delete PICS item MI2.

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CI 147 SC 147.1 P 167 L 23 # i-242

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A EZ

Grammer, this is a comparative sentence that doesn't actually have two things to compare.

SuggestedRemedy

Either actually do a comparison or get rid of the sentence.

Response Response Status W

ACCEPT IN PRINCIPLE.

Remove the following sentence

====

PLCA provides improved performance in terms of effective throughput and maximum transmission latency when operating in half-duplex mode over a mixing segment network.

====

CI 147 SC 147.3.7.2 P 188 L 3 # i-250

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A EZ

The leftmost transition into INACTIVE is confusing. It looks like it is an entrance from the text.

SuggestedRemedy

Put in a CR or, preferably, don't use a purely vertical transition line.

Response Response Status W

ACCEPT IN PRINCIPLE.

Editorial license to eliminate the visual artifact (top of hanging vertical line that goes into INACTIVE almost touches the 2nd underline in "link_hold_timer") as appropriate (without inserting CR to text).

CI 147 SC 147.4 P 190 L 32 # i-252

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A EZ

The statement: "The PMA provides ... communications to and from medium employing DME." would lead one to believe that the medium provides the DME. Such is not the case.

SuggestedRemedy

Change to: "The PMA utilizes DME to provide either half duplex communications, or, optionally full duplex communications to and from the medium.

Response Response Status W

ACCEPT.

CI 147 SC 147.5.1 P 193 L 1 # i-254

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A PMA Electrical

This clause and its sub-clauses don't say anything useful to specify the conformance requirements for 10BASE-T1S Physical Layer implementation. Saying something "may" be relevant also means it "may not" be relevant. It also means it is not an element of a conformance requirement.

SuggestedRemedy

Replace with: Applications for the specified device commonly have additional requirements that limit its conducted radio frequency emission and its susceptibility to electromagnetic interference coupling to the cabling system. Such requirements are beyond the scope of this standard.

Response Response Status U

ACCEPT IN PRINCIPLE.

Delete "Additional tests may be needed to verify EMC performance in various configurations, applications, and conditions."

Insert new second paragraph to 147.5.1 (after "manufacturer.")

"Applications for the specified device commonly have additional requirements that limit its conducted radio frequency emission and its susceptibility to electromagnetic interference. Such requirements are beyond the scope of this standard."

CI 147 SC 147.5.6 P 197 L 18 # i-256

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R PMA Electrical

I don't understand how the following text can be true: "The PMA local loopback function is optional" ...on a PMA where transmit is connected to receive.

SuggestedRemedy

Please clarify. I think you mean "The PMA local loopback test function is optional."

Response Response Status W

REJECT.

The CRG disagrees with the comment.

The PMA local loopback function is optional.

What this test mode does in

- half-duplex mode, is overriding part of the condition on the single-ended arrow that point into WAIT_SYNC in "Figure 147-7-PCS Receive state diagram", allowing receiving back transmitting station's own data.

- full-duplex mode, is suspending functionality that would prevent the transmitting station from receiving its own data.

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Cl 148 SC 148.1 P 214 L 12 # i-265

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A PLCA_SCOPE

The first sentence refers to PLCA as though it is already a familiar, well understood and well specified protocol that is familiar to the reader by the time he gets to clause 148 of IEEE Std. 802.3. Such is hardly the case.

SuggestedRemedy

Add the following text to the last paragraph: "PLCA modifies the CSMA/CD shared media access method so that assured access is provided via the collision free round robin protocol specified in this clause." This is a necessary but not sufficient addition. We'll leave further detail requirements to later in the clause..

Response Response Status U

ACCEPT IN PRINCIPLE.
Change "This clause specifies the optional Physical Layer Collision Avoidance (PLCA) capabilities. PLCA is defined for half-duplex mode of operation only. The PLCA RS is specified for operation with the PHY defined in Clause 147 (10BASE-T1S). PLCA is designed to work in conjunction with CSMA/CD and can be dynamically enabled or disabled via management interface."

to

"This clause specifies a reconciliation sublayer to provide optional Physical Layer Collision Avoidance (PLCA) capabilities among participating stations. The PLCA RS is specified for operation with Clause 147 (10BASE-T1S) PHYs operating in half-duplex multidrop mode. PLCA can be dynamically enabled or disabled via management interface.

When enabled, the PLCA RS aligns data from the MAC with transmission opportunities of the physical layer and maps the physical layer signals to PLS primitives towards the MAC. The use of PLCA-enabled physical layers in CSMA/CD half-duplex shared-medium networks provides enhanced performance relative to CSMA/CD without PLCA. PLCA-enabled nodes can coexist with nodes without PLCA enabled on the same mixing segment, all using 802.3 CSMA/CD."

Change "This clause specifies the optional Physical Layer Collision Avoidance (PLCA) capabilities. PLCA is defined for half-duplex mode of operation only. The PLCA RS is specified for operation with the PHY defined in Clause 147 (10BASE-T1S). PLCA is designed to work in conjunction with CSMA/CD and can be dynamically enabled or disabled via management interface."

to

"This clause specifies a reconciliation sublayer to provide optional Physical Layer Collision Avoidance (PLCA) capabilities among participating stations. The PLCA RS is specified for operation with Clause 147 (10BASE-T1S) PHYs operating in half-duplex multidrop mode.

PLCA can be dynamically enabled or disabled via management interface.

When enabled, the PLCA RS aligns data from the MAC with <scheduled> transmission opportunities of the physical layer <in a round robin fashion for PLCA participants> and maps the physical layer signals to PLS primitives towards the MAC. The use of PLCA-enabled physical layers in CSMA/CD half-duplex shared-medium networks provides enhanced performance relative to CSMA/CD without PLCA< by avoiding corruption of signals on the media itself>. PLCA-enabled nodes can coexist with nodes without PLCA enabled on the same mixing segment, all using 802.3 CSMA/CD."

Straw Poll #3: (pick one)

A: I am happy with an ACCEPT IN PRINCIPLE with the text above in angle brackets

B: I am happy with an ACCEPT IN PRINCIPLE with the text above without the text in angle brackets

C: I am unhappy with either A or B.

A: 1 B: 9 C: 2

Motion #9: Accept the text above as the response to comment i-265 without the text in angle brackets, as described by straw poll #3 choice B.

M: Peter Jones S: Phil Brownlee

Y: 21 N: 2 A:5 Motion Passes (technical >= 75%)

Cl 30 SC 30.3.9.2.3 P 39 L 4 # i-267

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R PLCA

(Wrong page & section ref. Put here for sorting purposes) In the current configuration of the draft it appears that the BEHAVIOUR of the Late Collision Counter (30.3.1.1.10 aLateCollisions) is incomplete.

SuggestedRemedy

Augment the referenced BEHAVIOUR with a PLCA conditional statement that describes what causes a late collision in the PLCA case including whether it is a normal or error condition.

Response Response Status U

REJECT.

The CRG disagrees with the commenter. A late collision at the MAC is a late collision. Its definition is unchanged and no additional text is needed.

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CI 148 SC 148.2 P 214 L 42 # i-268

Thompson, Geoffrey Independent Consultant

Comment Type TR Comment Status R PLCA_ID

This lack of a complete specification for full functionality is completely unprecedented for 10 Mb/s Ethernet and a major shortcoming. Plug and work, historically, has been a major factor in the success of Ethernet in face of the competition (which usually required a bunch of configuration before it would go on-line). Two examples of this in the history of Ethernet come to mind: (1) In the early days of 10 Mb/s full duplex and 100BASE-T early implementations of AutoNegotiation did not work very well. The failure of the promised plug 'n' play was a major marketing issue. (2) In the very first (3 Mb/s) version of Ethernet, DTEs only had 8 bit addresses. They had to have their addresses manually configured with push-on test leads as part of their installation process. This made the customer (most of whom were EEs or Computer Scientists) installation not possible and a technician had to be involved. Major network management problem.

SuggestedRemedy

Come up with and require availability of an automatic configuration app. No reason one shouldn't be able to use the CSMA/CD capability to (1) identify the stations on the local segment and (2) hand out the unique assigned node ID to each DTE.

Response Response Status U

REJECT.
CRG disagrees with the commenter:

The CRG specifically disagrees on these points:

- [1] PLCA is an optional feature that still operates under misconfiguration. See http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf
- [2] The draft does not constrain how the value for PLCA node ID is obtained. There are many different ways to implement this.
- [3] Defining an "automatic configuration app" may be a desirable feature, but is only one of a large set of possible solutions.
- [4] Default operation is with PLCA turned off, allowing interoperable plug-and-play, and opportunity for the management entity to configure for improved performance.

CI 148 SC 148.3 P 215 L 5 # i-270

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status R PLCA_SCOPE

The "Relationship with other IEEE standards" is incorrect with respect to the ISO Layer Model, 802 tradition and precedent and previous 802.3 projects that fiddled with shared media access methods[1]. When 802 did its adaptation of the ISO 7 Layer Model it subdivided the Data Link Layer into the LLC Sublayer and the MAC Sublayer specifically so that there was a separate place in the overall 802 model that "performs access control functions for the shared medium in support of the (common) LLC Sublayer[2]". Properly placed, PLCA would conform to this model, or (more properly) PLCA and CSMA/CD together would supply a complete MAC Sublayer for PLCA operation that would have a "Distinct Identity" that is different from CSMA/CD - Ethernet. To make things fit into the desired product implementation for fitting to existing IP the new PLCA block could have both a top MII to interface to existing designs and a bottom MII to attach to the PHY in the conventional manner. [1] Clause 64, Clause 99 [2] IEEE Std 802-1990 Overview & Architecture

SuggestedRemedy

Remove the entire PLCA clause (148) and associated textual material plus references from the draft. This will eliminate any scope issues and bring the draft into fully into line with the letter and expectations of the project paperwork at all levels (i.e. PAR, CSD, 802.3 project Objectives) [Further, thoughts not needed to resolve my required comment. I would fully support the creation of a new project to take place either within 802.3 or in a new 802 Working Group to standardize what we now call PLCA as a MAC sublayer element where the other required elements for a full DTE standard are provided by reference to the relevant portions of the 802.3 standard, as appropriate.]

Response Response Status U

REJECT.
The CRG disagrees with the commenter's description of layering and the proper placement of PLCA in the layering model. PLCA performs the functions delegated by the 802.3 layer model to the physical layer - carrier sense and collision detection. Commenter seems to posit an implementation which is not described in the amendment, where the PLCA sublayer interfaces to the MAC via an MII. (a "top MII" per the commenter), whereas PLCA maintains the layering and communicates to the MAC via the primitives PLS_CARRIER and PLS_SIGNAL defined in IEEE Std 802.3, and communicates with the remainder of the physical layer through the MII interface. For more detail on how PLCA relates to OSI layering please see http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf.

Additionally, the fact that PLCA-enabled half-duplex CSMA/CD stations may operate with and coexist with non-PLCA enabled half-duplex CSMA/CD stations on the same mixing segment is evidence that the PLCA RS is located beneath the CSMA/CD MAC and not a new MAC function in itself. See http://www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf and http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf

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Motion #10:

Resolve comment i-270 with the proposed reject response above:

M: Peter Jones

S: Tim Baggett

Y: 20 N: 0 A: 10 (motion passes)

Cl 148 SC 148.4.5.4 P 224 L 42 # i-272

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A Editorial

This is not a "should" in the usual standards sense of the word

SuggestedRemedy

Change "should" to "needs to be"

Response Response Status W

ACCEPT.

Cl 148 SC 148.4.6.1 P 227 L 51 # i-276

Thompson, Geoffrey Independent Consultant

Comment Type ER Comment Status A Editorial

3 different arcs with different terms coming into a join.

SuggestedRemedy

Shorten each arc and terminate separately with a "To C" symbol.

Response Response Status W

ACCEPT.

Cl 01 SC 1.3 P 26 L 38 # i-288

Schickeltanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status R Editorial

On link coupling attenuation limit it was decided to do the same as other limits but as being the first measurement standard specifying .1 MHz to add it in the list of references.

SuggestedRemedy

Add "IEC 62153-4-9 Ed2 Amd1: Coupling attenuation of screened balanced cables, triaxial method" in the list if Normative references

Response Response Status W

REJECT.

CRG disagrees with the commenter. IEC 62153-4-9 does not appear in the draft as a reference or normative requirement and, therefore, cannot be added as a Normative reference.

Cl 01 SC 1.4 P 29 L 51 # i-289

Schickeltanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status R Editorial

The definition of T1S shows the same wording as T1L. Only the reach is different. But this is not the only difference. It may be additionally a point to multipoint System and only half duplex. No optional PoDL is described. It may be also 25m long.

SuggestedRemedy

This needs some editing by a native speaker. As the commenter is not able to do this in good english he would grant editor liscence to do so

Response Response Status W

REJECT.

Proposed change in the comment does not contain sufficient detail so that the CRG can understand the specific changes that will satisfy the commenter.

The CRG disagrees with the commenter. The comment appears to desire some tutorial text on some certain aspects of Clause 146, Clause 147 and, possibly, Clause 104. The applicable clauses are referenced in the current text. Further information and exposition are not appropriate for the definition.

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CI **104** SC **104.3** P **87** L **4** # **i-294**

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type **TR** Comment Status **A** PoDL

Table 104-1a needs changes: 1-classes 10-12 for 36 V are outdated and should be deleted. 2-one more 60V class should be added

SuggestedRemedy

1-classes 10-12 for 36 V are outdated and should be deleted. 2-one more 60V class should be added

Response Response Status **W**

ACCEPT IN PRINCIPLE.

Accommodated by comment i-321.

Response to comment i-321 is:

Add additional power classes, including control registers, and adjust loop resistances as shown in http://www.ieee802.org/3/cg/public/May2019/stewart_3cg_01_0519_v3.pdf , by making the following changes: (references to "Stewart comment i-321 presentation" below are to this URL).

Modify Clause 30 to reflect new classes as follows:

Add new edit on (P41, L20):

Change text of BEHAVIOUR DEFINED AS section of 30.15.1.1.6 as shown:

"BEHAVIOUR DEFINED AS:

A read-only value that indicates the class of the detected PoDL PD as specified in Table 104-1 and Table 104-1a.

This value is only valid while a PD is being powered, that is the attribute

aPoDLPSEPowerDetectionStatus is reporting the enumeration "deliveringPower".

If a Clause 45 MDIO Interface to the PoDL PSE function is present, then this attribute may be derived from the PD Class and PD Extended Class bits specified in 45.2.9.2.8 and 45.2.9.3.1a."

Update the PoDL PSE Status registers to support the new classes as follows:

Modify Table 45-340 PoDL PSE Status 1 (P62) and Table 45-341 PoDL PSE Status 2 Register Bit Definitions (P63) to extend class codes as shown on slide 7 of Stewart comment i-321 presentation.

Update the PoDL Class register and Change the last sentence of the 45.2.9.2.8 (P62) from: When read as 0000 a Class 0 PD is indicated..., and when read as 1111 a Class 15 PD is indicated."

To:

"When read as 0000 a Class 0 PD is indicated..., and when read as 1111 the Class will be as indicated by the PD Extended Class (13.2.4:3) bits."

Add new subclause 45.2.9.3.1a PD Extended Class (13.2.4:3) to the draft, with editing instruction:

"Insert New subclause 45.2.9.3.1a PD Extended Class (13.2.4:3) after 45.2.9.3.1. Add text under 45.2.9.3.1a as below:"

"When read as 00 a Class 15 PD is indicated. Values of 01 and 1x are reserved."

Change the edit to last 3 sentences of the first paragraph of 104.2 Link segment (P86 L28-31) from:

"The link segment dc loop resistance shall be less than 59 ohm for Classes 10 and 13. The link segment dc loop resistance shall be less than 39 ohm for classes 11 and 14. The link segment dc loop resistance shall be less than 36 ohm for classes 12 and 15."

To "The link segment dc loop resistance shall be less than 65 ohm for classes 10 and 13. The link segment dc loop resistance shall be less than 25 ohm for classes 11 and 14. The link segment dc loop resistance shall be less than 9.5 ohm for Classes 12 and 15"

And, change the edit to Table 104-1a (P87 L1-22) deleting the last two rows (Cable mm (AWG) and Cable Length (m)) and modifying the entries in classes 10 through 15, as shown on Slide 10 of Stewart comment i-321 presentation.

Change Table 104-4 items 6 and 7 (Page 89 L22) to change class on existing values to Classes 0 to 9, and add new row for requirements on Classes 10 to 15 as shown on slide 11 of Stewart comment i-321 presentation.

Add new entries to Table 104-7 PD Power Supply limits table (Page 91 line 20), inserting new rows 4f, 4g and 5f, 5g for turn on and turn off voltages for the 2 new groups of classes respectively as shown on slide 12 of Stewart comment i-321 presentation, and add new entry for item 7 in Table 104-7 Inrush enable delay time for Classes 10 to 15 as shown on slide 13 of Stewart comment i-321 presentation.

Change Table 104-8 item 1 (Page 95 line 10) to change class on existing values of PSE Pull-up Voltage to apply to Classes 0 to 9, and add new row for requirement on Classes 10 to 15 as shown on slide 14 of Stewart comment i-321 presentation.

Editorial license granted to craft necessary Editing Instruction text in accordance with IEEE style.

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CI 104 SC 104.3 P 87 L 19 # i-295

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status A PoDL

In Table 104-1a cable related limits are specified in the last two lines. As this is outside scope it should be replaced just by the loop resistance, giving the IEC cable group the task to define the cables. In Annex 146B there is an informative Table 146B-1 showing a lot of details.

Suggested Remedy

The new classes 10 to 13 should show in the last row 9.25; 15; 25; 65 Ohm loop resistance at 60 C

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodated by comment i-321.

Response to comment i-321 is:

Add additional power classes, including control registers, and adjust loop resistances as shown in http://www.ieee802.org/3/cg/public/May2019/stewart_3cg_01_0519_v3.pdf, by making the following changes: (references to "Stewart comment i-321 presentation" below are to this URL).

Modify Clause 30 to reflect new classes as follows:

Add new edit on (P41, L20):

Change text of BEHAVIOUR DEFINED AS section of 30.15.1.1.6 as shown:

"BEHAVIOUR DEFINED AS:

A read-only value that indicates the class of the detected PoDL PD as specified in Table 104-1 and Table 104-1a .

This value is only valid while a PD is being powered, that is the attribute

aPoDLPSEPowerDetectionStatus is reporting the enumeration "deliveringPower".

If a Clause 45 MDIO Interface to the PoDL PSE function is present, then this attribute may be derived from the PD Class and PD Extended Class bits specified in 45.2.9.2.8 and 45.2.9.3.1a ."

Update the PoDL PSE Status registers to support the new classes as follows:

Modify Table 45-340 PoDL PSE Status 1 (P62) and Table 45-341 PoDL PSE Status 2 Register Bit Definitions (P63) to extend class codes as shown on slide 7 of Stewart comment i-321 presentation.

Update the PoDL Class register and Change the last sentence of the 45.2.9.2.8 (P62) from: When read as 0000 a Class 0 PD is indicated..., and when read as 1111 a Class 15 PD is indicated."

To:

"When read as 0000 a Class 0 PD is indicated..., and when read as 1111 the Class will be as indicated by the PD Extended Class (13.2.4.3) bits."

Add new subclause 45.2.9.3.1a PD Extended Class (13.2.4.3) to the draft, with editing instruction:

"Insert New subclause 45.2.9.3.1a PD Extended Class (13.2.4.3) after 45.2.9.3.1. Add text under 45.2.9.3.1a as below:"

"When read as 00 a Class 15 PD is indicated. Values of 01 and 1x are reserved."

Change the edit to last 3 sentences of the first paragraph of 104.2 Link segment (P86 L28-31) from:

"The link segment dc loop resistance shall be less than 59 ohm for Classes 10 and 13. The link segment dc loop resistance shall be less than 39 ohm for classes 11 and 14. The link segment dc loop resistance shall be less than 36 ohm for classes 12 and 15."

To "The link segment dc loop resistance shall be less than 65 ohm for classes 10 and 13. The link segment dc loop resistance shall be less than 25 ohm for classes 11 and 14. The link segment dc loop resistance shall be less than 9.5 ohm for Classes 12 and 15"

And, change the edit to Table 104-1a (P87 L1-22) deleting the last two rows (Cable mm (AWG) and Cable Length (m)) and modifying the entries in classes 10 through 15, as shown on Slide 10 of Stewart comment i-321 presentation.

Change Table 104-4 items 6 and 7 (Page 89 L22) to change class on existing values to Classes 0 to 9, and add new row for requirements on Classes 10 to 15 as shown on slide 11 of Stewart comment i-321 presentation.

Add new entries to Table 104-7 PD Power Supply limits table (Page 91 line 20), inserting new rows 4f, 4g and 5f, 5g for turn on and turn off voltages for the 2 new groups of classes respectively as shown on slide 12 of Stewart comment i-321 presentation, and add new entry for item 7 in Table 104-7 Inrush enable delay time for Classes 10 to 15 as shown on slide 13 of Stewart comment i-321 presentation.

Change Table 104-8 item 1 (Page 95 line 10) to change class on existing values of PSE Pull-up Voltage to apply to Classes 0 to 9, and add new row for requirement on Classes 10 to 15 as shown on slide 14 of Stewart comment i-321 presentation.

Editorial license granted to craft necessary Editing Instruction text in accordance with IEEE style.

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CI 146 SC 146.7.1.2 P 149 L 27 # i-299

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status A Link Segment

Return loss limits were changed often. The latest values were from a measured cable. Due to the high insertion loss the reach is much less then 1000m violating the 1000m objective. But there is an installed base and it should be a better route to capture this.

SuggestedRemedy

As the majority of the cables have an impedance around 100 ohm as a compromise return loss should be 15 dB from 1 MHz to 20 MHz and below 9+9f. To capture the special cable with high insertin loss there would be 2 exceptions. Long links could go down to 13 dB. The critical 10m should be avoided in short runs.

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodated by i-111. The resolution to i-111 is:
ACCEPT.
The suggested remedy of i-111 is:

Change the value 13.5 dB to 13 dB within Equation 146-13. Change the frequency dependency of the RL below 0.5 MHz from $9 + 9 \times f$ to $9 + 8 \times f$.

CI 146 SC 146.7.1.5 P 151 L 8 # i-300

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status A Link Segment

As conducted immunity is the same fort E1 and E2 the coupling attenuation should be the same fort E1 and E2 too.

SuggestedRemedy

Change the E1 value in Table 146-6 from 40 to 50

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodated by comment i-113. The resolution to i-113 is:
ACCEPT IN PRINCIPLE.

Change the coupling attenuation value for E1 from ≥ 40 dB to ≥ 50 dB.

CI 146 SC 146.7.1.4 P 150 L 39 # i-301

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status A Link Segment

As conducted immunity is the same for E1 and E2 TCL should be the same fort E1 and E2 too.

SuggestedRemedy

in table 146-5 change from .1 to 10 MHz to ≥ 50 and from 10 to 20 MHz to $50 - 20 \log(f/10)$ for E1 and E2.

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodate by comment i-112. The resolution to comment i-112 is:
ACCEPT IN PRINCIPLE.

Remove table 146-5 and replace this table by a table with the following entries for the TCL values: first row: of 0.1 MHz $\leq f \leq 10$ MHz: for E1: ≥ 50 dB; for E2: ≥ 50 dB, second row: 10 MHz $< f \leq 20$ MHz: for E1: $\geq 50 - 20 \log_{10}(f / 10)$ dB; for E2: $\geq 50 - 20 \log_{10}(f / 10)$ dB. Remove the specification of the ELTCTL values.

Grant editorial license to adjust text to accommodate removal of the ELTCTL values.

Change 146.7.1 to recognize UTP for 10BASE-T1L, change last sentence of first paragraph of 146.7.1 (P146 L50) as shown:
The transmission characteristics for the 10BASE-T1L link segment are specified to support applications requiring long reach such as industrial and process control, for up to at least 1000 m. 10BASE-T1L segments may be shielded or screened, consistent with the specification in 146.7.1.6 and 146.7.2 or unshielded consistent with the specifications in 146.7.1.6 and 146.7.1.4.

Change 146.8.2 MDI electrical specification (P155 L11)

Change: The MDI connector mated with a specified single balanced-pair connector shall meet the electrical requirements specified in 146.7.

To: The electrical requirements specified in 146.5.4 and 146.5.5 shall be met when the PHY is connected to the MDI connector mated with the specified plug connector.

Insert new subclause 146.8.4 MDI mode conversion loss, following 146.8.3 (P155 L13) and renumber subsequent subclauses as shown:

146.8.4 MDI mode conversion loss

Mode conversion LCL (Sdc11) or TCL (Scd11) of the PHY measured at the MDI shall meet the values determined using Equation (146-xx) at all frequencies from 0.1 MHz to 20 MHz.

ConversionLoss(f) \geq { 25 for 0.1 MHz $\leq f \leq 10$ MHz
25-20xlog₁₀ (f/10) for 10 MHz $< f \leq 20$ MHz} dB

where f is the frequency in MHz.

PICS:

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Add new PICS item after MDI2 (P166 L9) and renumber subsequent PICS items
MDI3 MDI mode conversion loss Shall meet Equation 146-xx M Yes []

CI 146 SC 146.7.1.4 P 150 L 39 # i-302

Schicketanz, Dieter University of Applied Science Reutlingen

Comment Type TR Comment Status A Link Segment

No specific limit could be elaborated for ELTCTL

Suggested Remedy

Delete this requirement in table 146-5

Response Response Status W

ACCEPT IN PRINCIPLE.

Accommodate by comment i-112. The resolution to comment i-112 is:
ACCEPT IN PRINCIPLE.

Remove table 146-5 and replace this table by a table with the following entries for the TCL values: first row: of 0.1 MHz $\leq f \leq 10$ MHz: for E1: ≥ 50 dB; for E2: ≥ 50 dB, second row: 10 MHz $< f \leq 20$ MHz: for E1: $\geq 50 - 20 \log_{10}(f / 10)$ dB; for E2: $\geq 50 - 20 \log_{10}(f / 10)$ dB. Remove the specification of the ELTCTL values.

Grant editorial license to adjust text to accommodate removal of the ELTCTL values.

Change 146.7.1 to recognize UTP for 10BASE-T1L, change last sentence of first paragraph of 146.7.1 (P146 L50) as shown:
The transmission characteristics for the 10BASE-T1L link segment are specified to support applications requiring long reach such as industrial and process control, for up to at least 1000 m. 10BASE-T1L link segments may be shielded or screened, consistent with the specification in 146.7.1.6 and 146.7.2 or unshielded consistent with the specifications in 146.7.1.6 and 146.7.1.4.

Change 146.8.2 MDI electrical specification (P155 L11)
Change: The MDI connector mated with a specified single balanced-pair connector shall meet the electrical requirements specified in 146.7.
To: The electrical requirements specified in 146.5.4 and 146.5.5 shall be met when the PHY is connected to the MDI connector mated with the specified plug connector.

Insert new subclause 146.8.4 MDI mode conversion loss, following 146.8.3 (P155 L13) and renumber subsequent subclauses as shown:
146.8.4 MDI mode conversion loss
Mode conversion LCL (Sdc11) or TCL (Scd11) of the PHY measured at the MDI shall meet the values determined using Equation (146-xx) at all frequencies from 0.1 MHz to 20 MHz.

ConversionLoss(f) = { 25 for 0.1 MHz $\leq f \leq 10$ MHz
25-20xlog₁₀ (f/10) for 10 MHz $< f \leq 20$ MHz } dB

where f is the frequency in MHz.

PICS:

Add new PICS item after MDI2 (P166 L9) and renumber subsequent PICS items
MDI3 MDI mode conversion loss Shall meet Equation 146-xx M Yes []

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-302

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CI 146 SC 146.3.3 P 117 L 18 # i-342

Law, David

Hewlett Packard Enterprise

Comment Type TR Comment Status A State Diagram

Is it correct that 'The PCS Transmit function shall conform to the PCS Transmit state diagram in Figure 146-5 ...' and that 'In each symbol period, PCS Transmit generates a symbol An provided to the PMA ...'? The PCS Transmit state diagram in Figure 146-5 changes state based on STD being true, with STD being an alias for symb_triplet_timer_done, and the output of the PCS Transmit state diagram is tx_symb_triplet which is defined in subclause 146.3.3.1.1 'Variables' as 'A triplet of ternary symbols generated by the PCS Transmit function after 4B3T encoding.'

I think the problem is that there is another function within the PCS Transmit function that is missing from the PCS Transmit state diagram in Figure 146-5, the 'multiplexor' shown in Figure 146-6 'PCS transmit symbol generation'. This 'multiplexor' function operates at the symbol clock rate and serialises the tx_symb_triplet code-groups output by the PCS Transmit state diagram into individual symbols.

This may also explain when subclause 146.3.3.1.3 'Timers' defines the symb_timer that is not used in the PCS Transmit state diagram in Figure 146-5.

Suggested Remedy

[1] Insert a new subclause 146.3.3.2 titled 'PCS Transmit multiplexor state diagram' that reads 'In each symbol period, the PCS Transmit multiplexor generates a ternary symbol that can take the values of {-1, 0, +1} and passes it to the PMA sublayer via the PMA_UNITDATA.request primitive. The nominal symbol clock frequency is specified in 146.5.4.5.'. Renumber the following subclauses as required.

[2] Add a new subclause 146.3.3.2.1 titled 'Variables' that reads:

pcs_reset

The pcs_reset parameter set by the PCS Reset function.

Values: TRUE or FALSE

tx_symb_vector

A ternary symbol generated through serialization of tx_symb_triplet. This symbol is conveyed to the PMA as the parameter of a PMA_UNITDATA.request(tx_symb_vector) service primitive.

Values: A ternary transmit symbol. The ternary symbols may take on one of the values {-1, 0, +1}.

tx_symb_triplet{TAn, TBn, TCn}

A triplet of ternary symbols generated by the PCS Transmit state diagram. The element TAn is the first ternary symbol transmitted; TCn is the last ternary symbol transmitted.

Value: A triplet of ternary transmit symbols. Each of the ternary symbols may take on one of the values {-1, 0, +1}.

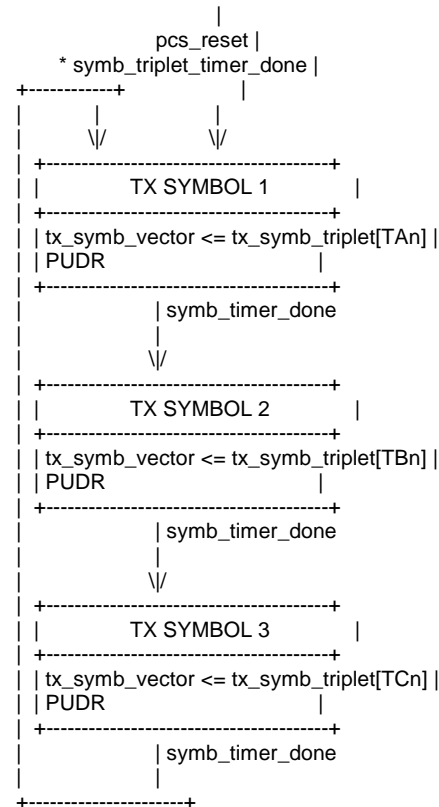
[4] Add a new subclause 146.3.3.2.2 titled 'Timers'. Move the symb_timer definition from subclause 146.3.3.1.3 Timers to this new subclause.

[5] Add a new subclause 146.3.3.2.4 'Abbreviations' that reads:

PUDR

Alias for PMA_UNITDATA.request(tx_symb_vector).

[6] Insert a new Figure 145-6 shown below (view using a non-proportional font such as courier), renumbering the following figures as required.



[7] Add text to subclause 146.3.3 'PCS Transmit' that reads 'The PCS Transmit function shall conform to the PCS Transmit state diagram in in Figure 146-5 and the PCS Transmit multiplexor state diagram in 146-6, and their associated state variables, functions, timers, and messages.'

[8] Delete the first and second paragraphs of subclause 146.3.3.1 'PCS Transmit State Diagram' as these not functions of the PCS Transmit state diagram which is what this

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subclause is describing, change the text '... the PCS Transmit function passes ...' in the current third paragraph to read '... the PCS Transmit state diagram passes ...'.

Response *Response Status* **W**
ACCEPT.

Cl **146** *SC* **146.3.3.2.6** *P* **123** *L* **51** *#* **i-359**
Law, David Hewlett Packard Enterprise
Comment Type **TR** *Comment Status* **A** *EZ*

Subclause 146.3.3.2.6 'Generation of symbol sequence' states that 'A ternary triplet (TAn, TBn, TCn) shall be sent in the following order: TAn, TBn, TCn, TAn+1, TBn+1, TCn+1 ...'. The following Tables, 146-1 to 146-3, then define the various ternary triplet code-groups. Of these three tables only one, Table 146-3, defines which symbols are TAn, TBn, TCn.

SuggestedRemedy

To ensure the unambiguous definition of the transmission order, define which symbols are TAn, TBn, TCn in Table 146-1 and 146-2.

Response *Response Status* **W**
ACCEPT IN PRINCIPLE.

Add a note under Table 146-1:
"NOTE - The Ternary Triplet is (TAn, TBn, TCn)."

Add "(TAn, TBn, TCn)" under "Disparity = 1", "Disparity = 2", "Disparity = 3", and "Disparity = 4"

Cl **146** *SC* **146.3.4.1.3** *P* **128** *L* **4** *#* **i-363**
Law, David Hewlett Packard Enterprise
Comment Type **TR** *Comment Status* **A** *Editorial*

The variable 'rcv_jab_detected' used in the open arrow entry to the WAIT SCRAMBLER and LINK FAILED states in Figure 146-8 'PCS receive state diagram (part a)' is not defined in subclause 146.3.4.1.1 'Variables'. On review of the draft, while I can find information about the transmit jabber, it is not clear to me where rcv_jab_detected would be sourced from, or when it would be asserted.

SuggestedRemedy

Add a definition for the rcv_jab_detected variable to subclause 146.3.4.1.1 'Variables', or remove rcv_jab_detected from the open arrow entry to the WAIT SCRAMBLER and LINK FAILED states.

Response *Response Status* **W**
ACCEPT IN PRINCIPLE.
Accommodated by comment i-164.
Response to comment i-164 is:
ACCEPT.

Suggested remedy to comment i-164 is:
Change rcv_jab_detected to rcv_overrun_detected in Figure 146-8 (2 instances, lines 4 & 5)

Cl **146** *SC* **146.3.4.1.3** *P* **128** *L* **5** *#* **i-364**
Law, David Hewlett Packard Enterprise
Comment Type **ER** *Comment Status* **A** *Editorial*

Subclause 146.1.3.1 'State diagram notation' states that 'The conventions of 21.5 are adopted with the extension that some states in the state diagrams use an IF-THEN-ELSE-END construct to condition which actions are taken within the state.'. Table 21-1 'State diagram operators' in IEEE Std 802.3-2018 subclause 21.5.4 'Operators' lists the characters '()' as 'Indicates precedence'. Based on this the use of '[']' in state diagram transitions should be replaced with '()'.

SuggestedRemedy

Replace the three instances of '[']' used to indicate precedence in Figure 146-8 state diagram transitions with '()'.

Response *Response Status* **W**
ACCEPT.

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CI 146 SC 146.3.4.1.3 P 128 L 25 # i-365

Law, David Hewlett Packard Enterprise

Comment Type ER Comment Status A Editorial

Subclause 146.1.3.1 'State diagram notation' states that 'The conventions of 21.5 are adopted with the extension that some states in the state diagrams use an IF-THEN-ELSE-END construct to condition which actions are taken within the state.'. Table 21-1 'State diagram operators' in IEEE Std 802.3-2018 subclause 21.5.4 'Operators' lists the 'Not Equal To' character <<http://unicode.org/cldr/utility/character.jsp?a=2260>> as 'Not equals'. I assume this is what is meant by the use '!=' in Figure 146-8, based on this the use of '!=' in state diagram transitions should be replaced with the 'Not Equal To' character.

SuggestedRemedy

Replace the eight instances of '!=' used in Figure 146-8 state diagram transitions with the 'Not Equal To' character <<http://unicode.org/cldr/utility/character.jsp?a=2260>>.

Response Response Status W

ACCEPT.

CI 148 SC 148.4.4 P 218 L 17 # i-372

Law, David Hewlett Packard Enterprise

Comment Type TR Comment Status A Editorial

Clause 148, which specifies the PLCA Reconciliation Sublayer (RS), cannot place requirement (shall statements) on the connected PHY. Subclause 1.1.3.2 'Compatibility interfaces' of IEEE Std 802.3-2018 defines the MII as a compatibility interface. As such an implementer is permitted to implement only the Clause 148 RS, however having shall statements related to the PHY results in requirements that this RS implementer will be unable to satisfy. This can be seen in the PICS where a Clause 148 RS implementer is required to respond to questions about the PHY such as PLCA2 and PLCA3 where the status is M and the support is Yes[. In addition a PLCA RS supports PHYs other than 10BASE-TS1.

SuggestedRemedy

- [1] Change 148.4.4 'Requirements for the PHY' to read 'In order to support Physical Layer Collision Avoidance the RS has to be connected to a 10BASE-TS1 PHY.
- [2] Remove requirements on the PHY from Clause 148.

Response Response Status W

ACCEPT IN PRINCIPLE.
Implement proposed remedy [1].

At page 218, line 29, change "the PHY shall encode and transmit a signal" to "the PHY encodes and transmits a signal"

At page 218, line 44, change "Upon the reception of this request, the RX_DV signal shall not be asserted" to "Upon the reception of this request, the RX_DV signal is not asserted"

At page 219, line 3, change "When the PHY receives a BEACON, it shall indicate this information" to "When the PHY receives a BEACON, it indicates this information"

At page 219, line 11, change "When the PHY receives a COMMIT from the line, it shall indicate" to "When the PHY receives a COMMIT from the line, it indicates"

Delete the following PICS entries in 148.5.3.3: PLCA2, PLCA3, PLCA4, PLCA5, PLCA8.

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CI 148 SC 148.4.6.1 P 227 L 31 # i-385

Law, David Hewlett Packard Enterprise

Comment Type TR Comment Status A Editorial

There is no definition for the mean of the subscript n-a in relation to plca_txd.

SuggestedRemedy

Define the meaning of the subscript n-a in subclause 148.4.6.1.

Response Response Status W

ACCEPT IN PRINCIPLE.

148.4.3.1.2 Change "The values ONE and ZERO are conveyed by the PLCA variables plca_txd<3>, plca_txd<2>, plca_txd<1>, and plca_txd<0>, each of which conveys"

to

"The values ONE and ZERO are conveyed by the individual bits of the four-bit variable plca_txd<3:0>. Each bit of plca_txd<3:0> conveys..."

Additionally, on page 228, line 11, change the description of plca_txd as follows:
Change from "plca_txd See 148.4.3.1.2"

to

"plca_txd<3:0> A four-bit data value conveying a nibble of data to transmit from four successive PLS_DATA.request(OUTPUT_UNIT) primitives where OUTPUT_UNIT has a value of ONE or ZERO. See 148.4.3.1.2."

CI 148 SC 148.4.6.2 P 228 L 40 # i-388

Law, David Hewlett Packard Enterprise

Comment Type TR Comment Status A Editorial

As noted in Figure 148-2 'PLCA functions within the Reconciliation Sublayer (RS)' and elsewhere in the IEEE P802.3cg draft, the TX_CLK is sourced from the PHY. In addition the relationship between MCD, that defines the when TXD, TX_EN and TX_ER change value in the TRANSMIT state, and phase of TX_CLK needs to be defined to meet subclause 22.3.1. MCD should therefore be derived from a free-running timer that expires synchronously with the rising edge of TX_TCLK.

SuggestedRemedy

[1] Add a new subclause as follows:

148.4.6.5 Abbreviations
MCD
Alias for mii_clock_timer_done.

[2] Add a new timer to subclause 148.4.6.4 as follows:

mii_clock_timer
A continuous free-running timer that shall expire synchronously with the rising edge of TX_TCLK.
Restart time: Immediately after expiration; restarting the timer resets the condition mii_clock_timer_done.'
Duration: see 22.2.2.1.

Response Response Status W

ACCEPT.

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CI 148 SC 148 P 214 L 1 # i-390

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA_SCOPE

[PAR] PLCA Reconciliation Sublayer (RS) contain specifications that handles contention avoidance and collision handling as well as access control. Media Access Control (MAC) specification is not a part of this Physical Layer project, as stated in this PAR scope: "5.2.b. Changes in scope of the project: Specify additions to and appropriate modifications of IEEE Std 802.3 to add 10 Mb/s Physical Layer (PHY) specifications and management parameters for operation, and associated optional provision of power, using a single balanced pair of conductors.", whereas the MAC definition is in CL 4.1.1 of IEEE 802.3-2018 states: "...The MAC sublayer defines a medium-independent facility...b) Media Access Management
1) Medium allocation (collision avoidance)
2) Contention resolution (collision handling).."

Furthermore, Reconciliation Sublayer, as defined in the same parent document IEEE 802.3-2018, in 1.4.425 states "1.4.425 Reconciliation Sublayer (RS): A mapping function that reconciles the signals at the Media Independent Interface (MII) to the Media Access Control (MAC)-Physical Signaling Sublayer (PLS) service definitions. (See IEEE Std802.3, Clause 22.)". PLCA RS claims to be an RS, but does NOT simply map PLS to MII, but performs 1) Medium allocation (collision avoidance) -- as the title says ("physical layer Collision Avoidance), 2) Contention resolution (collision handling). PLCA performs Medium Access control function (MAC).

SuggestedRemedy

Align this draft to the approved PAR (14-May-2018)by deleting CL148 in its entirety (pages 214 through 234, inclusive) and any changes associated with such deletion. Alternatively, submit a new PAR that substantially reflect this project content, including a MAC specification in the scope, and provide approved PAR with such revised scope. If a new PAR is submitted with MAC specification in scope, then re-open and seek technical contributions with regards to the new scope.

Response Response Status W

REJECT.

The CRG disagrees with the commenter.

Specifically the CRG disagrees that the Clause 148 PLCA RS is a new MAC. It does not meet the requirements for a MAC, and leaves the MAC functionality with Clause 4. In fact, the network could not work without the MAC functionality.

Additionally, the Task Force has previously considered the issues raised by the commenter and has also reviewed and evaluated contributions that rebut the commenter's assertions.

The CRG believes the PLCA RS only performs functions delegated to the physical layer, which the MAC uses to perform its functions. For example, see www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf for further information.

See also http://www.ieee802.org/3/cg/public/adhoc/brandt_020619_3cg_01a_adhoc.pdf for a discussion of layering as it relates to this draft.

CI 147 SC 147.1 P 167 L 12 # i-391

Kim, Yongbum

NIO

Comment Type TR Comment Status R Modes

[CSD] CSD/Broad Market Potential is no longer assured in this project when the half-duplex point to point link segment PHY operation, traditionally associated with broad market with use of star-wired multi-port repeaters (e.g. 10BASE-T hubs/repeaters) is not supported.

An explicit statement of mandatory operation of this PHY:

"The 10BASE-T1S PHY is specified to be capable of operating at 10 Mb/s in several modes. All 10BASE-T1S PHYs can operate as a half-duplex PHY with a single link partner over a point-to-point link segment defined in 147.7..."

An explicit statement of non-support of repeaters:

Pg 30, CL9.1 proposed change states "This clause specifies a repeater for use with IEEE 802.3 10 Mb/s baseband networks, with the exceptions of 10BASE-T1L (Clause 146) and 10BASE-T1S (Clause 147)...."

Repeating the concern -- only PHY operation that is mandatory is point-to-point link without any allowance for repeaters (i.e. exactly two node network) operating in half-duplex, contention resolution network does NOT have broad market potential.

SuggestedRemedy

Delete market-potential irrelevant PHY that supports exactly two node network over a point-to-point link, and make one of the more market-potential-relevant PHYs from "...additionally, there are two mutually exclusive optional operating modes: a full-duplex point-to-point mode over the link segment defined in 147.7, and a half-duplex shared-medium mode, referred to as multidrop mode,..."
and update the CSD/Broad Market Potential as appropriate.

Response Response Status W

REJECT.

CRG disagrees with the commenter. The clause contains one PHY with three modes, with a common-denominator for interoperability. CRG disagrees with the commenter on the relevance of the mandatory mode of operation (half-duplex point-to-point). There are multiple methods of inter-linking point-to-point half-duplex segments, without the use of clause 9 repeaters using multiple topologies of choice, allowing larger networks (with more than 2 stations). A bridge is considered to be an element in common networks. Bridges have functionally replaced repeaters in most networks.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-391

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and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

Cl 147 SC 147.1 P 167 L 13 # i-392

Kim, Yongbum

NIO

Comment Type TR Comment Status R Modes

[CSD] CL147 title states a single PCS/PMA type 10BASE-T1S. But in reality, it has three PHYs. Two of the three PHYs not compatible and do not interoperate. This issue is explicitly stated with "mutually exclusive" operation, which equals not-compatible and not interoperate.

"All 10BASE-T1S PHYs can operate as a half-duplex PHY with a single link partner over a point-to-point link segment defined in 147.7, and, additionally, there are two mutually exclusive optional operating modes: a full-duplex point-to-point mode over the link segment defined in 147.7, and a half-duplex shared-medium mode, referred to as multidrop mode, capable of operating with multiple stations connected to a mixing segment, defined in 147.8."

Full-duplex P2P PHY implements echo cancelation. Half-duplex shared medium does not. They do not interoperate with each other. These may share the similar or substantially same PCS, these do not share PMAs. They do not interoperate; PMAs are substantially different; they are different PHYs. These two PHYs should be, at least, designated as different type.

If the argument is made that these two PHYs must support P2P half-duplex (therefore interoperate), and in such case, they interoperate, then we should also be reminded that P2P half-duplex (with no provision for repeaters) allow for exactly two node network collision based network. Exactly two node, and only two node, connectivity does not network make.

SuggestedRemedy

Either structure CL147 to specify two different PHY types, P2P full-duplex PHY, and 'multi-drop' half-duplex PHY. They do not interoperate with each other, therefore they are not the same type of PHY.

Or split CL147 into a CL on common PCS, and two more CLs, one for each of the two separate PMA for respective PHYs.

With regards to the P2P half-duplex PHY, please delete it from this draft. The value and use of exactly two (and only two) node network is very limited to say the least.

Response Response Status W

REJECT.

The CRG disagrees with the commenter. The clause contains one PHY with a common-denominator for interoperability. CRG disagrees with the commenter that the modes do not interoperate.

The commenter seems to make multiple incorrect interpretations of the text. Mutual exclusivity is with regards to the fact that a single PHY cannot operate in half-duplex and full-duplex at the same time. The PHY contains a single PCS, and a single PMA is specified along with options.

Regarding the half-duplex point-to-point functionality, there are multiple methods of inter-linking point-to-point half-duplex segments, without the use of clause 9 repeaters using multiple topologies of choice, allowing larger networks (with more than 2 stations). A bridge is considered to be an element in common networks. Bridges have functionally replaced repeaters in most networks.

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CI 148 SC 148 P 214 L 1 # i-393

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA_SCOPE

[CSD] CSD/Economic Feasibility statements in CSD document is not valid for CL148 PLCA operation.

The project CSD states that "

The cost factors for Ethernet components and systems are well known. The proposed project may introduce new cost factors which can be quantified.

-The reduction in the number of legacy networks requiring specialized components, expertise, and gateways in the targeted markets is anticipated to result in a significant drop in both installation and operational costs."

While the cost factors for Ethernet is well known, this project introduces the new requirements that has not been a part of Ethernet. This project requires each node to be assigned a unique and sequential (as in little to no gaps in number sequence) node identifier to be assigned to each PHY, and allocate and assign a special node identifier value of zero to a 'master node' that is responsible for sending special 'beacon' frame. This project requires that the configuration is assured (outside of this draft standard) that node identifier of zero is present, and only one of such node identifier is present. This operation described in this project cannot reasonably assume that this new behavior requirement could inherit "well known Ethernet cost factors". Also this project cannot reasonably assert "drop in both installation and operational costs" when additional configuration of node assignment and behaviors are required and without any specification on how they are done.

CSD/Economic Feasibility with regard to other clauses, other than CL148, are not in question.

SuggestedRemedy

CSD/Economic Feasibility with regard to CL148 PLCA operation is no longer valid and grossly incorrect. Appropriate changes to the CSD/Economic Feasibility to be made and to be approved.

Response Response Status W

REJECT.

CRG disagrees with the commenter.

Both the 802.3 working group and the 802 Executive Committee have confirmed the CSD responses.

Any changes to the CSD documents, as the commenter requests, would be handled through internal 802 processes which are outside the SA ballot process.

With respect to the issues raised by the commenter regarding node ID assignment, the CRG specifically disagrees on these points:

[1] PLCA is an optional feature that still operates under misconfiguration. See http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf

[2] The draft does not constrain how the value for PLCA node ID is obtained. There are many different ways to implement this.

[3] Default operation is with PLCA turned off, allowing interoperable plug-and-play, and opportunity for the management entity to configure for improved performance.

The CRG additionally disagrees on these points the commenter asserts:

[1] PLCA node IDs do not need to be sequential

[2] There is no such description of master node in the draft

[3] The BEACON is not a frame, it is a 20 bit long signal on the line which carries no information apart from its own presence. It is conceptually not different from IDLE signals which most physical layers use to retrieve clocking information.

CI 22 SC 22 P 31 L 13 # i-394

Kim, Yongbum

NIO

Comment Type TR Comment Status R MII

[CSD] CSD/Compatibility states "As a PHY amendment to IEEE Std802.3, the proposed project will use MII, and follow the existing format and structure of IEEE 802.3 protocol-independent specification of managed objects." It does NOT state that it will change MII and then use the modified version of MII. It states that this project will use MII. This project violates the stated compatibility statement. In addition, MII is widely used and deployed exposed interoperability interface, still with large installed based that is difficult to determine (installation spread over 10~15 years, starting 20+ years ago). One of the test whether an interface has been materially changed is by looking at the PICS in CL22.8.3 and there are 5 entries that changes the requirements to the installed base of MII.

SuggestedRemedy

Reverse all material changes to CL22 and make appropriate changes in other clauses of this project to make it work with CL22. If this cannot be done, then appropriate changes to the CSD/Compatibility with regard to CL22 be made and to be approved.

Response Response Status W

REJECT.

The CRG disagrees with the commenter. Functionality is specified using reserved codes at the MII to prevent any compatibility issue with compliant PHYs.

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CI 22 SC 22.2.2.5 P 31 L 49 # i-395

Kim, Yongbum

NIO

Comment Type TR Comment Status R MII

In "...with the exception of 10BASE-T1L (see 146.3.3.1) and 10BASE-T1S(see 147.3.2.1, Figure 147-4).", 10BASE-T1L is unnecessarily included as if 10BASE-T1L requires this change. It doesn't. TXER was added during 100 Mbps Ethernet projects, and some 10 Mbps system implementations being upgraded to 100 Mbps would experience buffer underruns, and wanted to have an option to signal to the PHY to corrupt the FCS. 10 Mb/s system never had such considerations nor signal that corresponds to TXER. If TXER is asserted, then 10BASE-T1L merely maps to an error symbol.

There is no need to change CL22 from 10BASE-T1L, and having it included in this proposed revision to CL22 distracts from the fact that CL22 modification is entirely caused by CL148 PLCA RS.

SuggestedRemedy

Remove the text "10BASE-T1L (see 146.3.3.1) and ", and make appropriate changes to the 10BASE-T1L (CL146) to remove superfluous support of TXER.

(Note: the subjective "superfluous" is used because in modern (higher performance) systems as well as back in 10 Mbps systems, the need for FIFO underrun implementational error handling are not needed).

Response Response Status W

REJECT.

The CRG disagrees with the commenter. An exception has been added to clarify that the use of TX_ER with 10BASE-T1L/S PHYs is not precluded and, in fact, references to the behavior of these new PHYs with TX_ER are provided.

CI 148 SC 148.2 P 214 L 44 # i-396

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA_ID

[CSD] PLCA RS requires 1) each node/PHY to be configured with a nodeID, 2) entire network node/PHY configuration to be coordinated, i.e. unique and nearly sequential nodeID values, unique node with nodeID=0, etc 3) provides no protocol with which #2 could be accomplished, i.e. no interoperable protocol to achieve these requirements, 4) provides no remedy for boundary conditions such as multiple nodeID=0, no node with nodeID=0, non-unique nodeID in a network, unconfigured node in a configured network, etc, 5) provides no protocol that may discover any of these issues.

CSD/Compatibility means that two or more complaint implementations would interoperate with a high degree of probability. This is one of the main reasons most standards to exist -- assured and certain interoperability.

PLCA RS in CL148 does not meet this CSD requirements, nor its asserted claim in its CSD response.

SuggestedRemedy

CSD/Compatibility assertions with regard to CL148 PLCA operation is grossly incorrect. Appropriate changes to the CSD/Compatibility with regards to PLCA's inability to assure two compliant implementations interoperate without further engineering, design, and configuration be addressed, OR add appropriate specifications to remedy the concerns WRT interoperability and completeness of specification that assure interoperability, OR delete CL148 PLCA from this draft (and re-start the project development with completeness as a required scope, if desired.)

Response Response Status W

REJECT.

CRG disagrees with the commenter:

The CRG specifically disagrees on these points:

- [1] PLCA node IDs do not need to be sequential
- [2] PLCA is an optional feature that still operates under misconfiguration. See http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf
- [3] The draft does not constrain how the value for PLCA node ID is obtained. There are many different ways to implement this.
- [4] Default operation is with PLCA turned off, allowing interoperable plug-and-play, and opportunity for the management entity to configure for improved performance.

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CI 148 SC 148.2 P 214 L 44 # i-397

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA_ID

[CSD/Compatibility + PAR] CL148 PLCA RS does not specify how a node is selected for NodeID=0, how other NodeIDs are assigned, how an end-station is aware of other end-stations configuration enough to configure itself to operate, etc, such that two implementations connected via a referenced network segment is not assured to work. This indicates grossly incomplete specification.

SuggestedRemedy

Complete CL148 specification by including additional currently-missing specifications on how all parameters necessary to assure interoperability is achieved via non-vendor-dependant protocols. Since this is a concern WRT to missing specification, the suggested remedy is not included (i.e. filling in the missing specification is the scope of the IEEE 802.3cg project).

Response Response Status W

REJECT.
The CRG disagrees with the commenter.
Description or requirements of assignment of parameters in the management entity is beyond the scope of this standard.

This is clearly stated in 148.2 (draft 3.0 is quoted): "Other than the condition that the assigned node ID must be unique to the local collision domain, the method of determination of the node ID and to_timer by the management entity is beyond the scope of this standard."

Additionally, end stations on mis-configured networks or networks where not all the nodes are configured for PLCA operation will, in fact, operate, allowing configuration to be set by management for improved performance. See http://www.ieee802.org/3/cg/public/Sept2018/beruto_3cg_mixing_PLCA_with_non_PLCA_enabled_nodes_r1.2.pdf

CI 30 SC 30.3.9 P 38 L 3 # i-398

Kim, Yongbum

NIO

Comment Type ER Comment Status A Management

PLCA managed object class is put in the wrong part of the CL30. 30.3 is Layer mgmt for DTEs. This project claims to be a Physical Layer project. 30.8 is WIS. 30.14 is MAC Merge. Logically and structurally, PLCA does not belong under 30.3, where it is also more difficult to find. It should follow other sublayer additions in CL30 and go after 30.15. If this project insists that this content belongs in DTE (where MAC resides and Physical Layer doesn't) clause, then own up to what PLCA really is -- a MAC, or significant portion thereof.

SuggestedRemedy

Re-number and change the instructions to add this proposed 30.3.9 to be inserted after current 30.15

Response Response Status W

ACCEPT IN PRINCIPLE.

Implement the following changes:

P36 L1 - P37 L28: remove edits to Table 30-1c

P36 L1 Add editing instruction, "Change the last sentence of the first paragraph of 30.2.5 as follows:"

Change last sentence to, "The capabilities and packages for IEEE 802.3 Management are specified in Table 30-1a through Table 30-11." and grant editorial license to show changes with correct strikethrough and underline markings.

P36 L1 Add new editing instruction and table 30-11:
"Insert new Table 30-11 PLCA capabilities after Table 30-10 as follows:"

add new table 30-11 - PLCA capabilities
With 4 columns
(last column, with "X"s is labeled: "PLCA Capability (optional)")
Rows are from P36 L32 - P36 L42:

oPLCA managed object class (30.3.9)
aPLCAAdminState ATTRIBUTE GET X
aPLCANodeCount ATTRIBUTE GET-SET X
aPLCALocalNodeID ATTRIBUTE GET-SET X
aPLCATransmitOpportunityTimer ATTRIBUTE GET-SET X
aPLCAMaxBurstCount ATTRIBUTE GET-SET X
aPLCABurstTimer ATTRIBUTE GET-SET X
acPLCAAdminControl ACTION X
acPLCAReset ACTION X

P38 L1: Change editing instruction to read: "Insert new clause 30.16 after 30.15 (and its subclauses) as follows:"

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Change numbering of 30.3.9 oPLCA managed object class to 30.16 (and promote subclauses 1 level)

Cl 30 **SC 30.3.9.2.7** **P 39** **L 47** # **i-399**

Kim, Yongbum

NIO

Comment Type **TR** **Comment Status** **R** **PLCA**

aPLCABurstTimer measure bit times inside the internal process where the entire packet is transferred atomically. This is entirely (externally) invisible parameter, meaning any number of bit-times an implementation uses, it is indistinguishable from other MAC transmit scheduling; therefore meaningless. IPG is generated by PLS/RS. The default value of 128 *may be* relevant if this timer is measuring the gap at the PCS. But at RS, this timer is meaningless.

SuggestedRemedy

Delete this timer.

Response **Response Status** **W**

REJECT.

The CRG disagrees with the commenter. The RS interfaces to the MAC layer via the PLS primitives and to the PHY via the MII interface. The RS groups and aligns the bits conveyed by the MAC via the PLS_DATA.request primitive to the MII TX_CLK (See 22.2.1.1 and 22.2.1.1.3). This mapping clarifies the specification of bit times within an RS. (see also 148.4.3.1)

Cl 30 **SC 30.3.9.2.6** **P 39** **L 36** # **i-400**

Kim, Yongbum

NIO

Comment Type **TR** **Comment Status** **R** **PLCA**

Capability for aPLCAMaxBurstCount set to 255 packet bursts would significantly impact fairness ("multiple-access") and would cause upper layer protocol time-outs.

SuggestedRemedy

Reduce the burst down to maximum size frame worth of packet packing (which I believe is not possible in current MAC services model), or some reasonable length such as 2 x max size frame (which I believe is achievable), or demonstrate the max range still provides fairness and provide confidence that properly (in-range value) configured nodes in a given network would not cause upper layer protocol time-outs.

Response **Response Status** **U**

REJECT.

The CRG disagrees with the commenter. The comment regarding upper layer protocols is protocol specific, which is outside the scope of IEEE 802.3.

The commenter did not provide a proposed resolution in sufficient detail to readily determine the specific wording of changes that will cause him to change his vote to approve (see SASB Ops Manual clause 5.4.3.2,b).

Cl 30 **SC 30.3.9.2.5** **P 39** **L 24** # **i-401**

Kim, Yongbum

NIO

Comment Type **TR** **Comment Status** **A** **PLCA**

aPLCATransmitOpportunityTimer seem to be a tuning parameter that is related with both PHY delay and given propagation delay (network diameter). And the PHY delays of *all* the nodes in the system. The default value of 20 bit times does not match 8 node 15 meter network worst case parameter.

SuggestedRemedy

Provide the default value that represent the worst case delays and supported network diameter such that a network using all defaults (plug and play and no configuration) is assured to work. If

Response **Response Status** **W**

ACCEPT IN PRINCIPLE.

Accommodated by comment i-191.

Response to Comment i-191 is:

ACCEPT.

Suggested remedy is:

Replace, "The default value is 20."

with, "The default value is 24." on page 39, line 34.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-401

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and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 147 SC 147.8.1 P 199 L 52 # i-402

Kim, Yongbum

NIO

Comment Type TR Comment Status R Mixing Segment

The mixing segment shall meet the insertion loss characteristics specified for link segments in 147.7.1 between any two MDI attachment points. And from 147.8 "A mixing segment is specified based on cabling that supports up to at least 8 nodes and 25 m in reach". From both of this statement, this specification is requiring 28 (combination of any two) measurement taken. And any added nodes requires all combinations to be measured again, and with no assurances that the prior conformant MDI may fall out of range.

Suggested Remedy

Provide better medium specification and cable design considerations that can be followed assured scaleable MDI and medium construction.

Response Response Status W

REJECT.

The proposed change in the comment does not contain sufficient detail so that the CRG can understand the specific changes that satisfy the commenter.

Further, the CRG disagrees with the commenter, as the commenter mistakes 147.8 explanatory text with the specification ("is specified" vs. "shall meet.").

There are alternative ways to taking a large number of measurements to validate a mixing segment compliant with the specifications in 147.8. For example, simulation with sample validation is a common approach. It is also common practice for cabling systems to be specified to be compliant by design rather than necessarily measured for each instance. Further, the characteristics required have been specified based on measurements indicating that they support the described topologies, an existence proof that design is feasible.

CI 147 SC 147.8.2 P 200 L 52 # i-403

Kim, Yongbum

NIO

Comment Type TR Comment Status R Mixing Segment

The mixing segment shall meet the return loss characteristics specified for link segments in 147.7.2 between any two MDI attachment points. And from 147.8 "A mixing segment is specified based on cabling that supports up to at least 8 nodes and 25 m in reach". From both of this statement, this specification is requiring 28 (combination of any two) measurement taken. And any added nodes requires all combinations to be measured again, and with no assurances that the prior conformant MDI may fall out of range.

Suggested Remedy

Provide better medium specification and cable design considerations that can be followed assured scaleable MDI and medium construction.

Response Response Status W

REJECT.

The proposed change in the comment does not contain sufficient detail so that the CRG can understand the specific changes that satisfy the commenter.

Further, the CRG disagrees with the commenter, as the commenter mistakes 147.8 explanatory text with the specification ("is specified" vs. "shall meet.").

There are alternative ways to taking a large number of measurements to validate a mixing segment compliant with the specifications in 147.8. For example, simulation with sample validation is a common approach. It is also common practice for cabling systems to be specified to be compliant by design rather than necessarily measured for each instance. Further, the characteristics required have been specified based on measurements indicating that they support the described topologies, an existence proof that design is feasible.

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CI 45 SC 45.2.1.186e.1 P 51 L 16 # i-404

Kim, Yongbum

NIO

Comment Type ER Comment Status R Multidrop

The word "multi-drop" is a new term that does not convey any different meaning than "[half-duplex] [shared] mixing segment" as opposed to "[point to point] link segment". There is no reason to introduce a new term that does not convey anything new.

SuggestedRemedy

Delete the use of "multi-drop" here and the rest of the draft, and use existing "half-duplex", "shared medium", "mixing segment", etc, as appropriate. OR, clearly define what is different about the use of "multi-drop".

Response

Response Status W

REJECT.

The CRG disagrees with the commenter. Multidrop is only used in the draft for the name of the shared-medium mode of Clause 147 PHYs as "multidrop mode" (the term "multi-drop" is not used), and is defined at the start of clause 147 (page 167, line 15 "a half-duplex shared-medium mode, referred to as multidrop mode, capable of operating with multiple stations connected to a mixing segment, defined in 147.8."). No further description is needed, and it is not synonymous with any of the terms suggested by the commenter.

CI 45 SC 45.2.3.68b.5 P 54 L 40 # i-405

Kim, Yongbum

NIO

Comment Type TR Comment Status R Registers

"Fault -- Fault condition detected.. " is just too vague. Does reader assume the "fault" relates to PCS fault? And is it any detectable fault? Any implementation specific faults? So if I read this latched bit as one, what information do I get -- there was a fault and we don't know what caused it. So what value is there? Makes little sense. I cannot even suggest wording that may be satisfactory.

SuggestedRemedy

Assuming this is PCS fault TX or RX.. Reference detected fault types in relevant PCS clauses. If this is just thrown in for any fault and .3cg want it, then say "ANY DETECTED PCS FAULT". If there is no agreement how this is used, then I suggest deleting it.

Response

Response Status W

REJECT.

The CRG disagrees with the commenter. Text is consistent with specification for PMA and PCS faults in IEEE Std 802.3.

See, e.g., 45.2.1.2.3 Fault (1.1.7) for PMA/PMD faults, or 45.2.3.2.5 Fault (3.1.7), for the corresponding PCS fault.

CI 45 SC 45.2.3.68d.1 P 57 L 32 # i-406

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA

The concern is where entire function of PLCA resides. Is it just in RS (CL148)? Or is there PLCA mandatory components in PCS and/or PMA? This specification indicates that [optional] PLCA RS resides in PCA and PMA, requiring features otherwise not required for non-PLCA implementations.

10BASE-T1S PCS contains PLCA components that are optional. This is entirely inconsistent with PLCA is a optional function in RS layer. It looks to be that PLCA is also an optional function in PCS layer. If this is the case, the standard should state this. And if the PLCA is also an optional function in PMA layer, it should also be stated as such.

SuggestedRemedy

Either delete this PLCA Support in PCS/PMA and other PCS/PMA clauses, or clarify which layer(s), the optional PLCA function resides\, besides stated CL148 RS.

Response

Response Status W

REJECT.

The CRG disagrees with the commenter. The referenced text is purely a detection that the transmitted signal is not corrupted and is entirely in Clause 147 PCS/PMA and does not represent PLCA function. It is not strictly PLCA support, and is not PLCA function. It may be useful for a variety of debugging purposes, including, but not limited to, when the clause 148 PLCA is used.

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CI 45 SC 45.2.3.68f P 58 L 24 # i-407

Kim, Yongbum

NIO

Comment Type TR Comment Status A PLCA

CorruptedTxCnt is defined as "16 bits field counting each time a transmission initiated locally results in a corrupted signal at the MDI since last read of this register". This counter has several issues. It is not clear whether this counter is to count 1) every bit error (bit-by-bit comparison), 2) every error event (burst error event), or 3) every packet error event. Also "transmission initiated locally" is not clear. Assuming this means local node transmitting, does it apply to packets, BEACON and other signals? And is it bit-by-bit, or burst or symbol or packet or other error events?

SuggestedRemedy

Please clarify what "corruption" event this counter is counting, and reference where in the CL147 specification the event-to-be-counted resides (to assure proper formal reference to the event(s)).

Response Response Status W

ACCEPT IN PRINCIPLE.

Replace, "Bits 3.2294.15:0 count up each time a transmission initiated locally results in a corrupted signal at the MDI."

with,
"Bits 3.2294.15:0 count up at each positive edge of the MII signal COL."

CI 45 SC 45.2.3.68f P 58 L 19 # i-408

Kim, Yongbum

NIO

Comment Type TR Comment Status R MDI

"...MDI". There is no definition of MDI in CL147 that this refers to. Medium Dependant Interface, MDI, is an accepted interoperability interface. Optional-use connectors in CL147 are not MDI, unless it states the normative nature of the connector.

SuggestedRemedy

Either provide alternate referece to the medium connection point, or define nomative MDI in CL147.

Response Response Status W

REJECT.

The CRG disagrees with the commenter. The MDI is a defined interface point in Clause 147. See figure 147-1. A connector at the MDI may or may not be defined (and this varies in other IEEE Std 802.3 clauses), but the MDI remains at the plane of connection between the DTE and the specified link or mixing segment. See Figure 147-1. Additionally, electrical and tolerance characteristics of the MDI are specified in 147.9.2, 147.9.3, and 147.9.4.

CI 146 SC 146.4.3 P 133 L 35 # i-409

Kim, Yongbum

NIO

Comment Type TR Comment Status R PMA

"The sequence of symbols assigned to tx_symb_vector is needed to perform echo cancellation." is not sufficient. It should also include reference to the MASTER and SLAVE PMA clock recovery function.

SuggestedRemedy

Change the text to read
"In addition to the PMA Clock Recovery function (see 146.4.6), the sequence of symbols assigned to tx_symb_vector is needed to perform echo cancellation."

Response Response Status W

REJECT.
The CRG disagrees with the commenter.
The commenter asks for a tutorial and the standard is not a tutorial - no change required.

The only information which is inherently needed is the transmitted symbol stream. The echo can be removed an any implementation-dependent manner. The standard is not intended to be a tutorial on signal processing or constrain possible solutions. For example, a receiver could estimate the timing separately from the data, or cancel in the continuous time domain, neither of which requires the clock.

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 146 SC 146.8 P 153 L 1 # i-410

Kim, Yongbum

NIO

Comment Type TR Comment Status R Big Ticket Item MDI

The connectors described MAYBE used at the interface to the medium. This is an allowance. MDI is a normative conformance test point. The title of this subclause say "148.8 MDI specifications". It's not.

SuggestedRemedy

Change the title to "MDI Considerations" or "Medium Interface Connectors" or something else that avoids wrong inference that any of these connectors are normative interoperability test points.

Response Response Status W

REJECT.

The CRG disagrees with the commenter. The commenter appears to be confusing the MDI and the MDI connector.

The subclause, in its subordinate subclauses, spells out specifications for the MDI.

The second sentence of 146.8 states this - "It also specifies electrical requirements, including fault tolerance, at the MDI."

While connectors that may be used (and references to their specifications) are called out in 146.8.1, electrical, power, and fault tolerance specifications for the MDI are provided in subordinate subclauses 146.8.2, 146.8.3, 146.8.4, and 146.8.5.

CI 147 SC 147.1 P 167 L 12 # i-411

Kim, Yongbum

NIO

Comment Type TR Comment Status R Modes

Chater and scope of this PHY clause and CSD concern.

This clause has three separate PHYs that should not be considered as one PHY with two options.

1. Full-Duplex P2P PHY: Performs echo cancellation, full-duplex over one transmission line. This is an optional PHY in CL147.

2. Half-Duplex P2P PHY: Traditionally used with multi-port CL9 repeaters, this allows exactly two node network (one link, two link partners) and only such network, because the Clause 9 repeater is not supported as per proposed text in CL9. This is not a network. Two and only two node connection is a dedicated link. This is only mandatory PHY operation in CL147.

3. Half-Duplex Shared Medium PHY: Does NOT perform echo cancellation, half-duplex over shared medium. This is an optional PHY in CL147.

And the text says #1 and #3 are NOT interoperable -- CL147.1 says "...there are two mutually exclusive optional operating modes...".

The only mandatory PHY (Half-Duplex P2P) is useless. Two other PHYs are optional, but they are not optional to each other (mutually exclusive), yet all three PHYs are referred to as type 10BASE-T1S.

This clause organization is grossly in error. Each distinct PHY should has its own type designation (possibly its own clause, but only for clarity), #2 Half-duplex P2P PHY should be deleted for the stated reason of not being useful as a 'network'.

SuggestedRemedy

Pick the one PHY that meets CSD and objectives as written, or split this clause into at least two (one for P2P and one for Shared medium) separate PHY clauses and re-state the respective CSD as appropriate.

Response Response Status W

REJECT.

CRG disagrees with the commenter. The clause contains one PHY with three modes, with a common-denominator for interoperability. CRG disagrees with the commenter on interest in the mandatory mode of operation (half-duplex point-to-point). There are multiple methods of inter-linking point-to-point half-duplex segments, without the use of clause 9 repeaters using multiple topologies of choice, allowing larger networks (with more than 2 stations). A bridge is considered to be an element in common networks.

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CI 147 SC 147.3.7.1 P 185 L 19 # i-412

Kim, Yongbum

NIO

Comment Type TR Comment Status R PCS

WRT to "When the PHY is not in multidrop mode and a BEACON is received either over the MII or from the line, the state diagram in Figure 147-10 enters the DISABLE_HB state and stays there until PCS Reset is asserted,...". This statement makes support of PLCA RS in 10BASE-T1S PHY (current all three of 10BASE-T1S PHYs) not optional. PLCA RS is advertised as optional RS. The recognition of BEACON (in proposed changes to CL22) requires support of the optional RS, but this clause does not specify the optional RS beavior. This and two other shalls in this subclause makes it mandatoy implementation in all 10BASE-T1S PHYs.

SuggestedRemedy

Delete CL147.3.7.1 requirements.

Response Response Status W

REJECT.
The CRG disagrees with the commenter.
The decoding and signaling of the COMMIT and BEACON indications, and presentation of the signaling onto the MII does not make support of PLCA mandatory.
When the PLCA is not enabled or not supported, RS operation shall conform to C22, which would cause the signals to be ignored because the state diagrams they effect are not implemented, and the codes are defined as reserved with no action in existing clause 22, per IEEE Std 802.3-2018, 22.2.2.8:
"While RX_DV is deasserted, RXD<3:0> shall have no effect on the Reconciliation sublayer."

See also 215/51 ("148.4.2 Reconciliation Sublayer operation").

CI 147 SC 147.3.7.1 P 185 L 19 # i-413

Kim, Yongbum

NIO

Comment Type ER Comment Status A PCS

"... a BEACON is received..." the word "BEACON" is used without any x-reference, and the nature of 'BEACON' (signal?, state?, interface?, etc) is found in other clauses.

SuggestedRemedy

Please insert x-ref to 'BEACON'.

Response Response Status W

ACCEPT IN PRINCIPLE.

Change this:

====

When the PHY is not in multidrop mode and a BEACON is received either over the MII or from the line

====

to this:

====

When the PHY is not in multidrop mode and a BEACON request is received from the MII (See Table 22-2) or a BEACON signal is received from the line (See Table 147-1)

====

and Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 147 SC 147.3.3.10 P 185 L 10 # i-414

Kim, Yongbum

NIO

Comment Type TR Comment Status R PCS

Generation of Commit indication states PHY shall notify RS of received Commit by the means of MII interface in 22.2.2.8. This statement makes support of PLCA RS in 10BASE-T1S PHY not optional. PLCA RS is advertised as optional RS. The use of COMMIT (in proposed changes to CL22) requires support of the optional RS, but this clause does not specify the optional RS behavior. This and two other shalls in this subclause makes it mandatory implementation in all 10BASE-T1S PHYs.

SuggestedRemedy

Delete CL147.3.3.10 requirements.

Response Response Status W

REJECT.
The CRG disagrees with the commenter.
The decoding and signaling of the COMMIT and BEACON indications, and presentation of the signaling onto the MII does not make support of PLCA mandatory.
When the PLCA is not enabled or not supported, RS operation shall conform to C22, which would cause the signals to be ignored because the state diagrams they effect are not implemented, and the codes are defined as reserved with no action in existing clause 22, per IEEE Std 802.3-2018, 22.2.2.8:
"While RX_DV is deasserted, RXD<3:0> shall have no effect on the Reconciliation sublayer."

See also 215/51 ("148.4.2 Reconciliation Sublayer operation").

CI 147 SC 147.3.7.1.1 P 185 L 51 # i-415

Kim, Yongbum

NIO

Comment Type TR Comment Status R PCS

WRT to "...rx_cmd <= 'COMMIT' when a COMMIT indication is generated as specified". This statement makes support of PLCA RS in 10BASE-T1S PHY not optional. PLCA RS is advertised as optional RS. The use of COMMIT (in proposed changes to CL22) requires support of the optional RS, but this clause does not specify the optional RS behavior. This and two other shalls in this subclause makes it mandatory implementation in all 10BASE-T1S PHYs.

SuggestedRemedy

Delete CL147.3.7.1.1 requirements.

Response Response Status W

REJECT.
The CRG disagrees with the commenter.
The decoding and signaling of the COMMIT and BEACON indications, and presentation of the signaling onto the MII does not make support of PLCA mandatory.
When the PLCA is not enabled or not supported, RS operation shall conform to C22, which would cause the signals to be ignored because the state diagrams they effect are not implemented, and the codes are defined as reserved with no action in existing clause 22, per IEEE Std 802.3-2018, 22.2.2.8:
"While RX_DV is deasserted, RXD<3:0> shall have no effect on the Reconciliation sublayer."

See also 215/51 ("148.4.2 Reconciliation Sublayer operation").

CI 147 SC 147.3.7.1 P 185 L 15 # i-416

Kim, Yongbum

NIO

Comment Type TR Comment Status A PCS

WRT "... and Auto-Negotiation has achieved a good link." Auto-negotiation never achieves a good link. Auto-negotiation only negotiates capabilities.

SuggestedRemedy

Either delete the quoted text, or revise the text to describe appropriate condition while correcting for the error.

Response Response Status W

ACCEPT IN PRINCIPLE.

Page 185, line 15:

Replace, "Auto-Negotiation has achieved a good link"

with, "Auto-Negotiation has completed"

Link Management Parameters for 10 Mb/s Operation and Associated Power Delivery over a Single Balance

CI 147 SC 147.3.5 P 184 L 30 # i-417

Kim, Yongbum

NIO

Comment Type TR Comment Status A PCS

[CSD/Compatibility] [Collision Detect, no assurance thereof]

In IEEE 802.3 project where CSMA/CD ("half-duplex") is supported, the collision detection method always has been specified, AND the assurance of 100% collision detection has been obvious, i.e. DC bias voltage rise from two or more transmitters using current source into a known resistance, or simple logical AND function of PMA TXD enable and RXD enable. This project, however, does not specify any collision detection method except to say 1) data corruption == collision, and 2) require, without specification, find two or more stations transmitting somewhere in the network and assert CRS during that time.

We all know what collision condition is, 'two or more simultaneous transmission into a shared collision domain' or there about. It is the responsibility of the project to specify how this is done, and also assure us that collision detection confidence is at least at PAR with prior projects. This project does not specify the collision detection method; therefore, it is incomplete.

That said, there are tactical issues with the current draft, and I do not wish to indicate that fixing any of these tactical issues would be satisfactory to requiring 100% assurance of collision detect. But here goes.

1) "corrupted signal while transmitting" == collision. This has an obvious flaw that one station may see random bit-error (e.g. from a local noise hit) and detect collision and back-off, the other station does not see a collision 'corrupted signal while transmitting' and completes transmission. Some receivers may see errored frames, some may not see errored frame. Result = non-deterministic behavior and lost packet.

2) Local strong TX and remote weak TX may not assure corruption.

- Max Attenuation: Attenuation of the TX signal on the nominal-length worst-case channel is 65% (3.7 db)

- Max TX power of local, so +20% P-P from 147.5.4.1 transmit output voltage is 1V +/- 20% P-P. + minimum droop and power spectral density (highest power allowed).

- Min TX power of remote, so -20% P-P, with max droop.

so power diff give another ~66%. Or ~43% max interference from remote, and it could be as little as ~35% considering droop.

In addition, COL assertion within 256 bit times from the beginning of a transmission seems insufficient -- a minimum collision duration is 96 bit times. A min collision + IPG would allow a new transmission to occur at 192 bit times from the initial collision. So allowing collision to assert up to 256 bit time later, would potentially affect the subsequent packet transmission.

Without receiver specification we have NO CLUE how receiver would behave -- whether or not data corruption would be detected from the worst case remote TX interference.. And we've opted for TX and channel spec and leave RX to implementors to *recover* tx data over channel.

From 147.3.5 Collision Detection:

"When operating in half-duplex mode, the 10BASE-T1S PHY shall detect when a

transmission initiated locally results in a corrupted signal at the MDI as a collision. When collisions are detected, the PHY shall assert the signal COL on the MII for the duration of the collision or until TX_EN signal is FALSE. The method for detecting a collision is implementation dependent but the following requirements have to be fulfilled. a) The PHY shall assert COL within 256 bit times from the beginning of a transmission when one or more stations are transmitting at the same time.
b) The PHY shall assert CRS in the presence of a signal resulting from a collision between two or more stations."

Suggested Remedy

The draft is incomplete without 100% collision detection specification. 100% defined to be as obvious as prior 802.3 CSMA/CD PHY projects. Please complete the draft by including collision detection specification.

Response

Response Status W

ACCEPT IN PRINCIPLE.

Comment appears to comment on multiple issues, at least one of which is accommodated by comment i-248.

1. With regards to the 256 bit times delay in asserting COL, comment is accommodated by comment i-248.

Response to comment i-248 is:

ACCEPT IN PRINCIPLE.

Change:

=====

a) The PHY shall assert COL within 256 bit times from the beginning of a transmission when one or more stations are transmitting at the same time.

b) The PHY shall assert CRS in the presence of a signal resulting from a collision between two or more stations.

=====

to this:

=====

a) The PHY shall assert COL when it is transmitting, and one or more other stations are also transmitting at the same time.

b) The PHY shall assert CRS in the presence of a signal resulting from a collision between two or more other stations.

=====

The above response to comment i-248 effectively removes "within 256 bit times from the beginning of a transmission".

2. CRG disagrees with the remainder of the commenter's statements.

Various results have been presented to the Task Force, showing reliable collision detection on link segments using a variety of methods.

http://www.ieee802.org/3/cg/public/May2019/griffiths_3cg_01b_0519.pdf showed voltage-domain collision detection.

Additionally, analysis has been presented in

http://www.ieee802.org/3/cg/public/adhoc/beruto_3cg_collision_detection.pdf to address

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-417

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issues of existence, feasibility and reliability of collision detect (CD).

The highlights of this analysis relevant to this comment are:

- Target level of reliability (less-than-or-equal-to one miss-categorization per lifetime of universe) can be achieved based on the current specs.
- In the voltage domain, in presence of the specified Gaussian noise, reliable CD can be achieved. The commenter's calculation seems to confirm most of these (see commenter's figure compared to pages 4 and 5 of the study), but CRG has difficulty following commenter's calculations in full.
- Using the properties of the DME, the self-synchronizing scrambler and network geometry (reach, exclusion of the repeaters) and other properties of the Ethernet frame, the same can be achieved.
- At least one implementation exists that meet these requirements in specified noise environment.

CI 148 SC 148.4.6 P 214 L 22 # i-418

Kim, Yongbum

NIO

Comment Type TR Comment Status R PLCA_SCOPE

[CSD/Compatibility] [Installed base compatibility] [PAR -- scope did not include MAC function in the project scope]

In PLCA data state diagram, COLLIDE state and related functional behaviors create a condition where in half-duplex, CSMA/CD, MAC transmits a packet, into a substantially busy network, but the collision condition does not result in a collision on the shared media. The collision signal is asserted only for the local node for the TX to collide-&-retry, while the simultaneous received signal that caused the collision is expected to be received as if there is no collision. The remote transmitter is not notified of contention on the network. This is a new behavior for an half-duplex MAC.

Legacy and installed base of Ethernet MACs expect to operate in 'architecturally' separate TX and RX, i.e. full-duplex datapath, while in half-duplex mode. Explicit allowance for implementations to optimize the datapath resources to only support simplex datapath operation is found in 4.1.2 where only obvious externally testable condition was inserted into the CL4 spec:

"4.1.2 CSMA/CD operation. Transmit frame operations are independent from the receive frame operations. A transmitted frame addressed to the originating station will be received and passed to the MAC client at that station. This characteristic of the MAC sublayer may be implemented by functionality within the MAC sublayer or full duplex characteristics of portions of the lower layers."

And the clear architectural model vs implementations here in 1.1.3.1: "...The architectural model is based on a set of interfaces that may be different from those emphasized in implementations. One critical aspect of the design, however, shall be addressed largely in terms of the implementation interfaces: compatibility."

This new behavior specified in CL148 PLCA data state diagram is not compatible with many installed bases of 802.3 nodes with appropriate exposed MII interoperability test point that is also a physical interface with specified connectors. Also as forementioned, the contention management and collision handling are MAC functions, not a part of Physical Layer that Reconciliation Sub-layer belongs to.

Additional info could be found here : (slides 14~18 of):
http://www.ieee802.org/3/cg/public/Nov2018/Kim_3cg_01a_1118.pdf

Suggested Remedy

This clause CL148 PLCA RS should be deleted. Alternatively re-architected to avoid introducing new normative behaviors to the installed base with exposed interoperability interfaces.

Response

Response Status W

REJECT.

CRG disagrees with the commenter.

Commenter fails to show compatibility issues with conformant implementations and

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

SORT ORDER: Comment ID

Comment ID i-418

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incorrectly posits PLCA is a new MAC.

Additionally, the Task Force has previously considered the issues raised by the commenter and has also reviewed and evaluated contributions that rebut the commenter's assertions.

See for example:

http://www.ieee802.org/3/cg/public/Jan2019/Tutorial_cg_0119_final.pdf,

http://www.ieee802.org/3/cg/public/Jan2019/baggett_3cg_01_0119.pdf

http://www.ieee802.org/3/cg/public/adhoc/beruto_3cg_plca_mac_compatibility.pdf

http://www.ieee802.org/3/cg/public/adhoc/beruto_3cg_plca_multiple_collisions.pdf

CI 148	SC 148.4.5.1	P 219	L 35	# i-428
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Thompson, Geoffrey

Independent Consultant

Comment Type	TR	Comment Status	A	Editorial
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The text calls for things to be reset to the defaults shown in the figure. There are no defaults shown in the figure.

SuggestedRemedy

Point instead to subclause 148.4.5.2 where the items are defined and add the default values there,

Response	Response Status	W
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ACCEPT IN PRINCIPLE.

Delete "When PLCA functions are disabled (plca_en = FALSE), the PLCA control variables are reset to their default values as shown in Figure 148-3 and no special signaling is conveyed to the MII through the tx_cmd variable."

The intention was to describe what happens in Figure 148-3 / DISABLE state. Since the figure is self-explanatory the text is not needed.