	SC 0		P <b>0</b>	L <b>0</b>	# r02-66	Cl 30	SC :	30.3.1.1	P 38	L <b>40</b>	# r02-3
Thompsor	n, Geoffre	ey (	Independent C	Consultant		Anslow, Pet	ter		Ciena		
Comment	Туре	GR	Comment Status R		PLCA_Scope	Comment T	уре	Е	Comment Status A		
scope that th good DISAI comm Suggestee	of the wo be scope conscience PROVE entable t dRemedy	ork autho of the dra ce, affirm . It is my rext on th	es as a balloter is to ensure rized by the PAR. An affirma aft does not exceed the work that for reasons previously belief that, in spite of the co e draft that this comment is v	ative vote indicat authorized by th stated, therefore nverging nature within the scope	tes your agreement the PAR. I cannot, in my vote is of the scope of of this ballot.	30.3.1 I 30.3.1.3 Should 30.3.1 I 30.3.1 I	MAČ e 3 aSino be: MAC e 1 MAC	gleCollisio ntity mana entity attr	aged object class nFrames aged object class		
Since	the time	for modi	ying the PAR to change the	scope of this pro	pject is long past, the	SuggestedF		-			
			it would be to (1) disapprove ere in the project.	the project of (2	) remove clause 146	•••		-	g: "30.3.1.1 MAC entity att	ributes"	
Response			Response Status W			Chane	the hea		0.3.1.3 aSingleCollisionFra		5: 30.3.1.1.3
REJE The C	-	nrees wit	a the commenter			Response			Response Status C		
includ additio Respo REJE The C	The CRG disagrees with the commenter. This comment is a restatement of previous comments from the same commenter, including particularly R01-220 and R01-227, and restates the commenter's opinion without additional technical information. The commenter has a previously existing disapprove vote. Response to R01-227 is: REJECT. The CRG disagrees with the commenter, and believes the draft is within the PAR scope. A key responsibility of the ballot pool is to evaluate whether the scope of the draft is within					Accom Respon ACCEP Delete MAC er	modate ise to o PT. editing ntities i	instructio using a Ph	onse to comment r02-56.	abled.;" so that t	
the sc	ope of th	e PÁR, a	nd an affirmative vote indica	tes your agreem	ent that the work does	C/ 30	SC :	30.3.1	P 38	L <b>41</b>	# r02-26
not ex			the PAR. The ballot pool hat ially a restatement of the arc			Kabra, Loke	esh		Synopsys, Ir	nc.	
			lany a restatement of the art			Comment T	уре	Е	Comment Status A		
This c		and 1-27	0, and are not associated wi					ronoo to o			
This c comm The m	ents i-27 ajority of	the CRC	0, and are not associated will believes that the functions	are appropriately	y placed in the	Incorrec	ct reter	rence to st	ub-clause number for "aSir	gleCollisionFran	ne" in 802.3-2018
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This c comm The m archite	ents i-27 najority of ecture of SC 1	the CRO	B believes that the functions I. 802.3 and ISO layering mo	are appropriately		SuggestedF	Remed	ly		gleCollisionFran	ne" in 802.3-2018
This c comm The m archite C/ 01	ents i-27 najority of ecture of SC 1. eter	the CRO	B believes that the functions 8. 802.3 and ISO layering mo P <b>29</b>	are appropriately		SuggestedF Replace Response ACCEP	Remed e "30.3 PT IN F	/y 3.1.3" with PRINCIPLI	"30.3.1.1.3" <i>Response Status</i> <b>C</b> E.	gleCollisionFran	ne" in 802.3-2018
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This c comm The n architi C/ 01 Anslow, P Comment The re remov 147.1	ents i-27 najority of ecture of SC 1. eter Type eference ced from to 0.2.1	the CRC IEEE Sto .3 T "ISO 489 the draft,	B believes that the functions A 802.3 and ISO layering mo P 29 Ciena Comment Status A 2:1982, PlasticsMethods o	are appropriately odel. <i>L</i> 23 f exposure to lat	# <u>r02-2</u> EZ poratory light" has been	SuggestedF Replace Response ACCEP Accom Respon ACCEP Delete	Remed e "30.3 PT IN F modate ise to o PT. editing	ly 3.1.3" with PRINCIPLI ed by resp comment i instructio	"30.3.1.1.3" <i>Response Status</i> <b>C</b> E. onse to comment r02-56. 02-56 is: ns related to the "The content of the the the states of the	ents of this attrib	bute are undefined for
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TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **38** Li **41** 

CI 30	SC 30.3.1.3	P 38	L <b>42</b>	# r02-4		C/ 30	SC 30.3.1.3		P 38	L <b>50</b>		# r02-15
Anslow, Pe	eter	Ciena				Zimmerma	an, George	А	DI, APL Gr	oup, Aquantia, E	BMW, Ci	sco, Commscop
Comment	Туре Е	Comment Status A			ΕZ	Comment	Туре Т	Comment Sta	ntus A			Management
As note Also, re <i>Suggested</i>	ed in another cor efer to the "BEH	should reference the subclaus nment this should be 30.3.1. AVIOUR DEFINED AS" section ruction to:	1.3.	er than the title.		PLCA enable to use PLCA	enabled" - the d ed. This counter with a full duple should occur in	attribute are undef definition of the co r counts single co ex MAC. Reportin a PLCA clause 3	unter is ide llisions at th g of the PH	ntical, regardles ne MAC. The sit Y asserting a co	s of whe uation is prruption	ether PLCA is s not analogous
		OUR DEFINED AS" section of	f 30.3.1.1.3 as s	hown:		Suggested						
Response		Response Status C						he draft, including : "PLCA managed			new attri	bute after
Accom Respo ACCEI Delete MAC e	nse to comment PT. editing instructic entities using a P ert back to no cha SC <b>30.3.1.3</b>	oonse to comment r02-56.	oled.;" so that th			Gener counts receiv aPLC/ <i>Response</i> ACCE Accom	alized nonreseta per second. <c es an asserted ( ATransmitOppor PT IN PRINCIP nodated by com onse to commen</c 	ment r02-56	counter ha DEFINED A ;" In Add r PLCACorru	s a maximum in S <cr>A count new row after</cr>	crement of times	t rate of 13 000 the PLCA RS
Comment	Туре Е	Comment Status A			ΕZ			ions related to the	"The conte	ents of this attrib	ute are	undefined for
"5.2.4.	2" is an external	cross-reference				MAC e	entities using a F	Physical Layer wit	h PLCA en	abled.;" so that t		
Suggested	<i>IRemedy</i> character tag Ext	ernal to "5.2.4.2"				to reve	ert back to no ch	nange to this subc	lause and t	ext.		
Apply of		Response Status C										

to revert back to no change to this subclause and text.

Pa **38** Li **50** 

C/ 30	SC 30.3.1	P <b>38</b>	L <b>50</b>	# r02-27	CI 30	SC 30.16.1	P <b>42</b>	L 8	# r02-28
Kabra, Lo	kesh	Synopsys, In	C.		Kabra, Loke	esh	Synopsys, Inc.		
Comment	Type <b>G</b>	Comment Status A		Management	Comment 7	ype E	Comment Status A		Management
		ence is not accurate for MAC			Section	30.16.1 describes	both oPLCA managed obj	ect class attribu	tes and device actions.
		ntity (or MAC function includir y independent of normal RS c			Suggested	Remedy			
		are reduced by means of ex			Add "ai	nd actions" to the e	end of the sentence.		
		collisions can still occur if son CA rules or are incorrectly co			Response		Response Status C		
		is still valid and useful to hav		eeug ue	ACCEF	РТ.			
Jggeste	dRemedy				CI 30	SC 30.16.1.1.1	P 42	L 24	# r02-29
Delete	e the new senter	nce added in D3.2			Kabra, Loke		Synopsys, Inc.		
esponse	<b>)</b>	Response Status C			Comment 7		Comment Status A		OOS Editorial
	PT IN PRINCIP						nt that the term "MII RS" is	not a valid term	
	modated by com onse to commen				Suggested	-			
ACĊE	EPT.				00	the term "RS MII".			
		ions related to the "The conte Physical Layer with PLCA ena			Response		Response Status <b>C</b>		
to rev	ert back to no ch	ange to this subclause and te	ext.	io intollaca change io	, ACCEF	T IN PRINCIPLE.			
30	SC 30.3.1.3	P 39	L <b>50</b>	# r02-56	On P42	L24, delete "MII"			
m, Yond		NIO	200	102 30	C/ 30	SC 30.16.1.1.5	P 43	L 15	# r02-35
omment	5	Comment Status A		Management	Law, David		Hewlett Packa	rd Enterprise	
		ontents of this attribute are u	ndefined for MAC	0	Comment 7	ype E	Comment Status <b>D</b>	a =	Management
Physi	cal Layer with Pl	CA enabled." does not make	sense. CL148	PLCA RS claims to be	It seem	s odd to hide a sta	tement that the default for	the to_timer is 2	4 in the management
		orm MAC function. It further of This aSingleCollisionFrames			subclau				
MAC	and not relevant	to fuill-duplex MAC. But this	added text mak	es this counter	Suggested	•			
		uplex MAC and CL148 PLCA ant to half-duplex MAC	This change m	akes little sense.	Sugges	st that:			
		ister relevent and meaningful	event because	e PLCA does not	[1] The	text 'The default v	alue is 24.' be deleted from	subclause 30.1	6.1.1.5.
	ate collisions (if, d show how, and	PLCA always guarantees co	lision-free opera	tion, then it should say			alue is specified in 30.16.1.		d to read 'The default
		hakes little sense that optiona	l behavior in the	physical layer(s)			148.4.5.4 'Timers' (page 24	iz, ine 52).	
some	how changes the	e relevancy of the upper layer	statistics.		Proposed F REJEC		Response Status Z		
uggeste	dRemedy				REJEC	1.			
MAC	Delete editing instructions related to the "The contents of this attribute are undefined for MAC entities using a Physical Layer with PLCA enabled.;" so that the intended change is to revert back to no change to this subclause and text.					mment was WITH	DRAWN by the commenter		
esponse		Response Status W							
ACCE									
YPF·TR	/technical requir	ed ER/editorial required GR/	deneral required	T/technical E/editorial G	/general		Pa <b>43</b>		Page 3 of 30

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

C/ 104	SC 104.	.1.3	P <b>94</b>	L <b>22</b>	# r02-6		C/ 104	SC 104.4.3.5	P <b>97</b>	L <b>51</b>	# r02-64
Anslow, P	eter		Ciena				Stewart, H	eath	Analog Devic	es Inc.	
Comment	Type E		Comment Status A			ΕZ	Comment	Туре Т	Comment Status A		Powe
"Repl	ace 104-3" s	should b	e "Replace Figure 104-3"				*** Co	mment submitted	with the file 101686300003	-stewart_0819_0	01.pdf attached ***
00			to: "Replace Figure 104-3" <i>Response Status</i> <b>C</b>				power comm	Split this register and - Read_POW	egister was increased to 32 into two 16 bit registers- \ ER_INFO [0x77] and Rena NFO [0xBB] as Read_VOL	OLT_INFO and me command-	
7001							Suggested	Remedy			
C/ 104	SC 104.	.4.3.3	P 97	L 16	# r02-7			m the following te			that auguant appla
Anslow, P	eter		Ciena						ext: "VOLT_POWER_INFO t also return the VOLT PO		
Comment	Type E		Comment Status A			ΕZ			ontents." With "VOLT_INFO	_ 0	
			n the base standard is "PSE hould not be in underline for		le matrix".		descri	otion of contents."	t also return the VOLT_INF	Ū	
Suggeste	dRemedy								POWER_INFO_register: P n the POWER_INFO regist		
00	ve the unde	erline fro	m " matrix"					otion of contents."			
Response			Paananaa Statua						text: "VOLT_POWER_INF		
			Response Status C						t also return the VOLT_PO		
ACCE	PT.								ontents." With "VOLT_INFO t also return the VOLT_INF		
C/ 104	SC 104.	.4.3.3	P <b>97</b>	L <b>25</b>	# r02-8		descri	otion of contents."	_	Ū	
Anslow, P	eter		Ciena						: "POWER_INFO_register: n the POWER_INFO regist		
Comment	Type E		Comment Status A			ΕZ		otion of contents."			
In Tab	ole 104-2a th	here are	two occurrences of "Classe	es 0-9".					text: "PSEs and PDs that		
	EEE style ma								e VOLT_POWER_INFO a		
		•	e unit (e.g., 115 V to 125 V)		ld never be used				104-11)." With "PSEs and ne VOLT_INFO, POWER_		
		i be mis	construed as subtraction sig	JII5.					e 104-11 and Table 104-12		
00	dRemedy								text: "VReport_PD is the v		
In Tab	ole 104-2a cl	hange "	Classes 0-9" to "Classes 0	to 9" in two pla	ces				0] of VOLT_POWER_INFO uring the presence pulse as		
Response	,		Response Status C					104-10"	anny the presence pulse as		
ACCE	PT.								text: "via the PD Requeste		
									Register b[19:8]" With "via t	he PD Requeste	d Power, PPD_req,
									O Register b[11:0]." text: "PPD_req is the PD F	Paguastad Powe	r as reported in h[10-
									D in Table 104-10" With "Pf		
									OWER_INFO in Table 104-		
									igure 104-13 to rename the		
									e POWER_INFO [0x77] rea		
							0		of attached presentation- "s text: "104.7.2.6 Read_VO		•
									port cable resistance meas		
	toohnigol re	autirod	ER/editorial required GR/g	oporal required	T/toobnical E/ad	itorial C/a	nonorol		Pa <b>9</b>	7	Page 4 of 30

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Read\_VOLT\_POWER\_INFO command. After receiving a Read\_VOLT\_POWER\_INFO command, the PD shall respond with a 32-bit VOLT\_POWER\_INFO read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_VOLT\_POWER\_INFO command is shown in Figure 104-13. Table 104-10

illustrates the contents of the VOLT\_POWER\_INFO register" With "104.7.2.6 Read\_VOLT\_INFO command [0xBB] All PSEs and PDs that support cable resistance measurement shall support the 8-bit Read\_VOLT\_INFO command. After receiving a Read\_VOLT\_INFO command, the PD shall respond with a 16-bit VOLT\_INFO read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO register"

-On P111, L34: Change the title of Table 104-10 from "Table 104-10 VOLT\_POWER\_INFO Register Table" to "Table 104-10 VOLT\_INFO Register Table"

-On P111, L34: Replace existing Table 104-10 with Table 104-10 shown on slide 7 of attached presentation- "stewart\_0819\_01.pdf"

-On P111, L50: Add text: "104.7.2.7 Read\_POWER\_INFO command [0x77] All PSEs and PDs that support cable resistance measurement shall support the 8-bit

Read\_POWER\_INFO command. After receiving a Read\_POWER\_INFO command, the PD shall respond with a 16-bit POWER\_INFO read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the

Read\_POWER\_INFO command is shown in Figure 104-13. Table 104-11 illustrates the contents of the POWER\_INFO register."

-On P111, L50: Add "Table 104-11 POWER\_INFO Register Table" after new paragraph added on L50. The Table 104-11 is as shown on slide 8 of attached presentation-"stewart 0819 01.pdf"

-On P112, L4: Replace text: "After transmitting a Write\_POWER\_ASSIGN command, the PSE shall transmit a 32-bit POWER\_ASSIGN write payload followed by an 8-bit CRC8" With "After transmitting a Write\_POWER\_ASSIGN command, the PSE shall transmit a 16-bit POWER\_ASSIGN write payload followed by an 8-bit CRC8"

-On P112, L10: Modify Table 104-12 POWER\_ASSIGN Register Table as shown on slide 9 of attached presentation- "stewart\_0819\_01.pdf"

-On P112, L25: Replace text: "After receiving a Read\_POWER\_ASSIGN command, the PD shall respond with a 32-bit POWER\_ASSIGN read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_POWER\_ASSIGN command is shown in Figure 104-13. Table 104-11 illustrates the contents of the POWER\_ASSIGN register." With "After receiving a

Read\_POWER\_ASSIGN register. With Articl recoving a Read\_POWER\_ASSIGN command, the PD shall respond with a 16- bit POWER\_ASSIGN read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_POWER\_ASSIGN command is shown in Figure 104-13. Table 104-12 illustrates the contents of the POWER\_ASSIGN register." -On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return VOLT\_POWER\_INFO and POWER\_ASSIGN registers" to "Return VOLT\_INFO, POWER\_INFO and POWER\_ASSIGN registers"

Response Response Status C

ACCEPT IN PRINCIPLE.

(commenter's response with editorial license to rearrange figure)

Perform the following text changes:

-On P97, L51: Replace text: "VOLT\_POWER\_INFO\_register: PSEs that support cable

resistance measurement also return the VOLT\_POWER\_INFO register. Refer to Table 104-10 for a description of contents." With "VOLT\_INFO\_register: PSEs that support cable resistance measurement also return the VOLT\_INFO register. Refer to Table 104-10 for a description of contents."

-On P98, L1: Add text: "POWER\_INFO\_register: PSEs that support cable resistance measurement also return the POWER\_INFO register. Refer to Table 104-11 for a description of contents."

-On P101, L14: Replace text: "VOLT\_POWER\_INFO\_register: PDs that support cable resistance measurement also return the VOLT\_POWER\_INFO register. Refer to Table 104-10 for a description of contents." With "VOLT\_INFO\_register: PDs that support cable resistance measurement also return the VOLT\_INFO register. Refer to Table 104-10 for a description of contents."

-On P101, L18: Add text: "POWER\_INFO\_register: PDs that support cable resistance measurement also return the POWER\_INFO register. Refer to Table 104-11 for a description of contents."

-On P103, L52: Replace text: "PSEs and PDs that implement cable resistance measurement support the VOLT\_POWER\_INFO and POWER\_ASSIGN registers (see Table 104-10 and Table 104-11)." With "PSEs and PDs that implement cable resistance measurement support the VOLT\_INFO, POWER\_INFO and POWER\_ASSIGN registers (see Table 104-10, Table 104-11 and Table 104-12)"

-On P108, L16: Replace text: "VReport\_PD is the voltage at PD's PI during the presence pulse as reported in b[7:0] of VOLT\_POWER\_INFO in Table 104-10" With "VReport\_PD is the voltage at PD's PI during the presence pulse as reported in b[7:0] of VOLT\_INFO in Table 104-10"

-On P108, L38: Replace text: "via the PD Requested Power, PPD\_req, field of the VOLT\_POWER\_INFO Register b[19:8]" With "via the PD Requested Power, PPD\_req, field of the POWER\_INFO Register b[11:0]."

-On P108, L49: Replace text: "PPD\_req is the PD Requested Power as reported in b[19:8] of VOLT\_POWER\_INFO in Table 104-10" With "PPD\_req is the PD Requested Power as reported in b[11:0] of POWER\_INFO in Table 104-11"

-On P109, L11: Modify Figure 104-13 to rename the VOLT\_POWER\_INFO [0xBB] read command and to add the POWER\_INFO [0x77] read command. Replace the figure with figure shown on slide 6 of attached presentation- "stewart\_0819\_01.pdf" WITH EDITORIAL LICENSE TO REARRANGE TO MAKE THE NEW FIGURE FIT.

-On P111, L25: Replace text: "104.7.2.6 Read\_VOLT\_POWER\_INFO command [0xBB] All PSEs and PDs that support cable resistance measurement shall support the 8-bit Read\_VOLT\_POWER\_INFO command. After receiving a Read\_VOLT\_POWER\_INFO command, the PD shall respond with a 32-bit VOLT\_POWER\_INFO read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_VOLT\_POWER\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_POWER\_INFO register" With "104.7.2.6 Read\_VOLT\_INFO command [0xBB] All PSEs and PDs that support cable resistance measurement shall support the 8-bit Read\_VOLT\_INFO command. After receiving a Read\_VOLT\_INFO command, the PD shall respond with a 16-bit VOLT\_INFO read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read\_VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO command is shown in Figure 104-13. Table 104-10 illustrates the contents of the VOLT\_INFO register"

-On P111, L34: Change the title of Table 104-10 from "Table 104-10 VOLT\_POWER\_INFO

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

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Li 51	8/15/2019 2:50:49 PM

-On P111, L34: Replace existing Table 104-10 with Table 104-10 shown on slide 7 of	C/ 104 SC 104	4.4.6	P 99	L 31	# r02-62	
attached presentation- "stewart_0819_01.pdf"	Stewart, Heath		Analog Device	es Inc.		
-On P111, L50: Add text: "104.7.2.7 Read_POWER_INFO command [0x77] All PSEs and PDs that support cable resistance measurement shall support the 8-bit	Comment Type T	Comment	Status A		Power	əring
Read_POWER_INFO command. After receiving a Read_POWER_INFO command, the PD shall respond with a 16-bit POWER_INFO read payload followed by an 8-bit CRC8 field		on times need to be r lass (max) timer to 1		ount for longer s	ignaling times.	
as specified in 104.7.2.5. A flowchart for operation of the address and the	SuggestedRemedy					
Read_POWER_INFO command is shown in Figure 104-13. Table 104-11 illustrates the contents of the POWER_INFO register." -On P111, L50: Add "Table 104-11 POWER_INFO Register Table" after new paragraph	Change the edit t	to Table 104-4 (P99 300". Edit the classif			ication time Max value	e
added on L50. The Table 104-11 is as shown on slide 8 of attached presentation- "stewart_0819_01.pdf" -On P112, L4: Replace text: "After transmitting a Write_POWER_ASSIGN command, the PSE shall transmit a 32-bit POWER_ASSIGN write payload followed by an 8-bit CRC8"		on time} {TClass} {m 0} {Classes 10 to 15}		sses 0 to 9} {All	} {See 104.4.5}}	
With "After transmitting a Write_POWER_ASSIGN command, the PSE shall transmit a 16- bit POWER_ASSIGN write payload followed by an 8-bit CRC8" -On P112, L10: Modify Table 104-12 POWER_ASSIGN Register Table as shown on slide	Response ACCEPT.	Response S	Status C			
9 of attached presentation- "stewart_0819_01.pdf" -On P112, L25: Replace text: "After receiving a Read_POWER_ASSIGN command, the	C/ 104 SC 104	4.5.1a	P 100	L <b>34</b>	# r02-10	
PD shall respond with a 32-bit POWER_ASSIGN read payload followed by an 8-bit CRC8	Anslow, Peter		Ciena			
field as specified in 104.7.2.5. A flowchart for operation of the address and the Read_POWER_ASSIGN command is shown in Figure 104-13. Table 104-11 illustrates the	Comment Type E	Comment	Status A			ΕZ
						EZ
	Repeated "Table	e" in "Table Table 10	4-4a"			EZ
contents of the POWER_ASSIGN register." With "After receiving a Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN	·	e" in "Table Table 10	4-4a"			EZ
contents of the POWER_ASSIGN register." With "After receiving a Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for	Repeated "Table <i>SuggestedRemedy</i> Delete the first "T		4-4a"			EZ
contents of the POWER_ASSIGN register." With "After receiving a Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read_POWER_ASSIGN command is shown in Figure	SuggestedRemedy Delete the first "T	Table"				EZ
contents of the POWER_ASSIGN register." With "After receiving a Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read_POWER_ASSIGN command is shown in Figure 104-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." -On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return	SuggestedRemedy Delete the first "T Response					EZ
ontents of the POWER_ASSIGN register." With "After receiving a ead_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for peration of the address and the Read_POWER_ASSIGN command is shown in Figure 04-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return OLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,	SuggestedRemedy Delete the first "T	Table"				EZ.
contents of the POWER_ASSIGN register." With "After receiving a Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ead payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for operation of the address and the Read_POWER_ASSIGN command is shown in Figure 104-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return /OLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO, POWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O	SuggestedRemedy Delete the first "T Response	Table" Response S		L 47	# <u>r02-63</u>	
ontents of the POWER_ASSIGN register." With "After receiving a lead_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ead payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for peration of the address and the Read_POWER_ASSIGN command is shown in Figure 04-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return OLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO, OWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O "RM:M"	SuggestedRemedy Delete the first "T Response ACCEPT.	Table" Response S	Status C		# <u>r02-63</u>	
Intents of the POWER_ASSIGN register." With "After receiving a bad_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for eration of the address and the Read_POWER_ASSIGN command is shown in Figure 4-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." In P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return DLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO, DWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O RM:M"         Image: Sc 104.4.6       P 99       L 27       # [r02-9]	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104	Table" Response S 4.5.6	Status <b>C</b> P <b>102</b> Analog Device		# <u>r02-63</u> Power	
Intents of the POWER_ASSIGN register." With "After receiving a bad_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for eration of the address and the Read_POWER_ASSIGN command is shown in Figure 4-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register." In P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return DLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO, DWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O RM:M"         A       SC 104.4.6       P 99       L 27       # [r02-9]         v, Peter       Ciena	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction	Table" <i>Response S</i> 4.5.6 <i>Comment</i> on times need to be r	Status <b>C</b> P <b>102</b> Analog Device Status <b>A</b> modified to acco	es Inc. ount for longer s	Power	
Intents of the POWER_ASSIGN register." With "After receiving a bad_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for eration of the address and the Read_POWER_ASSIGN command is shown in Figure 4.13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         In P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return DLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO, DWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O RM:M"         In SC 104.4.6       P 99       L 27       # [r02-9]         In Peter       Ciena       E       Comment Status       A       EZ	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC	Table" Response S 4.5.6	Status <b>C</b> P <b>102</b> Analog Device Status <b>A</b> modified to acco	es Inc. ount for longer s	Power	
ontents of the POWER_ASSIGN register." With "After receiving a         ead_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN         ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for         beration of the address and the Read_POWER_ASSIGN command is shown in Figure         04-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         0n P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return         0LT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,         0WER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O         RM:M"         4       SC 104.4.6       P 99       L 27       # [r02-9]         w, Peter       Ciena         nent Type       E       Comment Status       A       EZ         the Additional information cell for Item 7 of Table 104-4, "104.4.6.4" is an external cross-	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC SuggestedRemedy	Table" <i>Response</i> S 4.5.6 <i>Comment</i> - on times need to be r CCP_Watchdog time	Status C P 102 Analog Device Status A modified to acco er to be from 100	es Inc. ount for longer s 00ms to1300ms	Power	
contents of the POWER_ASSIGN register." With "After receiving a         Read_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN         read payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for         opperation of the address and the Read_POWER_ASSIGN command is shown in Figure         104-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return         VOLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,         POWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O         CRM:M"         04       SC 104.4.6       P 99       L 27       # r02-9         ow, Peter       Ciena <i>imment Type</i> E       Comment Status       A       EZ         In the Additional information cell for Item 7 of Table 104-4, "104.4.6.4" is an external cross-reference.       gestedRemedy	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC SuggestedRemedy Change the edit t	Table" <i>Response S</i> 4.5.6 <i>Comment</i> on times need to be r	Status C P 102 Analog Device Status A modified to accc er to be from 100 2 L47) to add ar	es Inc. ount for longer s 00ms to1300ms n edit to item 15	Power	
contents of the POWER_ASSIGN register." With "After receiving a         ead_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN         ead payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for         peration of the address and the Read_POWER_ASSIGN command is shown in Figure         04-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return         OLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,         OWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O         RM:M"         4       SC 104.4.6       P 99       L 27       # r02-9         w, Peter       Ciena       EZ         nent Type       E       Comment Status       A       EZ         the Additional information cell for Item 7 of Table 104-4, "104.4.6.4" is an external cross-oference.       Page 104-4, "104.4.6.4" is an external cross-	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC SuggestedRemedy Change the edit t timeout. Edit the {	Table"       Response S         4.5.6       Comment of COP_Watchdog times         to Table 104-7 (P10)       e watchdog timeout I	Status C P 102 Analog Device Status A modified to acco er to be from 100 2 L47) to add ar imits as follows:	es Inc. ount for longer s 00ms to1300ms n edit to item 15 :	Power ignaling times. S 5- SCCP watchdog	ering
contents of the POWER_ASSIGN register." With "After receiving a         ead_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN         ead payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for         peration of the address and the Read_POWER_ASSIGN command is shown in Figure         04-13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         On P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return         OLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,         OWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O         RM:M"         4       SC 104.4.6       P 99       L 27       # r02-9         w, Peter       Ciena       EZ         on the Additional information cell for Item 7 of Table 104-4, "104.4.6.4" is an external cross-seference.       estedRemedy	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC SuggestedRemedy Change the edit t timeout. Edit the { {{15} {SCCP wata {{15} {SCCP wata {See 104.5.5}}	Table"       Response S         4.5.6       Comment of COP_Watchdog times         to Table 104-7 (P10)       e watchdog timeout I	Status C P 102 Analog Device Status A modified to acco er to be from 100 2 L47) to add ar imits as follows:	es Inc. ount for longer s 00ms to1300ms n edit to item 15 :	Power	ering
Intents of the POWER_ASSIGN register." With "After receiving a         ad_POWER_ASSIGN command, the PD shall respond with a 16- bit POWER_ASSIGN         ad payload followed by an 8-bit CRC8 field as specified in 104.7.2.5. A flowchart for         eration of the address and the Read_POWER_ASSIGN command is shown in Figure         4.13. Table 104-12 illustrates the contents of the POWER_ASSIGN register."         n P115, L9: Modify item PSE37 to change the Value/ Comment field from "Return         DLT_POWER_INFO and POWER_ASSIGN registers" to "Return VOLT_INFO,         WWER_INFO and POWER_ASSIGN registers" and change the Status field to "SCCP:O         WM:M"         SC 104.4.6       P 99       L 27       # r02-9         ant Type       E       Comment Status       A       EZ         the Additional information cell for Item 7 of Table 104-4, "104.4.6.4" is an external cross-       erence.         stedRemedy       ply character tag External to "104.4.6.4"       C	SuggestedRemedy Delete the first "T Response ACCEPT. Cl 104 SC 104 Stewart, Heath Comment Type T SCCP transaction Increase the TSC SuggestedRemedy Change the edit t timeout. Edit the { {{15} {SCCP wata {{15} {SCCP wata {See 104.5.5}}	Table"       Response S         4.5.6       Comment I         f       Comment I         on times need to be r       CCP_Watchdog time         to Table 104-7 (P10)       e watchdog timeout I         tcchdog timeout I       TSC	Status C P 102 Analog Device Status A modified to acco er to be from 100 2 L47) to add ar imits as follows: CCP_watchdog}	es Inc. ount for longer s 00ms to1300ms n edit to item 15 :	Power ignaling times. S 5- SCCP watchdog	ering

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **102** Li **47**  Page 6 of 30 8/15/2019 2:50:49 PM

C/ 104	SC 104.9.4.3	P 115	L <b>29</b>	# r02-	-65	CRM:M} {Ye	h a 16-bit VO es [] N/A []} }	
Stewart, H Comment		Analog Device Inment Status <b>A</b>	es Inc.		Powering	PDs that im {{SCCP32}	(8-bit Read Po plement CRM (Reception of	1} {S Rea
							h a  16-bit PO RM:M} {Yes []	
SCCP " {{SCC implen {{SCC	15, L29 insert rows for r 34, SCCP35, SCCP36 a P29} {8-bit Read VOLT_ nent CRM} {CRM:M} {76 P30} {Reception of Rea	after last item SCCP28 INFO command} {104 es [] N/A []} } d VOLT_INFO function	3 as shown belo I.7.2.6} {Suppor n command} {10	w: ted by all PDs 04.7.2.6} {PD s	that shall	{{SCCP33} and PDs tha {{SCCP34} shall transm {SCCP:O C {{SCCP35} and PDs tha	{8-bit Write Po at implement ( {Reception of hit a 16-bit PO RM:M} {Yes [] {8-bit Read Po at implement ( {Reception of	OWI CRM Wri OWE ] N/A OWI OWI
{Yes [] {{SCC implen	nd with a 16-bit VOLT_I   N/A []} } P31} {8-bit Read POWE nent CRM} {CRM:M} {Ye P32} {Reception of Rea	R_INFO command} { es [] N/A []} }	104.7.2.7} {Sup	ported by all P	Ds that		nd with a 16-b P:O CRM:M} {	
respor	nd with a 16-bit POWER					C/ 146 SC	C 146.3.3.1	
{{SCC that im {{SCC shall tr {CRM: {{SCC that im {{SCC shall re	M} {Yes [] N/A []} } P33} {8-bit Write POWE pplement CRM} {CRM:M P34} {Reception of Writ ransmit a 16-bit POWEF M} {Yes [] N/A []} } P35} {8-bit Read POWE pplment CRM} {CRM:M} P36} {Reception of Read espond with a 16-bit PC CRM:M} {Yes [] N/A []} }	I} {Yes [] N/A []} } e POWER_ASSIGN fu R_ASSIGN write paylo :R_ASSIGN command {Yes [] N/A []} } d POWER_ASSIGN fu DWER_ASSIGN read	unction commar ad followed by a d} {104.7.2.9} {S unction commar	nd} {104.7.2.8} an 8-bit CRC8 supported by al nd} {104.7.2.9}	{PSE field} II PDs {PD	- SSD VEC - ESD VEC Some of the the draft sta http://www.iv which incluc In state SSI {tx_symb_tr	T 5 PCS Transm TOR calls RN TOR calls RN e changes reg indard. These eee802.org/3/ des the followi D VECTOR re iplet, tx_dispa D VECTOR re	ID_E ID_E gardin e cha /cg/p ing: eplac arity}
	PT IN PRINCIPLE.						iplet, tx_dispa	
add ne Insert	15, L29 ew subclause 104.9.4.7 rows for new Items SCC anged rows			SCCP28 as fo	llows	SuggestedReme		
not sh							SD VECTOR r SD VECTOR r	
	sert rows for new items 35, SCCP36 after last it			32, SCCP33, S	CCP34,	Response ACCEPT.		Res
PDs th	P29} {8-bit Read VOLT_ nat implement CRM} {SC P30} {Reception of Rea	CCP:O CRM:M} {Yes [	] N/A []} }	-				
{{SCC		d VOLT_INFO function	n command} {10			eneral		

\_INFO read payload followed by an 8-bit CRC8 field} {SCCP:O

NER\_INFO command} {104.7.2.7} {Supported by all PSEs and SCCP:O CRM:M} {Yes [] N/A []} }

ead POWER\_INFO function command} {104.7.2.7} {PD shall ER\_INFO read payload followed by an 8-bit CRC8 field} I/A []} }

VER\_ASSIGN command} {104.7.2.8} {Supported by all PSEs RM} {SCCP:O CRM:M} {Yes [] N/A []} }

/rite POWER\_ASSIGN function command} {104.7.2.8} {PSE ER\_ASSIGN write payload followed by an 8-bit CRC8 field} J/A []} }

VER\_ASSIGN command} {104.7.2.9} {Supported by all PSEs RM} {SCCP:O CRM:M} {Yes [] N/A []} }

ead POWER\_ASSIGN function command} {104.7.2.9} {PD POWER\_ASSIGN read payload followed by an 8-bit CRC8 es [] N/A []} }

C/ 146	SC 14	6.3.3.1	P 133	3	L 30	#	r02-21	
McCarthy,	Mick		Analog	Devices In	с.			
Comment	Туре <b>т</b>	-	Comment Status	4				PCS

state diagram uses undefined functions in certain states:

ESD; should be RND SSD4

ESD; should be RND ESD4

ding delimiter randomization were not transcribed correctly into hanges are recorded in /public/May2019/i-284%20Delimiter%20Randomization.txt,

ace tx\_disparity <= 2, tx\_symb\_triplet <= SSD4 by v} <= RND\_SSD4(Syn-1[4]). ace tx\_disparity <= 2, tx\_symb\_triplet <= ESD4 by y <= RND ESD4(Syn-1[4]).

Fransmit state diagram as follows:

place RND ESD with RND SSD4 place RND\_ESD with RND\_ESD4

esponse	Response Status	~
sponse	Nesponse Status	U.

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa 133

Li 30

C/ 146	SC 146.3.3.2.	2 <i>P</i> 134	L 28	# r02-70		C/ 146	SC 14	6.3.4.1.3	P 142	L 17	# r02-23
Graber, S	Steffen	Pepperl+Fuch	s AG			McCarthy,	Mick		Analog [	Devices Inc.	
Comment	51	Comment Status A			Later	Comment	Туре	E	Comment Status A		State Diagrams
The li Suggeste Chan are is timer being Response ACCE Chan	adRemedy adRemedy ge text from: "A co sound by the PCS of expiring synchronom serviced (see 146 e EPT IN PRINCIPLE ge text from: "A co	mb_timer and TX_TCLK is n ntinuous free-running timer. concurrently with symb_time busly to TX_TCLK, based on .5.4.5)." Response Status <b>C</b>	PMA_UNITDA r_done." to: "A PMA_UNITD/ PMA_UNITDA	continuous free-ru	ages Inning ive	The de diagra rcv_m intend spent Suggestec Chang A tim diagra	escription ms. ax_timer ed to refe n the DA <i>Remedy</i> e the def er used to m stays in	of rcv_ma: is not used r to the PC TA state. inition of th o determine n the RECE indition rcv	x_timer does not age in the 'PHY Receive S Receive state dia e rcv_max_timer to e the maximum amo	ree with how the ti e state diagram', v gram, and it does be as follows: bunt of time the Re er shall expire 4 m ecomes true upon	mer is used in the state which presumably is not determine the time eceive watchdog state as +/- 100 us after being
		ing timer. The symb_timer e st is serviced, synchronously				ACCE At P14	2 L17, cł	INCIPLE.	Y Receive state diag e diagram stays in th	ram stays in DAT	
C/ <b>146</b>	SC 146.3.4.1.	1 <i>P</i> 140	L 1	# r02-22				ondog olar	o alagiani olayo in a		
McCarthy	, Mick	Analog Device	es Inc.								
Comment	t Type E	Comment Status R		State D	iagrams						
gener	rated by the PCS F	eceiving variable does not ag Receive state diagram (Figur s not set to TRUE only when	e 146-9 and Fi	gure 146-10).	ble is						
Suggeste	edRemedy										
Chan	ge the definition of	the receiving variable to be	as follows:								
functi	nerated by PCS Re ion is not in an idle ues: TRUE or FALS		E, it indicates t	nat the PCS Rece	ive						
Response	9	Response Status C									
REJE Comr		e of the recirculation, on unc	hanged, descr	iptive text.							
does low p	not define "idle mo ower idle, but also	commenter. While the text odes". The variable receiving when the link fails or is waiti description is not precise.	g is not only se	t FALSE during id	e or						

Pa **142** Li **17** 

The signals clocked on MII RX CLK need to have a minimum setup time of 10 ns. Therefore it is important to add synchronization with the MII RX CLK, where it is missing in the PCS receive state diagram. Additionally "receive_overrun_detected" variable at the input condition of WAIT SCRAMBLER state can never be TRUE, while receiving is FALSE, thus this can be removed to prevent a possible ambiguity. In state LINK FAILED, RX_DV is set to TRUE. As this state may also be entered out of BAD SSD of BAD ESD states in case of a receive overrun (or also the SSD decoding states in case the link status or local receiver status gets bad), where RX_DV is FALSE in these states, RX_DV assignment in LINK FAILED state should be removed to prevent an accidental indication of an error in data reception (in this case in LINK FAILED state the former status of RX_DV will be returned). SuggestedRemedy P143, L2: Change input condition of WAIT SCRAMBLER state from: "pcs_reset + ((!receiving) * ((loc_rcvr_status = NOT_OK) + (link_status = FAIL) + rcv, overrun detected)" to: "pcs_reset + (RSTCD * (!ncceiving) * ((loc crvr status = retyred) * (loc_rovr_status = NOT_OK) + (link_status = FAIL) + rcv, overrun detected)" to: "pcs_reset + (RSTCD * (!ncceiving) * ((loc crvr status = KIC 146 SC 146.5.5.1 P 163 L 18 # r02-11 Anslow, Peter Ciena													
Comment TypeTComment Status AEaThe signed solded on MIR XCLK need to have a minimum set time of 10 n.The signed solded on MIR XCLK need to have a minimum set time of 10 n.Comment Status AEXThe redore it is important to add synchronization with the MIR XCLK, where it is missing in the PCS receive statu diagram. Additionally receive use and miniput. In status of condition is the Status CComment Status AEXStatus Status AComment Status AEXCase of a receive statu diagram. Additionally receive use and miniput. In status of condition is the Status CStatus Status CLINK FAILED state form TeXINING state to SLENT state for condition is the status of condition is the status of condition is the Status AEXSuggestedRemedyP143, L3: Change input condition of WAIT SCRAMBLER state form: "pcs_reset + (frecowing)" (floc_row, status = NDT_OK) + (link, status = FAIL)", for cover, status = NOT_OK) + (link, status = FAIL) + (row covernin, detected)" is instrong, time, done + (mintraining_time_done + (mintr	C/ 146	SC 146.3.4.1.4	P 143	L 1	# r02-69		C/ 146	SC	146.4.4.3	P <b>153</b>	L <b>24</b>	# <u>r02-17</u>	
Therefore it is important to add synchronization with he MIR XCLK, where it is missing in the PCS receive state diagram. Additionally "receive, overun, detected" variable at the input condition of WAIT SCRAMBLER state can even be TRUE, while receiver states is indeced out of BAD SSD states is not an even be TRUE, while receiver states is a loss of the new Yes (while receiver) states is ease an even to TRUE. While receiver states is a case the link status or local is there, but the arc itself is missing in the interval of a PSD economic of the PSD receive overun (or also the SSD decoding states in case the link status or local is there but the arc itself is missing in the returned.       Arc (from TRAINING state to SILENT state for condition "maxtraining_timer_done" (blacked) is more status or local change, as this arc got accidently missed from D3.1 to D3.2.         SuggestedRemedy       P143, L2. Change input condition of WAIT SCRAMBLER state from: "pcs_reset + ((from shifts) = FAIL) + (rcc, voerun, detected)".       P143, L2. Change input condition of UNAT SCRAMBLER state from: "pcs_reset + ((from shifts) = FAIL) + (rcv, overun, detected)".       P143, L2. Change input condition of UNAT SCRAMBLER state from: "pcs_reset + ((from shifts) = FAIL) + (rcv, overun, detected)".       P143, L2. Change input condition of UNAT SCRAMBLER state from: "pcs_reset + ((from shifts) = FAIL) + (rcv, overun, detected)".       P143, L2. Change input condition of UNAT SCRAMBLER state from: "pcs_reset + ((from Shifts) = FAIL) + (rcv, overun, detected)".       P143, L2. Change input condition of UNAT SCRAMBLER state from: "pcs_reset + ((from Shifts) = FAIL) + (rcv, overun, detected)".       P143, L10. Remove TRAINING state states is referenced.       P143, L10. Remove TRAINING state states is referenced. After a change in the PHT Cortio state diagram, from D2.0 having some intermediae steps to D3	Graber, S	teffen	Pepperl+Fuch	ns AG			Graber, St	teffen		Pepperl+Fuch	hs AG		
Therefore it is important to add synchronization with the MII RX CLX, where it is missing in the PCS receive state (directive state) directive state) directive state) directive state (directive state) directive st	Comment	Type T	Comment Status A			Later	Comment	Туре	Е	Comment Status A			ΕZ
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case of a receive surrun (or also the SSD decoding states in case the link status or local receiver status gets bad, where XZ, DV is FALSE in these states, RX, DV will be treturned).       Add the required arc from TRAINING state to SILENT state for condition fail arc equire states, RX, DV will be treturned).         Suggested/Remedy       P143, L2: Change input condition of WAIT SCRAMBLER state from: 'pcs_reset + ((!receiving) * ((!oc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)'' to 'psc_reset + form: 'receiving' * ((!oc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)'' to 'psc_reset + (!itereving) * (!oc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)'' to 'receiving * (!(loc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)'' to 'receiving * (!(loc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)'' to 'receiving * (!(loc_rcvr.status = NOT_OK) + (!ink, status = FALL) + rcv_overun_detected)''.         P143, L10: Remove 'RX_DV <= TRUE'' from LINK FAILED state.							Suggestee	dReme	dy				
returned). SuggestedRemedy Pri43, L2: Change input condition of WAIT SCRAMBLER state from: "pcs_reset + ((!receiving) * ((!oc_rcvr_status = NOT_OK) + (!ink_status = FAIL) + tro_overrun_detected) * (!receiving) * ((!oc_rcvr_status = NOT_OK) + (!ink_status = FAIL))*. Pi43, L3: Change input condition of LINK FAILED state from: "receiving * ((!oc_rcvr_status = NOT_OK) + (!ink_status = FAIL) + tro_overrun_detected) * "RSTCD * receiving * ((!oc_rcvr_status = NOT_OK) + (!ink_status = FAIL) + tro_overrun_detected)". Pi43, L10: Remove *RX_DV <= TRUE* from LINK FAILED state. Pi43, L40: Change exit conditions of BAD SSD and BAD ESD states from: "check_idle" to: "RSTCD * check_idle". Response Response Status C ACCEPT. C/ 146 SC 146.4.2 P 152 L9 # [02-68] Graber, Steffen PepperI+Fuchs AG Comment Type E Comment Status A Later In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the NOTE to relate to statue argum, from 2.0 khaing some intermediate steps to D3.2, this now needs to reference the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate to the SILENT state. Response Response Status C Change in the NOTE to relate the SILENT state. Response Response Status C Change in the NOTE to relate the SILENT state. Response Response Status C Change in the NOTE to relate the SILENT state. Response Response Status C Change in the NOTE to relate the SILENT state. Response Response Status C Change in the NOTE to relate the SILENT state. Response Response	case ( receiv	of a receive overrun ( ver status gets bad), v	or also the SSD decoding where RX_DV is FALSE is	g states in case n these states,	the link status or le RX_DV assignmer	ocal nt in	"maxt	raining_					-
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In the NOTE or page 152 the DISABLE TRANSMITTER' state to "SILENT' state.       Response       Response       Response Status       C         ACCEPT.       Comment Type       E       Comment Status       C         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT' state.       Later         Response       Response Status       C         Accept.       Comment Type       E         Comment Type       E       Comment Status       C         Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       Later       Response       Response Status       C         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       Later       Accept.       Accept.									140.3.3.1		210	# <u>102-11</u>	
Pital, L3: Change input condition of LINK FAILED state from: "receiving * ((loc_rcvr_status = FAIL) + rov_overrun_detected)" to: "RSTCD * receiving * ((loc_rcvr_status = FAIL) + rov_overrun_detected)".       Comment 101-48 was ACCEPT with suggested remedy:         Pital, L40: Remove "RX_DV <= TRUE" from LINK FAILED state.				(!receiving) * ((I	oc_rcvr_status =		,		F				ΕZ
P143, L49: Change exit conditions of BAD SSD and BAD ESD states from: "check_idle" to:       SuggestedRemedy         "RSTCD * check_idle".       Delete "1 x "         Response       Response Status       C         ACCEPT.       Cl       146       SC 146.4.4.2       P 152       L 9       # r02-68         Graber, Steffen       Pepperl+Fuchs AG       Later       ACCEPT.         Comment Type       E       Comment Status       A       Later         In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state.       Later         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       Response       Response Status       C         Response       Response Status       C       C       C       C         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       C       C       C         Response       Response Status       C       C       C       C       C	= NO ((loc_	T_OK) + (link_status rcvr_status = NOT_C	= FAIL) + rcv_overrun_de DK) + (link_status = FAIL)	etected)" to: "RS + rcv_overrun_	STCD * receiving *	status	"Delet make The s The n	te "1x" the mir econd p umber	nus sign an part has be should just	n en-dash" en done, but the first part ha	as not.		
<pre>"RSTCD * check_idle". Response Status C Response Status C ACCEPT. Cl 146 SC 146.4.4.2 P 152 L 9 # r02-68 Graber, Steffen Pepperl+Fuchs AG Comment Type E Comment Status A Later In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state. SuggestedRemedy Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state. Response Response Status C</pre>	,	_							dy				
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ACCEPT.       Cl 146       SC 146.4.4.2       P 152       L 9       # 102-68         Graber, Steffen       Pepperl+Fuchs AG         Comment Type       E       Comment Status       A       Later         In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state.       SuggestedRemedy         Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       Response       Response Status       C		—	Pesnonse Status C							Response Status C			
Cl       146       SC       146.4.4.2       P       152       L       9       #       r02-68         Graber, Steffen       Pepperl+Fuchs AG         Comment Type       E       Comment Status       A       Later         In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state.       SuggestedRemedy         Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.       Response       Response Status       C	•						ACCE	PT.					
Graber, Steffen       PepperI+Fuchs AG         Comment Type       E       Comment Status       A       Later         In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state.       Later         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.         Response       Response Status       C			D 450	1.0	# 00.00								
Comment Type       E       Comment Status       A       Later         In the NOTE on page 152 the DISABLE TRANSMITTER state is referenced. After a change in the PHY Control state diagram, from D2.0 having some intermediate steps to D3.2, this now needs to reference the SILENT state.       SuggestedRemedy         SuggestedRemedy       Change in the NOTE's text "DISABLE TRANSMITTER" state to "SILENT" state.         Response       Response Status       C				-	# r02-68								
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TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **163** Li **18** 

C/ 146	SC 146.8.1	P 171	L <b>46</b>	# r02-25
Maguire, V	/alerie	The Siemon C	Company	
Comment	Туре Т	Comment Status R		Big Ticket Item MDI

The commenter wishes to emphasize that a speedy path to publication of the P802.3cg amendment is best for industry adoption of single-pair Ethernet. The commenter is concerned that going from 2 MDI connectors to 0 MDI connectors to 1 MDI connector (or back to 2 MDI connectors) at this stage in the SA ballot cycle raises uncertainty about the stability of the single-pair Ethernet amendment. The commenter is also concerned that re-introduction of MDI connector information will delay publication through the generation of new negative votes. There is significant justification not to make further changes, including:

1. There is no precedent to identify an MDI interface for any single-pair Ethernet project. The market will determine the interface.

2. The success of single-pair applications today is not based on plug-and-play at the MDI. Virtually all of the MDI connections are screw terminals and that has not hindered adoption. The single-pair connector is a channel deployment differentiator, not an MDI feature.

3. A preference for the IEC 63171-1 connector or the IEC C 63171-1-6 connector or any other connector to be used in all "E" environments has never been made in a peer reviewed manner. Neither experts at TIA and ISO/IEC nor within the IEEE 802.3 community have not made such a determination based on an agreed-upon set of desired features and functionality. More problematic, the U.S., China, Mexico, and several other countries didn't even select either the -1 or the -6 connector as the preferred connector in E1/E2 environments.

4. Adding guidance out of alignment with TIA and ISO/IEC recommendations at a historically poorly attended interim meeting with limited PHY vendor representation puts P802.3cg at great risk of recommending the wrong connector. The commenter does not want a repeat of past history, as with the MT-RJ interface.

5. Neither the -1 connector nor the -6 connector is a good choice for multidrop implementations.

#### SuggestedRemedy

Do not add information related to specific IEC 63171 MDI interfaces into the amendment.

Response

Response Status C

REJECT. The CRG disagrees with the commenter.

Comment was discussed with comment r02-14. Response to comment r02-14 is:

### ACCEPT IN PRINCIPLE.

add Annex A (Bibliography) into the draft, with the editing instruction:

follows:

Editor's Note (to be removed prior to publication):

IEC 63171-1 is in the FDIS stage. The publication date for IEC 63171-1 will need to be inserted prior to publication of IEEE Std 802.3cg.

IEC 63171-1 Ed.1:20xx, Connectors for Electrical and Electronic Equipment -Part 1: Detail specification for 2-way, shielded or unshielded, free and fixed connectors: mechanical mating information, pin assignment and additional requirements for TYPE 1 / Copper LC Style

Editor's Note (to be removed prior to publication):

IEC 63171-6 is in the FDIS stage. The publication date for IEC 63171-6 will need to be inserted prior to publication of IEEE Std 802.3cg.

IEC 63171-6 Ed.1:20xx Connectors for Electrical and Electronic Equipment -Detail specification for 2-way and 4-way (data/power), shielded, free and fixed connectors for power and data transmission with frequencies up to 600 MHz

146.8.1 MDI connectors –Page 171, Line 52 add new paragraph;

Connectors meeting the mechanical requirements of IEC 63171-1 or IEC 63171-6 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY. The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 146–29 and Figure 146–30 respectively, and the mating interface is depicted in Figure 146–31. The IEC 63171-6 plug and jack are depicted (for informational use only) in Figure 146–32 and Figure 146–33 respectively, and the mating interface is depicted in Figure 146–34. These connectors should support link segment DCR characteristics for 1.02 mm (18 AWG) to 0.40 mm (26 AWG) in Table 146B–1.

Re-instate IEC 63171-1 plug and jack figures from D3.1 as Figures 146-29, 146-30, and 146-31.

Re-instate IEC 63171-6 plug and jack figures from D3.1 as Figures 146-32, 146-33, and 146-34.

147.9.1 MDI connectors - Page 220, Line 52 add new paragraph;

Connectors meeting the mechanical requirements of IEC 63171-1 or IEC 63171-6 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY. The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 147–21 and Figure 147–22 respectively, and the mating interface is depicted in Figure 147–23. The IEC 63171-6 plug and jack are depicted (for informational use only) in Figure 147–24 and Figure 147–25 respectively, and the mating interface is depicted in Figure 147–26. These connectors should support link segment DCR characteristics for 1.02 mm (18 AWG) to 0.40 mm (26 AWG) in Table 146B–1.

Re-instate IEC 63171-1 plug and jack figures from D3.1 as Figures 147-21, 147-22, and 147-23.

Insert the following references and associated editor's notes in alphanumeric order as

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 Pa 171

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
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 SORT ORDER: Page, Line
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Page 10 of 30 8/15/2019 2:50:49 PM Re-instate IEC 63171-6 plug and jack figures from D3.1 as as Figures 147-24, 147-25, and 147-26.

Editorial license to revise figure numbers as needed.

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Discussion alternatives:

Alternative "A" (above): ACCEPT IN PRINCIPLE (commenter's suggested remedy with correction for missing text, implementing "-1" connector with "may use")

Alternative B: ACCEPT IN PRINCIPLE - Same text as "A", but with -6 as well. (Return to the draft 3.0 text, with references corrected)

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Alternative C: Remain as is (no connector in the draft), (REJECT - No consensus to change).

Alternative D: ACCEPT IN PRINCIPLE - Same text as "A", but "shall" instead of "may".

Alternative E: ACCEPT IN PRINCIPLE - text as in "A", but with the following first paragraph subtituted. (paragraph to the figures remains the same) "Connectors meeting the mechanical specifications of IEC 63171-1 shall be used as the compatibility interface between the PMA and the medium. The use of other types of connectors, if any, within a PMA or within the medium, although not precluded, is outside the scope of this standard."

Motion #5:

Move to respond to comment r02-14 with Alternative B: (see straw polls, ACCEPT IN PRINCIPLE - Same text as "A", but with -6 as well. (Return to the draft 3.0 text, with references corrected)) M: Chris Diminico

S: Ron Nordin (Technical >= 75%) Y:13 N: 4 A: 7 Motion Passes

C/ 146	SC 146.8.1	P 179	L <b>1</b>	# r02-14
Diminico, Ch	ristopher	Panduit Corp.		

Comment Type TR Comment Status A Big Ticket Item MDI
\*\*\* Comment submitted with the file 101659700003-diminico\_3cg\_01\_0819.pdf attached \*\*\*

The continued success of BASE-T technology is largely predicated on leveraging the cost-effectiveness and plug-and-play simplicity ensured by compatibility at the MDI. We need to be forward thinking in developing a compatible user interface for BASE-T1. The MDI is to specify mechanical compatibility and electrical specifications not EMC conformance.

SuggestedRemedy

146.8.1 MDI connectors -Page 179, Line 1 add text; Connectors meeting the mechanical requirements of IEC 63171-1 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY.

Re-instate IEC 63171-1 plug and jack figures from D3.1. with text below.

Editorial license to revise figure numbers as needed. The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 146-29 and Figure 146-30 respectively, and the mating interface is depicted in Figure 146-31. The assignment of PMA signals to connector contacts for PHYs are given in Table 146-8.

147.9.1 MDI connectors -Page 227, Line 1 add text; Connectors meeting the mechanical requirements of IEC 63171-1 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY.

Re-instate IEC 63171-1 plug and jack figures from D3.1. with text below. Editorial license to revise figure numbers as needed.

The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 147-21 and Figure 147-22 respectively and the mating interface is depicted in Figure 147-23. The assignment of PMA signals to connector contacts for PHYs are given in Table 147-3. These connectors should support link segment DCR characteristics for 1.02 mm (18 AWG) to 0.40 mm (26 AWG) in Table 146B-1.

Response Status C

Response

ACCEPT IN PRINCIPLE.

----

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **179** Li **1**  Page 11 of 30 8/15/2019 2:50:49 PM add Annex A (Bibliography) into the draft, with the editing instruction:

Insert the following references and associated editor's notes in alphanumeric order as follows:

Editor's Note (to be removed prior to publication): IEC 63171-1 is in the FDIS stage. The publication date for IEC 63171-1 will need to be inserted prior to publication of IEEE Std 802.3cg.

IEC 63171-1 Ed.1:20xx, Connectors for Electrical and Electronic Equipment -Part 1: Detail specification for 2-way, shielded or unshielded, free and fixed connectors: mechanical mating information, pin assignment and additional requirements for TYPE 1 / Copper LC Style

Editor's Note (to be removed prior to publication):

IEC 63171-6 is in the FDIS stage. The publication date for IEC 63171-6 will need to be inserted prior to publication of IEEE Std 802.3cg.

IEC 63171-6 Ed.1:20xx Connectors for Electrical and Electronic Equipment -Detail specification for 2-way and 4-way (data/power), shielded, free and fixed connectors for power and data transmission with frequencies up to 600 MHz

146.8.1 MDI connectors –Page 171, Line 52 add new paragraph;

Connectors meeting the mechanical requirements of IEC 63171-1 or IEC 63171-6 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY. The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 146–29 and Figure 146–30 respectively, and the mating interface is depicted in Figure 146–31. The IEC 63171-6 plug and jack are depicted (for informational use only) in Figure 146–32 and Figure 146–33 respectively, and the mating interface is depicted in Figure 146–34. These connectors should support link segment DCR characteristics for 1.02 mm (18 AWG) to 0.40 mm (26 AWG) in Table 146B–1.

Re-instate IEC 63171-1 plug and jack figures from D3.1 as Figures 146-29, 146-30, and 146-31.

Re-instate IEC 63171-6 plug and jack figures from D3.1 as Figures 146-32, 146-33, and 146-34.

### 147.9.1 MDI connectors -Page 220, Line 52 add new paragraph;

Connectors meeting the mechanical requirements of IEC 63171-1 or IEC 63171-6 may be used as the mechanical interface to the balanced cabling. The plug connector is used on the balanced cabling and the MDI jack connector on the PHY. The IEC 63171-1 plug and jack are depicted (for informational use only) in Figure 147–21 and Figure 147–22 respectively, and the mating interface is depicted in Figure 147–23. The IEC 63171-6 plug and jack are depicted (for informational use only) in Figure 147–24 and Figure 147–25 respectively, and the mating interface is depicted in Figure 147–26. These connectors should support link segment DCR characteristics for 1.02 mm (18 AWG) to 0.40 mm (26 AWG) in Table 146B–1.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Re-instate IEC 63171-1 plug and jack figures from D3.1 as Figures 147-21, 147-22, and 147-23. Re-instate IEC 63171-6 plug and jack figures from D3.1 as as Figures 147-24, 147-25, and

re-instate IEC 63171-6 plug and jack figures from D3.1 as as Figures 147-24, 147-25, and 147-26.

Editorial license to revise figure numbers as needed. ==== Discussion alternatives:

Alternative "A" (above): ACCEPT IN PRINCIPLE (commenter's suggested remedy with correction for missing text, implementing "-1" connector with "may use")

Alternative B: ACCEPT IN PRINCIPLE - Same text as "A", but with -6 as well. (Return to the draft 3.0 text, with references corrected)

Alternative C: Remain as is (no connector in the draft), (REJECT - No consensus to change).

Alternative D: ACCEPT IN PRINCIPLE - Same text as "A", but "shall" instead of "may".

Alternative E: ACCEPT IN PRINCIPLE - text as in "A", but with the following first paragraph subtituted. (paragraph to the figures remains the same) "Connectors meeting the mechanical specifications of IEC 63171-1 shall be used as the compatibility interface between the PMA and the medium. The use of other types of connectors, if any, within a PMA or within the medium, although not precluded, is outside the scope of this standard."

#### Motion #5:

Move to respond to comment r02-14 with Alternative B: (see straw polls, ACCEPT IN PRINCIPLE - Same text as "A", but with -6 as well. (Return to the draft 3.0 text, with references corrected)) M: Chris Diminico S: Ron Nordin (Technical >= 75%) Y:13 N: 4 A: 7 Motion Passes

> Pa **179** Li 1

C/ 146 SC 146.1	1.4.2.2	P 181	L <b>43</b>	# r02-18	C/ 147	SC 147.3.2.	6	P 196	L 18	# r02-	71
Graber, Steffen		Pepperl+Fuch	s AG		Graber, St	effen		Pepperl+Fuc	hs AG		
Comment Type E	Comment	Status A		E				t Status A			Later
	erance in 146.5.4.1	has been char	nged in D3.2, ne	eds to be reflected in	TX_T	CLK should be "	TX_CLK".				
PICS.					Suggestee	dRemedy					
SuggestedRemedy		(/ 450/!!! -!				ge "TX_CLK (see				ner with the risir	ıg edge
15%"	5% to 2.4 v + 5%	%/- 15% and ci	nange 1.0 v +/-	5%" to "1.0 V + 5%/-	of IX_	_TCLK generate	d synchronou	sly with symb_ti	imer_done."		
Response	Response S	Status C				_CLK (see 22.2				h the rising edg	e of
, ACCEPT.		•			TX_T	CLK generated s	synchronously	with symb_time	er_done."		
C/ 147 SC 147.1		P 186	L 22	# r02-55	In Fig	ure 147-15 chan	ige "TX_TCLK	" with "TX_CLK	<b>*</b> #		
	I			# 102-55	At page	ae 214 line 42 re	enlace "To allo	w an easy sync	hronization of th	ne measuremer	nt
Brandt, David Comment Type <b>E</b>	Comment	Rockwell Auto	mation	PLC	equipi	, ment, the PHY s	hall provide a				
PLCA is not an opt			lause 148	PLC		nsmitted symbo		ion of the meas	urement equipr	nent the PHV e	hall
		in this bat of t				e access to the					nan
SuggestedRemedy Change from:					At page	ae 230, line 44 re	oplaco "TV T(		<u> </u>		
10BASE-T1S PHY	s optionally suppo	rt PHY Level C	ollision Avoidan	ce (PLCA), described	Response		• –	Status C	JLK		
in Clause 148.						PT IN PRINCIP		Status C			
To:					Chang	ge "TX_CLK (see	e 22.2.2.1) sh			ner with the risir	ıg edge
10BASE-T1S PHY	s support optional	Clause 148 PH	IY Level Collisio	n Avoidance (PLCA).	of TX_	_TCLK generate	d synchronou	sly with symb_ti	imer_done."		
Response	Response S	Status C			to "TX	_CLK (see 22.2	.2.1) shall be	generated from	symb_timer wit	h the rising edg	e of
REJECT. Comment is out of	scope of the recir	culation on unc	hanged text		TX_C	LK generated sy	nchronously w	with symb_timer	r_done."		
CRG has no conse			nangeu text.		In Fig	ure 147-15 chan	ige "TX_TCLK	" with "TX_CLK			
Straw Poll #1					At pag	ge 214 line 42 re	place "To allo	w an easy sync	hronization of th	ne measuremer	nt
I support (pick one A: Rejecting Comn		of scope with r	o conconsus to	change		nent, the PHY s		ccess to the syr	mbol rate clock	TX_TCLK, whic	h times
				he text as necessary.		nsmitted symbo		ion of the meas	urement equipr	nent. the PHY s	hall
A: 10 B: 4						e access to TX_				- ,	
						ge 230, line 44 (l				CLK" in "Feature	e", and
Strow Doll #1					chang	e description to	"PHY to provi	de access to T	X_CLK"		
Straw Poll #1 I support (pick one	)				Also c	hange in clause	e 148:				
A: Rejecting Comm	nent r02-55 as out					6.4 page 238 lir		"TX_TCLK" to "	TX_CLK"		
B: resolving Comm A: 10 B: 4	ent r02-55 with: "A	Accept in Princi	ple". Adjusting t	he text as necessary.							

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **196** Li **18**  Page 13 of 30 8/15/2019 2:50:50 PM

C/ 147	SC 147.3.3.7	P <b>202</b>	L <b>44</b>	# r02-20	Cl 147	SC 147.3.7	P2	05	L 10	# r02-57
Beruto, P	iergiorgio	Canova Tech	S.r.l.		Kim, Yongt	bum	NIO			
Comment	t <i>Туре</i> Е	Comment Status A		State Diagrams	Comment T	Type TR	Comment Status	R		State Diagrams
		ecution of the IF and precnt i ay be misinterpreted.	increment state	ments within the PRE	"Other	wise all the HE	B functions shall be disa	bled" has		ion, and the deleted text irate. The deletion
Suggeste	dRemedy					,	d be reversed and kept			
		e PRE state, remove the IF s	statement and i	ts embodied	Suggested	,				
	ctions.				Revers	e the change	, i.e. undo deleted text.			
		e PRE state, change the cor to "RSCD * (precnt != 4)"	idition of the rea	circulating arc from	Response		Response Status	W		
[4] In precn DEC0 " [5] In	figure 147-7, add tt <= precnt + 1 DDE(RXn-3) figure 147-7, add	e PRE state, delete the trans a new state "SCRAMBLER" a transition between the PRE	containing the	following statements: "	The rea function describ	Disagrees with ason that the mality describe bed is capture		and is un mit state	necessary. T diagram by th	•
		"RSCD * (precnt = 4)" e SCRAMBLER state, add a	recirculating a	c with the following	C/ 147	SC 147.5.5	5.1 P 2	16	L 51	# r02-12
	tion: "RSCD * (pre	,			Anslow, Pe	ter	Ciena			
		a transition between the SCF tion: "RSCD * (precnt = 9)"	KAMBLER STate	e and the "A" connector	Comment T	Гуре Е	Comment Status	Α		EZ
Response	e	Response Status C					en changed to "1 x 10^- er should just be 10^-7 a		-10 on the line	e above
In Fig "prec	nt <= precnt + 1	E. PRE state, change:			Suggested Delete	2		·		
•	ecnt > 3 THEN DDE(RXn-3)				Response ACCEF	PT.	Response Status	С		
precn	ecnt > 3 THEN t <= precnt + 1 DDE(RXn-3)									

precnt <= precnt + 1 END"

> Pa **216** Li **51**

C/ 147	SC 147.8	P 219	L <b>2</b>	# r02-58
Kim, Yongbu	ım	NIO		
Comment Ty	vpe TR	Comment Status R		Mixing Segment

#### [Related to unresolved disapprove comment]

Shared medium with 10 cm stubs (at least 8 and 25 meters in reach) references 147.7, which specifies a single link (with no stubs) up to 15 meters. So this specification basically says 40% longer reach with at least 8 x 10 cm unterminated stubs must meet the same transmission medium characteristics of a single terminated link. And this requirement is stated without any guidance on how one could met them. In an installation where one stub is added, the specification states that any to any stub must meet the same requirement -- requiring the number of measurement of 1 + ... + (n-1).

The comment response (unsatified) states that there are methods that could be used WITHOUT stating what method could be used. If one exists, it should be stated and without which the standard is incomplete.

As an example, think coax (10BASE5) has very specific rules and methods on how each tap must be constructed (i.e. formal specifcation for the MDI) and how the medium must be marked so that reflections from the tap could be minimized (reduce chance of false collection deteect from all worst case reflections adding up at any particular point). Thin coax (10BASE2) also as formal MDI specification and coax segment installation requirments. These are examples of how standard includes details to assure interoperability and ease of installation. This clause on mixing segment characteristics states to meet a set of requirements (SHALL statements), but WITHOUT any details on how one could construct, preferrably incrementally, network segments that are assured to meet the requirements. Interoperability requirement only. No details that provide confidence one could be constructed in interoperable fashion. This mixing segment characteristics clause is grossly incomplete.

#### SuggestedRemedy

Specify how mixing segment characteristics could be met via specificatoin, methodology, or other means. Proposed change is that -- complete the draft.

Response

Response Status W

#### REJECT.

The proposed change in the comment does not contain sufficient detail so that the CRG can understand the specific changes that satisfy the commenter.

#### Further, the CRG disagrees with the commenter.

While the draft describes physical length and topology, those are not the requirements. The draft does not specify the physical length, gauge, twist pitch, loss per meter, or similar physical construction parameters of the medium, consistent with practice in IEEE Std 802.3. The main specifications related to the mixing segment length and stub topology are insertion loss (147.8.1) and MDI impedance limits (Table 147-4) (for full-duplex echo cancelled transmission, delay is relevant, but it is not relevant here). Analysis and measurements have been presented to the Task Force validating that mixing segments with the described 10 cm stubs, 8 nodes, and 25 meters in length can be constructed which meet the insertion loss specified for mixing segments. See, e.g., http://www.ieee802.org/3/cg/public/Sept2017/kaindl\_matheus\_3cg\_01c\_09\_2017.pdf

, and

http://www.ieee802.org/3/cg/public/Jan2018/Caliskan\_3cg\_01a\_0118.pdf.

C/ 147	SC 147.12.3	P <b>226</b>	L 11	# r02-54	
Brandt, Da	avid	Rockwell A	utomation		
Comment	Type E	Comment Status A			ΕZ
None	of the PICS are c	onditioned on the condition	nal PICS Item *PL	CA.	
Suggested	dRemedy				
Remo	ve the "147.12.3	Major capabilities/options"	row for Item *PLC	A.	
Response	•	Response Status C			
ACCE	PT.				

C/ 147	SC 147.12.3	P 226	L <b>26</b>	# <u>r02</u> -	53
Brandt, Da	avid	Rockwell Aut	omation		
Comment	Type <b>T</b>	Comment Status A			PICS

As shown in Figure 147-1, the MEDIUM is outside of the PHYSICAL layer. The PICS for "147.12.4.7 Point-to-point link Segment characteristics" and "147.12.4.8 Mixing Segment characteristics" do not directly apply to the physical layer.

As a correct example, "146.11.3 Major capabilities/options" creates an Item "\*INS" that is further used to qualify "146.11.4.4 Link Segment characteristics". INS indicates the PICS apply to "installation practice and cabling specifications". Clause 147 should have similar qualifications.

SuggestedRemedy

Append the following row to the end of the table "147.12.3 Major capabilities/options": \*INS; Installation / cabling; 147.7, 147.8; Items marked with INS include installation practices and cabling specifications not applicable to a PHY manufacturer.; O; Yes [] No []

Replace for all rows (Items PPLS1-5) of "147.12.4.7 Point-to-point link Segment characteristics" the Status of "M" with the Status of "INS:M"

Replace for all rows (Items MXS1-3) of "147.12.4.8 Mixing Segment characteristics" the Status of "M" with the Status of "INS:M"

Response Response Status C

ACCEPT.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

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C/ 148	SC 148.1	P 234	L 9	# r02-30	C/ 14	8 3	SC 148.2	P 23	5 L	1	# r02-60
Kabra, Loke	esh	Synopsys, Inc	C.		Kim,	Yongbum	1	NIO			
Comment 7	Гуре Е	Comment Status A		E	Z Comi	nent Typ	9 TR	Comment Status	R		PLCA
(with ca Suggested	apital letter) Remedy e "reconciliatio	ement that the RS should be re on sublayer" with "Reconciliatic <i>Response Status</i> <b>C</b>		econciliation Sublayer	"   6  8  7  1  1	f the nod evel of a ( 02.3WG a) how no nultiple no andled in ailure = st	e with ID = ( CSMA/CD n ballot and o de_id=0 is c ode_id=0 no IEEE 802.4 ill operation	adds little value and ac ) fails, the network is s etwork without PLCA." In SA ballot cycles) are thosen, handling when de exists, etc all the token bus or other sir al sound more like ma ult handling, completer	till operational The set of uns : node_id=0 fail chosen central nilar systems. rketing and pro	with the s satisfied o ls, b) does l controlle Simply s ovides little	ame performance concerns (from s not exist at all, c) r complexities that are stating node_id=0 e overall benefit to the
					Sugg	estedRer	nedy				
					D	elete this	new senter	nce added in D3.2 in its	s entirety.		
					T T T C C P F F C C P F F '' C S S d e e < (() 1 "" ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !	EJECT. he CRG he sente he refere rom a dif onsensu om opera- comment comment esponse ACCEPT Explanat /hen Noc SMA/CD tate Diag iagram c nabled, b end explo changes [] At page 48.2 add f the nod evel of a (2 2) In Figu polace the local_noc 8] In Figu	nce was not nced senter ferent comm s of the CR( ation when N r01-223 was there is if N to commen IN PRINCIF ory note - nc le ID = 0 fail network. Su ram. Howev bould get stud efore the fir anatory note to draft follow 234, apper ed by comm e with ID = ( CSMA/CD n re 148-3 in t deID = 0) * ( re 148-4 in t t + (!plca_e)	lode ID = 0 fails or dis s: "Overview does not ode ID = 0 fails or disa t r01-223 was: PLE. bt to be incorporated in s or disappears the ne uch behavior has been rer, there is one missir ck if the Node with ID = st BEACON is transmi > w): nd the following senten ent r01-222: 0 fails, the network is s etwork without PLCA." he transition from NEX (local_nodeID = 0) * (c curID >= plca_node_c he global transition to n)" to "plca_reset + (!p	ncern from this onse to "Must b the commente provides a use appears. even give a hin ppears." the draft> twork behaves intentionally d g corner case = 0 fails immed tted. ce to the end o till operational (T_TX_OPPOF curID >= plca_r ount) + curID = the NORMAL s lca_en) + (!plca	be satisfie er indicatir ful descrip nt as to wh s like a noo lefined in t where the liately afte of the new with the s RTUNITY node_cour = 255". state, char a_status)'	d" comment r01-223 ng satisfaction. botion of what to expect nat sort of recovery n-PLCA enabled the PLCA Control e mentioned state er PLCA has been v last paragraph for same performance to the B connector, nt)" with nge the condition ".
	echnical requi	ired ER/editorial required GR/	deneral required	T/technical E/editoria	"			he transition from the l n * (!plca_reset) * plca			Page 16 of 30

TTE. Tratechined required Eracutonal required Oragener			1 age 10 01 00
COMMENT STATUS: D/dispatched A/accepted R/rejected	RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn	Li 1	8/15/2019 2:50:50 PM
SORT ORDER: Page, Line			

<pre>[5] In Figure 148-4 in the TRANSMIT state box replace " IF COL THEN SIGNAL_STATUS &lt;= SIGNAL_ERROR ELSE" with " IF COL THEN SIGNAL_STATUS &lt;= SIGNAL_ERROR a &lt;= 0 ELSE " [6] At page 249, line 3 append the following: " plca_status see 148.4.7.2 "</pre>	Cl 148       SC 148.2       P 235       L 11       # Image: Common Status         Kim, Yongbum       NIO         Comment Type       TR       Comment Status       R         This added paragraph is adds little value to the draft and frankly appears more limmarketing statement than Ethernet specification. Mixed PLCA+CSMA/CD and Cooperation. configuration, etc are not specified, so this paragraph does not serve material purpose (except, perhaps as marketing statement).       "PLCA-enabled nodes may be used in the same CSMA/CD collision domain as enabled nodes.         As the percentage of non-PLCA enabled nodes increases, performance advanta decrease. If the node       with ID = 0 fails, the network is still operational with the same performance level CSMA/CD network without PLCA."         SuggestedRemedy						
	00			- D2 0 in its setin	-4.		
	Delete	this new pa	aragraph added i	n D3.2 in its entir	ety.		
	The pa The re (from a Conse from o withou Comm "Overv of sucl Respo Add ne CSMA	CT. RG disagree aragraph wa ferenced pa a different co nsus of the peration of a t PLCA. view does no h on networl nse to comr ew sixth (fina /CD collision	es with the comm is not added relat aragraph was add ommenter) and r CRG is that the a network compr ? is: bt even give a hir k performance." ment r01-222 wa al) paragraph to n domain as non	tive to a concern ded in response t esulted in the con sentence provide ising a mixture of nt as to what hap s: 148.2, "PLCA-ena	o "Must be satis mmenter indicat is a useful descr i nodes with PLC pens in a mixed abled nodes ma nodes. As the pe	fied" comment r01-222 ing satisfaction. ription of what to expect CA enabled and nodes network or the impact y be used in the same ercentage of non-PLCA	

Pa **235** Li **11** 

	P <b>236</b>	L <b>5</b>	# r0	2-61	C/ 148	SC 148.4.5.1	P <b>2</b>	40	L 15	#	r02-37	
Kim, Yongbum	NIO				Law, David		Hewle	ett Packard	Enterprise			
Comment Type TR C	Comment Status R			PLCA	Comment 7	уре Е	Comment Status	D				ΕZ
This new statement is factu the PLCA RS as an extensi					Should	n't RXlat be RX<	SUB>lat bas	sed on delta	RX <sub>lat</sub>		above?	
*REPLACES* Clause 22 RS provided by the PLCA RS a desirable but more correcct	S. The previous sentences an extension to the MII	e "This subclause specified in Clau	e specifies	services	Suggestedl See co	-						
	. Indri the new sentence i	103.2.			Proposed F	Response	Response Status	Z				
uggestedRemedy					REJEC	т.						
Suggest replacing the refer "This subclause specifies so Clause 22."			laces RS s <sub>l</sub>	pecified in	This co	mment was WIT	HDRAWN by the co	ommenter.				
REJECT. Comment is arguably out of	esponse Status W	o regirgulation V	Nhilo this ir	troductory	[19] De	lete lines 10 thro	d in response to com ough 20 of page 240 ough " appearing a	. This remov	es the text be	eginning		
sentence and subclause wa	as changed, it was touche	ed in a way that m			C/ 148	SC 148.4.5.1	P <b>2</b>	40	L <b>25</b>	#	r02-38	
word. The comment does r	tot touch on the change t	hat was made.			Law, David		Hewle	ett Packard	Enterprise		-	_
CRG disagrees with the cor				not replace	Comment 7	<i>уре</i> Е	Comment Status	Α			OOS Edit	orial
the Clause 22 RS, but defin descriptions, fit with the Cla specifications of the Clause	ause 22 definitions by mal	king extensive ref		where the		t that ' node o t opportunity'.	wns now a transmit o	opportunity	' should rea	d ' nod	le now owns	s a
•					Suggested	Remedy						
7/148 SC 148.4.5.1	P <b>240</b>	L 10	# r0	2-36	See co	mment.						
aw, David	Hewlett Packa	rd Enterprise			Response		Response Status	С				
comment Type E C	Comment Status D		-	OS Editorial		T IN PRINCIPL		1.25				
Suggest that 'After syncing complete, the'.	is done, the' is change	d to read 'After s	ynchronisa	ition is	Note th	at the comment	"now owns" on P240 is out of scope of the	e recirculati				
Suggest that 'After syncing complete, the'. uggestedRemedy	is done, the' is change	d to read 'After s	ynchronisa	ition is	Note th previou	at the comment s draft, but is a r	is out of scope of the nonsubstantive edito	e recirculation rial change	which improv	es clarity	/.	
Suggest that 'After syncing complete, the'. uggestedRemedy See comment.	is done, the' is change	ed to read 'After s	ynchronisa	ition is	Note th	at the comment	is out of scope of the nonsubstantive edito	e recirculation rial change 40	which improve	es clarity		
Suggest that 'After syncing complete, the'. uggestedRemedy See comment. roposed Response Re	is done, the' is change esponse Status Z	ed to read 'After s	ynchronisa	ition is	Note th previou <i>Cl</i> <b>148</b> Law, David	at the comment s draft, but is a r SC 148.4.5.1	is out of scope of the nonsubstantive edito P2 Hewle	e recirculatio rial change <b>40</b> ett Packard	which improve	es clarity	/.	
Suggest that 'After syncing complete, the'. uggestedRemedy See comment.		ed to read 'After s	ynchronisa	ition is	Note th previou Cl 148 Law, David Comment 1	at the comment s draft, but is a r SC 148.4.5.1 Type E	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status	e recirculation rial change 40 ett Packard A	which improve <i>L</i> 27 Enterprise	es clarity #	r. 102-39	EZ
Suggest that 'After syncing complete, the'. uggestedRemedy See comment. troposed Response Re	esponse Status Z		ynchronisa	ition is	Note th previou Cl 148 Law, David Comment 1 Sugges	at the comment s draft, but is a r SC 148.4.5.1 Type E	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status wns now a transmit of	e recirculation rial change 40 ett Packard A	which improve <i>L</i> 27 Enterprise	es clarity #	r. 102-39	
Suggest that 'After syncing complete, the'. uggestedRemedy See comment. roposed Response Re REJECT. This comment was WITHDI (Text has been removed in [19] Delete lines 10 through	esponse Status Z RAWN by the commenter response to comment r02 a 20 of page 240. This ren	r. 2-33, shown, in-pa noves the text be	part, below:		Note th previou Cl 148 Law, David Comment 1 Sugges	at the comment s draft, but is a r SC 148.4.5.1 Type E tt that ' node of t opportunity'. Remedy	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status wns now a transmit of	e recirculation rial change 40 ett Packard A	which improve <i>L</i> 27 Enterprise	es clarity #	r. 102-39	
Suggest that 'After syncing complete, the'. uggestedRemedy See comment. roposed Response Re REJECT. This comment was WITHDI (Text has been removed in	esponse Status Z RAWN by the commenter response to comment r02 a 20 of page 240. This ren	r. 2-33, shown, in-pa noves the text be	part, below:		Note th previou Cl 148 Law, David Comment T Sugges transm Suggested See co	at the comment s draft, but is a r SC 148.4.5.1 Type E tt that ' node of t opportunity'. Remedy	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status wns now a transmit of	e recirculati rial change 40 ett Packard A opportunity	which improve <i>L</i> 27 Enterprise	es clarity #	r. 102-39	
Suggest that 'After syncing complete, the'. uggestedRemedy See comment. roposed Response Re REJECT. This comment was WITHDI (Text has been removed in [19] Delete lines 10 through	esponse Status Z RAWN by the commenter response to comment r02 a 20 of page 240. This ren	r. 2-33, shown, in-pa noves the text be	part, below:		Note th previou Cl 148 Law, David Comment T Suggested See co Response ACCEF Change Note th	at the comment s draft, but is a r SC 148.4.5.1 Sype E it that ' node or t opportunity'. Remedy mment. PT IN PRINCIPLI e "owns now" to at the comment	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status wns now a transmit of Response Status	e recirculation rial change 40 ett Packard A opportunity C 0 L27 e recirculation	which improve <i>L</i> 27 Enterprise ' should read on on text unc	es clarity # d ' nod	r. r02-39 le now owns	
Suggest that 'After syncing complete, the'. SuggestedRemedy See comment. Proposed Response Re REJECT. This comment was WITHDI (Text has been removed in [19] Delete lines 10 through	esponse Status <b>Z</b> RAWN by the commenter response to comment r02 a 20 of page 240. This ren a " appearing at the MD	r. 2-33, shown, in-pa noves the text be I to CRS asserted	vart, below: ₂ginning witi d." )	h "After	Note th previou Cl 148 Law, David Comment T Suggest transm Suggested/ See co Response ACCEF Change Note th previou	at the comment s draft, but is a r SC 148.4.5.1 Sype E it that ' node or t opportunity'. Remedy mment. PT IN PRINCIPLI e "owns now" to at the comment	is out of scope of the nonsubstantive edito P 2 Hewle Comment Status wns now a transmit of Response Status E. "now owns" on P240 is out of scope of the	e recirculation rial change 40 ett Packard A opportunity C 0 L27 e recirculation	which improve <i>L</i> 27 Enterprise ' should read on on text unc	es clarity # d ' nod changed es clarity	r. r02-39 le now owns	s a

aw, David	3.4.5.1	P 240	L <b>34</b>	# r02-40	C/ 148	SC 148.4.5.1	P 240	L <b>45</b>	#	r02-43	
,		Hewlett Packar	d Enterprise		Law, David	Ł	Hewlett Pack	ard Enterprise			
Comment Type E	Comment	Status A		OOS Editorial	Comment	Туре Е	Comment Status A			OOS Ed	itoria
EARLY_RECEIN subclause is des	EARLY_RECEIVE s /E state, the PLCA C cribing the PLCA Co state diagram, not o	Control state diag	ram is waiting am, and the EAR		synchi Suggested	ronisation,'. IRemedy	be out of sync,' be change	ed to read ' might b	be out	of	
SuggestedRemedy					See co	omment.					
See comment.					Response		Response Status C				
Response ACCEPT IN PRI	Response S NCIPLE.	Status C			Chang	PT IN PRINCIPL ge "out of sync," t his is a nonsubs	o "out of synchronization,"				
On P240 L34 ch	ange "PLCA is waitir	ig" to "the PLCA	Control state dia	agram is waiting".	C/ 148	SC 148.4.5.2	P 241	L 14	#	r02-31	
previous draft, b	mment is out of scop ut nonsubstantively o LCA Control and the	orrects an ambi	guity which could	be misinterpreted to	Kabra, Lok <i>Comment</i> Incorre		Synopsys, In <i>Comment Status</i> <b>A</b> nanaged object	с.			ΕZ
C/ 148 SC 148 .aw, David	3.4.5.1	P <b>240</b> Hewlett Packar	L <b>36</b> d Enterprise	# r02-41	Suggested Replac		" with "acPLCAReset"				
	Comment : CEIVE state is then gram then remains in	kept until' sho		OOS Editorial to read 'The PLCA	Accom	PT IN PRINCIPL nodated by comm nse to comment PT.	nent r02-13.				
Response ACCEPT.	Response S	Status C			Replac		t" with "acPLCAReset" in two				
C/ 148 SC 148	8451	P 240	L <b>41</b>	# r02-42	C/ 148	SC 148.4.5.2		L 14	#	r02-13	
aw, David Comment Type E		Hewlett Packar		OOS Editorial	Maguire, V <i>Comment</i> This is		The Siemon Comment Status A Fable 30-11	Company			ΕZ
Suggest that '	might be out of sync '.	' be changed to	read ' might be	e out of	Suggested Replace		t" with "acPLCAReset" in two	o locations in line 14	L.		
synchronisation.					Response						
synchronisation. SuggestedRemedy					Response		Response Status C				
synchronisation.						DT					
synchronisation. SuggestedRemedy	Response S	Status C			ACCE	PT.					

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **241** Li **14** 

C/ 148	SC 148.4.5.2	P <b>241</b>	L <b>20</b>	# r02-32	C/ 148	SC 148.4.5.1	P <b>245</b>	L 51	# r02-1
Kabra, Lo	kesh	Synopsys, Inc.			Huszak, G	ergely	Kone		
Comment	• •	Comment Status R		Management	Comment	••	Comment Status A		State Diagrams
	ect reference to method	nanaged object; plca_en is co .2.1	ntrolled by acF	LCAAdminControl as		_	COPPORTUNITY->RESYN ativity that is not spelled out		•
Suggeste	dRemedy				Suggestee	lRemedy			
Repla	ice "aPLCAAdmin	State" with "acPLCAAdminCo	ontrol"				= 0) * (curID >= plca_node		
Response	9	Response Status C				_ , ,	curID >= plca_node_count))	) + (curID = 255)	"
REJE	CT.				Response		Response Status C		
		be of the recirculation on unch	anged text.			PT IN PRINCIPL			an the fellowing
	disagrees with the	AAdminControl changes the	state of the att	ibute			parenthesis to maintain pre cal_nodeID = 0) * (curID >=		
		variable plca_en reflects the					curID >= plca_node_count))		
C/ 148	SC 148.4.5.4	P 245	L 50	# r02-19	P202	1.33: Change "R	SCD * ((RXn = ESD) + (RXn	) != SSD) * (RXn	!= SYNC) *
				102 10	(!fc_s	upported))" to			,
Graber, S		Pepperl+Fuchs Comment Status A	SAG	State Diagrams	"RSCI	D * ((RXn = ESD)	+ ((RXn != SSD) * (RXn !=	SYNC) * (!fc_su	pported)))
Comment			TV atata ara m	0	P203	1 28-47. All the e	xit conditions on the lower h	alf ("C" and "D")	of Figure 147-8 need
		on of NEXT_TX_OPPORTUN	i i state are n	lissing.	paren	hesis: Add brack	ets around "Rxn != HB", "R	kn = HB", "Rxn !=	= BEACON", and "Rxn =
Suggeste	2						= is the non equal symbol).		
		= 0) * (curID >= plca_node_c curID >= plca_node_count)) +							
Response	9	Response Status C							
ACCE	EPT IN PRINCIPL	E.							
	nodated by comm								
	onse to comment								
	-	parenthesis to maintain prece	dence, includi	ng the following:					
		cal_nodeID = 0) * (curID >= pl							
"((loca	al_nodeID = 0) * (0	curID >= plca_node_count)) +	(curID = 255)'						
P202,	, L33: Change "RS	SCD * ((RXn = ESD) + (RXn !=	= SSD) * (RXn	!= SYNC) *					
	upported))" to			( )))					
RSC	$D^{\circ}$ ((RXN = ESD)	+ ((RXn != SSD) * (RXn != S	rinc) " (!tc_su	pported)))					
		kit conditions on the lower hal							
		ets around "Rxn != HB", "Rxn	= HB", "Rxn !=	BEACON", and "Rxn =					
BEAC	CIN CONDITIONS (!	= is the non equal symbol).							

Pa **245** Li **51** 

C/ 148	SC 148.4.5.4	P 245	L 51	# r02-44	C/ 148	SC 148.4.	6.1	P 246	L 25	#	r02-45	
Law, Davi		Hewlett Packa			Law, David				ard Enterprise			
Comment		Comment Status A		State Diagrams	Comment 7		Comment S					ΕZ
Since clear	the precedence of c if the equation (local is perform the AND t	pperators isn't defined in 2 _nodeID = 0) * (curID => p hen the OR, or as I believe	olca_node_count	) + (curID = 255)	We do	h't normally u se 148. Sugo e used.	ise inverted comm gest that the inver					
Suaaeste	dRemedy				00		1, page 246, line 2	25				
Sugg	est that '(local_nodel	D = 0) * (curID => plca_no ) * ((curID => plca_node_o					"committed" varia		inged to read '	the com	mitted varia	able
Accor Resp ACCE Editor P245 "((loc: P202 (!fc_s "RSC P203 paren	EPT IN PRINCIPLE. modated by commen onse to comment r02 EPT IN PRINCIPLE. rial license to add pa L51: Change "(local al_nodeID = 0) * (cur , L33: Change "RSCI upported))" to D * ((RXn = ESD) + , L28-47: All the exit thesis: Add brackets		blca_node_count + (curID = 255)" != SSD) * (RXn ! SYNC) * (!fc_sup alf ("C" and "D") c	) + (curID = 255)" to = SYNC) * ported))) of Figure 147-8 need	Sugges state Subcla Sugges Subcla Sugges Subcla Sugges Subcla Sugges Subcla Sugges Subcla Sugges the INA full stop	st that ' ent .'. use 148.4.7. st that ' plca use 148.4.7. st that ' plca use 148.4.7. st that 'From use 148.4.7. st that ' ent ERESIS state st that ' as use 148.4.7. st that ' to " use 148.4.7. st that ' to "	"OK" and' be ch 1, page 252, line ' ACTIVE" state' 1, page 252, line ' INACTIVE" state, b, reporting plca_s of this sentence).	tate' be changed 10 12 ' be changed t 14 .' be changed t 15 S" state' be hanged to read 17 ' be changed t 19 , reporting plca tatus as FAIL.	to read ' plca_ to read ' plca_ to read 'From the changed to rea d ' as OK and o read ' to the a_status as "FA	status as status as ne ACTIV d ' ente '. ACTIVE	as FAIL.'. OK.'. E state'. ers the state'. anged to '	to
					Response		Response S	Status C				
					ACCEF	PT.						

AUGEL I.

Pa **246** Li **25** 

C/ 148 S											
	SC 148.4.6.2	P <b>247</b>	L <b>7</b>	# r02-46	Cl	148 SC	C 148		P <b>250</b>	L 1	# r02-34
aw, David		Hewlett Packa	ard Enterprise		Ber	uto, Piergior	gio		Canova Tech	n S.r.l.	
Comment Type	e E	Comment Status A			EZ Col	mment Type	Е	Comment S	Status A		
	ause title seer by an editor's	ns to have become detacher	d from the subcla	use number,		The PLCA Diagram in t			d be put into a	a dedicated subcl	lause, as for the sta
SuggestedRen	nedy				Sug	ggestedReme	ədy				
		ata variables' from before th 4.6.2 Variables'.	e editor's note bo	ox and change the				to its own subcla e 148-3 on pag		iagrams" 148.4.6	6.7.
Response		Response Status <b>C</b>			Re	sponse		Response S	tatus C		
ACCEPT.						ACCEPT.					
C/ 148 S	SC 148.4.6.3	P 248	L 16	# r02-47							
Law, David		Hewlett Packa	ard Enterprise								
Comment Type Missing cro	e E oss reference	Comment Status A			EZ						
suggestedRen											
Change 'O	therwise it ret	turns the value of the plca_tx value of the plca_txer variat									
Response		Response Status <b>C</b>									
Response ACCEPT.		Response Status C									
ACCEPT.	C 148.4.6.6	Response Status C	L <b>4</b>	# r02-67							
ACCEPT.			L <b>4</b>	# r02-67							
ACCEPT.	orgio		L <b>4</b>		Late						
ACCEPT. Cl <b>148</b> S Beruto, Piergio Comment Type The delay_	orgio e <b>T</b>	P 249 Comment Status A onstant should count nibbles									
ACCEPT. Cl 148 S Beruto, Piergio Comment Type The delay_ is used in t	orgio e <b>T</b> _line_length c the State Diag	P 249 Comment Status A onstant should count nibbles									
ACCEPT. Cl 148 S Beruto, Piergio Comment Type The delay_ is used in t SuggestedRem Change "T the PLCA I Value: up t to	ergio e <b>T</b> Line_length c the State Diag <i>nedy</i> his constant i RS variable d to 396 bit time	P 249 Comment Status A onstant should count nibbles grams. s implementation dependen elay line depicted in Figure o ss."	s instead of bits, a t and specifies th 148-2.	according to the wa e maximum length	y it of						
ACCEPT. Cl 148 S Beruto, Piergio Comment Type The delay_ is used in t SuggestedRem Change "T the PLCA I Value: up t to "This const	orgio P T Line_length c the State Diag nedy his constant i RS variable d to 396 bit time tant is implem LCA RS variab	P 249 Comment Status A onstant should count nibbles grams. s implementation dependen elay line depicted in Figure 4	s instead of bits, a t and specifies th 148-2.	according to the wa e maximum length	y it of						
Cl 148 S Beruto, Piergio Comment Type The delay_ is used in t SuggestedRem Change "T the PLCA I Value: up t to "This const that the PL	orgio P T Line_length c the State Diag nedy his constant i RS variable d to 396 bit time tant is implem LCA RS variab	P 249 Comment Status A onstant should count nibbles grams. s implementation dependen elay line depicted in Figure 4 ss."	s instead of bits, a t and specifies th 148-2.	according to the wa e maximum length	y it of						

Pa **250** Li **1** 

C/ 148	SC 148	P <b>250</b>	L 17	# r02-52	C/ 148	SC 14	8.4.6.6	P <b>250</b>	L 38	# r02-48
Beruto, Pi	ergiorgio	Canova Tech	S.r.I.		Law, David			Hewlett Pac	kard Enterprise	
Comment	Туре Т	Comment Status D		State Diagrams	Comment T	Гуре 1	R	Comment Status A		State Diagrams
TRAN its ow In this real d From difficu	ISMIT/FLUSH s n latency. case, the PLC ata to receive. a functional per lities during sys	a State Diagram is done sendir states), the CRS signal may stil A Data State Diagram enters th rspective, this is not an issue, b tem validation.	l be asserted by ne RECEIVE st	y the PHY because of ate, even if there is no	comple once th diagrar and as The pro	etion of a ne looped n enters t a result t	transmis back C he WAI he node	pears that node 0 ceases t ssion, the node 0 PLCA Da RS ends. At the same time T_TO state. After an IPG, 0 PLCA Data state diagra pe that when the node 0 Pl and tx_cmd is set to BEA0	ata state diagram e, the node 0 PL0 the plca_txen for am entering the F _CA Control state	n enters the IDLE state CA Control state r node 0 is then asserted IOLD. e diagram enters the
00	dRemedy							N. This is because TX_ER		
[1] ad		e following: ox called "WAIT_CRS" with the	e following conte	ent: "	0000 ir	the HOL	D state.	As a result, the curlD courses stations don't get thei	inters in the othe	r stations don't get set
	S THEN RRIER STATU	S <= CARRIER_ON			Suggested	Remedy				
ELSE		S <= CARRIER_OFF						4 PLCA Data state diagrar t to BEACON.	n to send a BEA	CON while in the HOLD
END					Response			Response Status C		
TXD <	R <= ENCODE_ <= ENCODE_TX N <= FALSE	_TXER(tx_cmd) XD(tx_cmd)			Accom	PT IN PR modated tion of co	by reso	lution of comment #33.		
instea [3] Ad condit	nd of the IDLE s Id a transition fr	om the WAIT_CRS state to the		_	Implem Propos instruc	ed Respo tions belo	ges sho onse.pdf w and tl	E. wn in http://www.ieee802. f with editorial license to re he figures in the reference g., r02-01 and r02-24).)	solve differences	s between the written
"	,	,				igure 148 R <= plca		e HOLD state, replace "		
[4] Ad	ld a recirculating	g arc to the WAIT_CRS state w	vith "ELSE" as a	condition		<= 0000	LIXEI			
• •	Response	Response Status Z			"					
REJE								ΓXER(tx_cmd_sync) D(tx_cmd_sync)		
This c	comment was W	ITHDRAWN by the commenter	er.		"	= 21100	02_174			
					TX_E TXD • " with " TX_E	R <= plca <= 0000 R <= EN0	_txer	e ABORT state, replace " TXER(tx_cmd_sync) D(tx_cmd_sync)		

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Pa **250** Li **38**  Page 23 of 30 8/15/2019 2:50:50 PM

3] In Figure 148-4, in both the COLLIDE and DELAY_PENDING states add the following: " TX_ER <= ENCODE_TXER(tx_cmd_sync) TXD <= ENCODE_TXD(tx_cmd_sync)	[13] Change the condition on the open-ended transition to NORMA 148–4—PLCA Data state diagram" from " plca_reset + (!plca_en) * (!plca_status)	L of "Figure
	to "	
4] In Figure 148-4, add a recirculating arc with an "ELSE" condition to the following state oxes: WAIT_MAC, PENDING, DELAY_PENDING, COLLIDE and ABORT.	to " _plca_reset + (!plca_en) + (plca_status != OK) "	
5] In Figure 148-4, in the transition from WAIT_MAC to TRANSMIT state, change the ondition from "plca_txen" to "MCD * plca_txen"	[14] Change the condition on the NORMAL->IDLE transition of "Fig state diagram" from "	ure 148–4—PLCA Data
	plca_en * (!plca_reset) * plca_status	
At page 244 in Figure 148-3, in the transition from the RESYNC state to the	"	
ND_BEACON state change the condition from: "	to "	
local_nodeID = 0	"plca_en * (!plca_reset) * (plca_status = OK)	
to: "		
MCD * (local_nodelD = 0)	[15] Update the PLCA Control state diagram as follows:	
	1. Within the EARLY_RECEIVE state, add the action "start beac	on det timer".
	2. Create a transition from the EARLY_RECEIVE state to a conn	
Add subclause "148.4.5.5 Abbreviations" with the following content: "	following exit condition:	
MCD See 148.4.6.5	(local_nodeID != 0) * (!receiving) *	
	((rx_cmd = BEACON) + ((!CRS) * beacon_det_timer_not_don	e))
	<ol> <li>Change the exit transition from EARLY_RECEIVE to connector</li> </ol>	
At page 244 in Figure 148-3, in the transition from the RECOVER state to the	(local_nodeID != 0) * ((rx_cmd = BEACON) + recv_timer_don	
ND_BEACON state change the condition from: "	to:	, ( · · · · · · · · · · · · · · · · · ·
(!CRS) * recv_beacon_timer_done	(local_nodeID != 0) * recv_timer_done * (!receiving)	
	4. Delete the transition from RESYNC to SYNCING including its	exit condition.
): "	5. Add a connector, D, with arrow to SYNCING.	
MCD * (!CRS) * recv_beacon_timer_done	6. Within the SYNCING state, add the action:	
	IF (local_nodeID != 0) * (rx_cmd != BEACON) THEN	
At page 248, line 8 remove the duplicate MCD declaration (the correct definition is at	start invalid_beacon_timer	
50 in the Abbreviations section).	END	
,	7. For the SYNCING exit condition to connector A, replace the co	andition from:
At page 248, line 34 change "A continuous free-running timer that shall expire	rx_cmd != BEACON	
chronously with the rising edge of TX_TCLK."	to:	
n "A continuous free-running timer that shall expire synchronously with the rising edge of	!CRS	
MII TX_CLK"	8. Add an open arrow global transition to RESYNC with the cond	tion
	"invalid_beacon_timer_done".	
Add the following variable definition in 148.4.6.2: "	9. Add an exit transition from RESYNC to new connector, E, with	the condition
cmd_sync	"(local_nodeID != 0) * (CRS)"	
he value of the tx_cmd variable sampled on the falling edge of the MII TX_CLK.	10. Add a connector, E, with arrow to EARLY_RECEIVE.	
alues: see tx_cmd in 148.4.5.2	<ol> <li>Change the exit condition from EARLY_RECEIVE to RECEIV (!recv_timer_done) * receiving</li> </ol>	E from:
	to:	
1] In Figure 148-4, replace all occurrences of "ENCODE_TXD(tx_cmd)" with NCODE_TXD(tx_cmd_sync)"	recv_timer_not_done * receiving	
	[16] In section 148.4.5.4, page 242 Line 46 (before burst_timer) ad	d the following timers:
<ol><li>In Figure 148-4, replace all occurrences of "ENCODE_TXER(tx_cmd)" with</li></ol>	,	-
NCODE_TXER(tx_cmd_sync)"	beacon_det_timer Timer for detecting received BEACONs.	
: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/ger	neral Pa 250	Page 24 of 30

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

-			-		
Duration: 22 bit times. Tolerance: +/- 1 bit time.	C/ 148	SC 148	P <b>250</b>	L 38	# r02-33
	Beruto, Pi	ergiorgio	Canova 1	ech S.r.I.	
invalid_beacon_timer Timer used for BEACON validation. This timer is stopped any time rx_cmd = BEACON.	Comment	0 0	Comment Status A		State Diagrams
Duration: 4000 ns			State Diagram is in the H		
Tolerance: +/- 400 ns			e to send a BEACON. At the XD is forced to 0000 in the		
[17] In clause 30.16.1.1.5 "aPLCATransmitOpportunityTimer", Page 43, Line 15, Change "The default value is 24." to "The default value is 32."	This is The in	a regression c tention of comr	aused by the resolution of nent i-373 was to align wit cually changing the behavi	comment i-373 on E h the IEEE State Dia	03.0. Igram rules and
18] Change equation 148-2 on Page 243, Line 17, from " to_timer > 2 x max(t <propdelay>) +</propdelay>			dy to this comment is to re ulfilling i-373 original inten		keeping current
max(TX_EN sampled to MDI output) + max(MDI input to CRS asserted) +	Suggested	Remedy			
max(MDI input to CRS asserted) + max(MDI input to CRS deasserted) –	In Fig	ire 148-4, in the	e HOLD state, replace "		
min(MDI input to CRS deasserted)		R <= plca_txer			
u 1- u	1XD <	= 0000			
to " to_timer > 2 x max(t <propdelay>) +</propdelay>	with "				
max(TX_EN sampled to MDI output) +		_txer THEN			
max(MDI input to CRS asserted) +	_	ER <= TRUE			
max(MDI input to CRS deasserted) –	ELSE	<= 0000			
min(MDI input to CRS deasserted) + max(MII propagation delay)	-	ER <= ENCOD	E_TXER(tx_cmd)		
"		<= ENCODE_	TXD(tx_cmd)		
	END "				
[19] Delete lines 10 through 20 of page 240. This removes the text beginning with "After syncing is done" through " appearing at the MDI to CRS asserted."	Pooponoo				
	Response		Response Status C		
[20] Make changes in Table 147-6 on page 224 in the following order:		PT IN PRINCIE	hown in http://www.ieee80	)2 ora/3/ca/public/Au	a2019/r02-33
1. Remove row with Event "TX_EN sampled to CRS asserted"		0	pdf with editorial license to	0 01	0
<ol> <li>Remove row with Event "TX_EN sampled to CRS deasserted"</li> <li>Change all occurances of "TX_EN" to "TX_EN / TX_ER"</li> </ol>			d the figures in the referen	ced file at the url, an	d combine with other
4. Change all occurances of "RX_DV" to "RX_DV / RX_ER"	comm	ent responses	(e.g., r02-01 and r02-24).)		
• <u> </u>	[1] In	- iaure 148-4. in	the HOLD state, replace	,	
	• •	R <= plca_txe	· ·		
	TXD	<= 0000			
	with "				
		R <= ENCODE	E_TXER(tx_cmd_sync)		
	TXD	<= ENCODE_1	TXD(tx_cmd_sync)		
	"				
	[2] In	- igure 148-4, in	the ABORT state, replace	e "	
	TX_E	R <= plca_txer			
	_TXD	<= 0000			
	with "				
E: TR/technical required ER/editorial required GR/general required T/technical E/editorial C MENT STATUS: D/dispatched A/accented R/rejected RESPONSE STATUS: O/open W/				a 250 38	Page 25 of 30 8/15/2019 2:50

TYPI COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Li **38** 

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E: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/ge IMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/writ		Page 26 of 30
11] In Figure 148-4, replace all occurrences of "ENCODE_TXD(tx_cmd)" with ENCODE_TXD(tx_cmd_sync)"	recv_timer_not_done * receiving	
Values: see tx_cmd in 148.4.5.2	<ol> <li>Change the exit condition from EARLY_RECEIVE to RECEIVI (!recv_timer_done) * receiving to:</li> </ol>	= trom:
The value of the tx_cmd variable sampled on the falling edge of the MII TX_CLK.	10. Add a connector, E, with arrow to EARLY_RECEIVE.	- (
x_cmd_sync	"(local_nodelD != 0) * (CRS)"	
10] Add the following variable definition in 148.4.6.2: "	9. Add an exit transition from RESYNC to new connector, E, with	the condition
_	"invalid_beacon_timer_done".	
he MII TX_CLK"	8. Add an open arrow global transition to RESYNC with the condi	tion
vith "A continuous free-running timer that shall expire synchronously with the rising edge of	ICRS	
9] At page 248, line 34 change "A continuous free-running timer that shall expire synchronously with the rising edge of TX_TCLK."	rx_cmd != BEACON to:	
D1 At page 248, line 34 change "A continuous free running timer that chall evoire	<ol> <li>For the SYNCING exit condition to connector A, replace the co rx_cmd != BEACON</li> </ol>	naition from:
ine 50 in the Abbreviations section).	END 7. For the SYNCING suit condition to consister A replace the co	
8] At page 248, line 8 remove the duplicate MCD declaration (the correct definition is at	start invalid_beacon_timer	
n	IF (local_nodeID != 0) * (rx_cmd != BEACON) THEN	
MCD * (!CRS) * recv_beacon_timer_done	6. Within the SYNCING state, add the action:	
to: "	5. Add a connector, D, with arrow to SYNCING.	
(,	<ol> <li>Delete the transition from RESYNC to SYNCING including its e</li> </ol>	exit condition.
(!CRS) * recv_beacon_timer_done	(local_nodeID != 0) * recv_timer_done * (!receiving)	
SEND_BEACON state change the condition from: "	(local_hodelD != 0) ((IX_chid = BEACON) + recv_limer_done to:	
7] At page 244 in Figure 148-3, in the transition from the RECOVER state to the	<ol> <li>Change the exit transition from EARLY_RECEIVE to connector (local_nodeID != 0) * ((rx_cmd = BEACON) + recv_timer_done</li> </ol>	
	((rx_cmd = BEACON) + ((!CRS) * beacon_det_timer_not_dom Change the exit transition from EAPLY_RECEIVE to connector	
MCD See 148.4.6.5	(local_nodeID != 0) * (!receiving) *	2))
Add subclause "148.4.5.5 Abbreviations" with the following content: "	following exit condition:	
	2. Create a transition from the EARLY_RECEIVE state to a conne	ector, D, with the
· · · · · ·	1. Within the EARLY_RECEIVE state, add the action "start beaco	
MCD * (local_nodeID = 0)	[15] Update the PLCA Control state diagram as follows:	
to: "		
	"	
local nodeID = 0	plca_en * (!plca_reset) * (plca_status = OK)	
6] At page 244 in Figure 148-3, in the transition from the RESYNC state to the SEND_BEACON state change the condition from: "	to "	
CI At some Odd in Figure 440.0 in the transition from the DEOV/NO state (s) the	_plca_en * (!plca_reset) * plca_status	
condition from "plca_txen" to "MCD * plca_txen"	state diagram" from "	
5] In Figure 148-4, in the transition from WAIT_MAC to TRANSMIT state, change the	[14] Change the condition on the NORMAL->IDLE transition of "Fig	ure 148–4—PLCA Data
boxes: WAIT_MAC, PENDING, DELAY_PENDING, COLLIDE and ABORT.	ű	
4] In Figure 148-4, add a recirculating arc with an "ELSE" condition to the following state	plca_reset + (!plca_en) + (plca_status != OK)	
	" to "	
TXD <= ENCODE_TXD(tx_cmd_sync)	_plca_reset + (!plca_en) * (!plca_status)	
TX_ER <= ENCODE_TXER(tx_cmd_sync)	148–4—PLCA Data state diagram" from "	0
3] In Figure 148-4, in both the COLLIDE and DELAY_PENDING states add the following: "	[13] Change the condition on the open-ended transition to NORMAI	_ of "Figure
	"ENCODE_TXER(tx_cmd_sync)"	
TXD <= ENCODE_TXD(tx_cmd_sync)	[12] In Figure 148-4, replace all occurrences of "ENCODE_TXER(t)	_cmd)" with

[16] In section 148.4.5.4, page 242 Line 46 (before burst_timer) add the following timers:	C/ 148	SC 148.4.6.6	6 P 2	50	L 41	#	r02-49	
beacon_det_timer	Law, David		Hewl	ett Packar	d Enterprise			
Timer for detecting received BEACONs.	Comment 1	ype T	Comment Status					MII
Duration: 22 bit times.						ъ. тv г	N and	
Tolerance: +/- 1 bit time. invalid_beacon_timer	TX_ER be the	defines TX_EI	Table 22-1 'Permissib N = 0, TX_ER = 1 and inted on the MII if the	d TXD = 0 Figure 14	000 as Reserve 8-4 'PLCA Data	ed. This	however v	
Timer used for BEACON validation. This timer is stopped any time rx_cmd = BEACON.	the HO	LD or ABORT s	states and plca_txer i	s asserted	1.			
Duration: 4000 ns	Suggested	Remedy						
Tolerance: +/- 400 ns		the actions in ca_txer is asse	the HOLD or ABORT	states to	issue a defined	d encodir	ng on the	MII
[17] In clause 30.16.1.1.5 "aPLCATransmitOpportunityTimer", Page 43, Line 15, Change "The default value is 24." to "The default value is 32."	Response			<u>^</u>				
			Response Status	C				
[18] Change equation 148-2 on Page 243, Line 17, from " to_timer > 2 x max(t <propdelay>) +</propdelay>		T IN PRINCIPI modated by res	LE. solution of comment #	±33.				
max(TX_EN sampled to MDI output) +	Deerly		400 :					
max(MDI input to CRS asserted) +		ion of commen						
max(MDI input to CRS deasserted) –		-	∟∟. nown in http://www.ie		/2/ca/public/Au	a2010/r0	2 22	
min(MDI input to CRS deasserted)			of with editorial licens					'n
a a a a a a a a a a a a a a a a a a a			the figures in the ref					
to "			e.g., r02-01 and r02-2					
to_timer > 2 x max(t <propdelay>) +</propdelay>			o.g., .o_ o. aao	• • • • • • •				
max(TX_EN sampled to MDI output) +	[1] In F	gure 148-4, in	the HOLD state, repla	ace "				
max(MDI input to CRS asserted) +	TX_E	R <= plca_txer						
max(MDI input to CRS deasserted) – min(MDI input to CRS deasserted) +	TXD <	= 0000						
max(MII propagation delay)	"							
"	with "							
			_TXER(tx_cmd_sync	)				
[19] Delete lines 10 through 20 of page 240. This removes the text beginning with "After syncing is done" through " appearing at the MDI to CRS asserted."	"   XD <	= ENCODE_1	XD(tx_cmd_sync)					
1001 Malassia and in Table 447. One many 204 in the faller in random	[2] In F	gure 148-4, in	the ABORT state, rep	lace "				
[20] Make changes in Table 147-6 on page 224 in the following order: 1. Remove row with Event "TX_EN sampled to CRS asserted"	TX_E	R <= plca_txer						
2. Remove row with Event "TX_EN sampled to CRS asserted"	TXD <	= 0000						
3. Change all occurances of "TX_EN" to "TX_EN / TX_ER"	"							
4. Change all occurances of "RX_DV" to "RX_DV / RX_ER"	with "			、 、				
			_TXER(tx_cmd_sync	)				
	"	= ENCODE_1	XD(tx_cmd_sync)					
			both the COLLIDE ar		_PENDING sta	ates add	the followi	ing: "
			_TXER(tx_cmd_sync XD(tx_cmd_sync)	)				
			d a recirculating arc v ENDING, DELAY_PE				llowing sta	ate
E: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/ge			7/	Pa 250	1		Page 27 o	
//MENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/writt RT ORDER: Page, Line	en C/Closed	U/unsatisfied	∠/witndrawn	Li <b>41</b>		8	3/15/2019	2:50:5

[5] In Figure 148-4, in the transition from WAIT\_MAC to TRANSMIT state, change the condition from "plca\_txen" to "MCD \* plca\_txen"

[6] At page 244 in Figure 148-3, in the transition from the RESYNC state to the SEND BEACON state change the condition from: "

local\_nodeID = 0

to: " MCD \* (local\_nodeID = 0)

Add subclause "148.4.5.5 Abbreviations" with the following content: ' MCD \$See 148.4.6.5\$

[7] At page 244 in Figure 148-3, in the transition from the RECOVER state to the SEND\_BEACON state change the condition from: "

```
(!CRS) * recv_beacon_timer_done
"
to: "
MCD * (!CRS) * recv_beacon_timer_done
```

[8] At page 248, line 8 remove the duplicate MCD declaration (the co

[8] At page 248, line 8 remove the duplicate MCD declaration (the correct definition is at line 50 in the Abbreviations section).

[9] At page 248, line 34 change "A continuous free-running timer that shall expire synchronously with the rising edge of TX\_TCLK."

with "A continuous free-running timer that shall expire synchronously with the rising edge of the MII TX\_CLK"  $\,$ 

[10] Add the following variable definition in 148.4.6.2: "

tx\_cmd\_sync

The value of the tx\_cmd variable sampled on the falling edge of the MII TX\_CLK. Values: see tx\_cmd in 148.4.5.2

[11] In Figure 148-4, replace all occurrences of "ENCODE\_TXD(tx\_cmd)" with "ENCODE\_TXD(tx\_cmd\_sync)"

[12] In Figure 148-4, replace all occurrences of "ENCODE\_TXER(tx\_cmd)" with "ENCODE\_TXER(tx\_cmd\_sync)"

[13] Change the condition on the open-ended transition to NORMAL of "Figure 148–4—PLCA Data state diagram" from " plca\_reset + (!plca\_en) \* (!plca\_status)

to "

plca\_reset + (!plca\_en) + (plca\_status != OK)

[14] Change the condition on the NORMAL->IDLE transition of "Figure 148-4--PLCA Data state diagram" from " plca\_en \* (!plca\_reset) \* plca\_status

to '

plca\_en \* (!plca\_reset) \* (plca\_status = OK)

[15] Update the PLCA Control state diagram as follows:

1. Within the EARLY\_RECEIVE state, add the action "start beacon\_det\_timer".

2. Create a transition from the EARLY\_RECEIVE state to a connector,  $\overline{D}$ , with the following exit condition:

(local\_nodeID != 0) \* (!receiving) \* ((rx\_cmd = BEACON) + ((!CRS) \* beacon\_det\_timer\_not\_done))

 Change the exit transition from EARLY\_RECEIVE to connector B from: (local\_nodeID != 0) \* ((rx\_cmd = BEACON) + recv\_timer\_done) \* (!receiving) to:

(local\_nodeID != 0) \* recv\_timer\_done \* (!receiving)

- 4. Delete the transition from RESYNC to SYNCING including its exit condition.
- 5. Add a connector, D, with arrow to SYNCING.
- Within the SYNCING state, add the action: IF (local\_nodeID != 0) \* (rx\_cmd != BEACON) THEN start invalid beacon timer
- FND
- For the SYNCING exit condition to connector A, replace the condition from: rx\_cmd != BEACON

to: !CRS

8. Add an open arrow global transition to RESYNC with the condition "invalid\_beacon\_timer\_done".

9. Add an exit transition from RESYNC to new connector, E, with the condition "(local\_nodeID != 0) \* (CRS)"

10. Add a connector, E, with arrow to EARLY\_RECEIVE.

11. Change the exit condition from EARLY\_RECEIVE to RECEIVE from: (!recv\_timer\_done) \* receiving

to: recv\_timer\_not\_done \* receiving

[16] In section 148.4.5.4, page 242 Line 46 (before burst\_timer) add the following timers:

beacon\_det\_timer Timer for detecting received BEACONs. Duration: 22 bit times. Tolerance: +/- 1 bit time.

invalid\_beacon\_timer Timer used for BEACON validation. This timer is stopped any time rx\_cmd = BEACON. Duration: 4000 ns Tolerance: +/- 400 ns

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TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Page, Line

Page 28 of 30 8/15/2019 2:50:50 PM [17] In clause 30.16.1.1.5 "aPLCATransmitOpportunityTimer", Page 43, Line 15, Change "The default value is 24." to "The default value is 32."

```
[18] Change equation 148-2 on Page 243, Line 17, from "
to_timer > 2 x max(t<propdelay>) +
max(TX_EN sampled to MDI output) +
max(MDI input to CRS asserted) +
max(MDI input to CRS deasserted) –
min(MDI input to CRS deasserted)
```

to "

```
to_timer > 2 x max(t<propdelay>) +
max(TX_EN sampled to MDI output) +
max(MDI input to CRS asserted) +
max(MDI input to CRS deasserted) –
min(MDI input to CRS deasserted) +
max(MII propagation delay)
```

[19] Delete lines 10 through 20 of page 240. This removes the text beginning with "After syncing is done ..." through "... appearing at the MDI to CRS asserted."

[20] Make changes in Table 147-6 on page 224 in the following order:

- 1. Remove row with Event "TX\_EN sampled to CRS asserted"
- 2. Remove row with Event "TX\_EN sampled to CRS deasserted"

3. Change all occurances of "TX\_EN" to "TX\_EN / TX\_ER"

4. Change all occurances of "RX\_DV" to "RX\_DV / RX\_ER"

	SC 148.4	P <b>2</b>	50	L <b>42</b>	# r02-24
Koczwara,	Wojciech	Rock	well Automa	ation	
Comment	Туре Т	Comment Status	Α		State Diagram
1. HOI recv_t 2. HOI comm	LD can exit eithe imer_not_done LD can exit eithe itted *!receiving	*MCD * !commited * !	IDE when ( receiving) OLLIDE wh ie).	(a == delay_li ien (a == dela	ne_length * plca_txer * ay_line_length * MCD *
Suggested	dRemedv	. –	. ,		
1. Cha receivi	ange the transitio	on condition from HOI y_line_length)], to [!pl			_timer_done + one + receiving + (a >=
(!recei	iving) * recv_tim	on condition from HOI er_not_done], to [!plc * (a < delay_line_leng	a_txer * MC		
		on condition from HOI * MCD * (!commited)			e: from )], to [plca_txer * MCD]
Response		Response Status	С		
1. Cha receiv		on condition from HOI y_line_length)], to [(!p			
(!recei	iving) * recv_tim	on condition from HOI er_not_done], to [(!plo * (a < delay_line_leng	ca_txer) * N		
		on condition from HOI * MCD * (!commited)			e: from )], to [plca_txer * MCD]
C/ 148	SC 148.4.6.	6 P 2	50	L <b>48</b>	# r02-50
	b	Hewle	ett Packard	Enterprise	
Law, David					
	rrow seems to h	Comment Status ave become detached		·	a state on another
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Comment The ar page I Suggested	rrow seems to h abelled 'B'. <i>Remedy</i> anect the arrow w	ave become detached	d from the c	·	-

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C/ 148	SC 148.4.6.6	P <b>251</b>	L <b>32</b>	# r02-51	
Law, David		Hewlett	Packard Enterprise		
Comment T	<i>уре</i> <b>т</b>	Comment Status A		State Diag	rams
The sub be defir		-a used in relation to p	lca_txd <sub>n-a&lt;</sub>	:/SUB> doesn't seem t	0
Suggested	Remedy				
mii_clo	ck_timer expiration	he 'n-a' subscript indica ons before the most re definition in subclause	cent one." be adde	,	
Response		Response Status C			
148.4.6 The add convey	.2: dition of a subscr ed 'a' mii_clock_t	ipt 'n-a', i.e., plca_txd<	sub>n-a<\sub>, ind the most recent o	ble definition in subclau dicates the plca_txd one." be added to the e	
C/ 146	SC 146.7.1.3	P 116	9 L <b>30</b>	# <u>r</u> 02-16	
Schicketanz	z, Dieter	Universi	ty of Applied Scien	ce Reutlingen	
could n	0 there is a refer ot be found in the	Comment Status R rence to equation (80- e document. written NVP without ex	I) in green. The refe	Link Seg erence	men
Suggested					
It is rec from: ment le to:	omended to fix th ngth of 1589 m g	nis editorially by chang given in Table 146B-1 u	using Equation (80-	1) with an 'n' of 0.6 ocity of propagation of	0.6
Response		Response Status C	0	only of propagation of	0.0.
REJEC The CR Equatio	G disagrees with n 80-1 is in gree	n the commenter. n and not in the draft b	ecause it is an exte	ernal cross reference to edium. This is the way	

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