

Resolution to comments 117 and 118

Delete 147.3.7 and subclauses 147.3.7.1 and 147.3.7.2

Move the content of 147.3.7.1 and 147.3.7.2 to PCS receive as 147.3.3.9 and 147.3.3.10 as follows:

Insert new subclauses immediately prior to 147.3.4 (after 147.3.3.8)

147.3.3.9 Generation of BEACON indication

The PHY shall notify the RS of a received BEACON indication (see Clause 148) by the means of MII interface as specified in 22.2.2.8.

When a sequence of at least two consecutive 'N' symbols is received, the MII signals RX_DV, RX_ER, and RXD<3:0> shall be set to the BEACON indication as shown in Table 22–2, overriding the current state. Override shall cease as soon as the currently received symbol is anything other than an 'N' code.

147.3.3.10 Generation of COMMIT indication

The PHY shall notify the RS of a received COMMIT indication (see Clause 148) by the means of MII interface as specified in 22.2.2.8.

When a sequence of at least two consecutive 'J' symbols is received, the MII signals RX_DV, RX_ER, and RXD<3:0> shall be set to the COMMIT indication as shown in Table 22–2, overriding the current state. Override shall cease as soon as the currently received symbol is anything other than a 'J' code.

AND Delete PLCA support bit in the 10BASE-T1S PCS status register by deleting subclause 45.2.3.68d.1 and making the row for bit 3.2292.15 in Table 45-237d Reserved.