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*IEEE802.3cg TF
PSD mask and updated EMC simulations
November 22th, 2017*

Channel Characteristics

- Channel defined as in

[http://www.ieee802.org/3/cg/public/Sept2017/DiBiaso
Bergner_01c_0917.pdf](http://www.ieee802.org/3/cg/public/Sept2017/DiBiaso_Bergner_01c_0917.pdf) slide #18

$$\begin{aligned} \text{Return Loss } (f) & > 14 \text{ dB} && \text{for } f \text{ (0.3MHz } \rightarrow 10\text{MHz)} \\ & > 14 - 10 \cdot \log_{10}(f/10) \text{ dB} && \text{for } f \text{ (10MHz } \rightarrow 40\text{MHz)} \end{aligned}$$

$$\begin{aligned} \text{Insertion Loss } (f) & < 1.0 + 1.6 \cdot (f-1)/9 \text{ dB} && \text{for } f \text{ (0.3MHz } \rightarrow 10\text{MHz)} \\ & < 2.6 + 2.3 \cdot (f-10)/23 \text{ dB} && \text{for } f \text{ (10MHz } \rightarrow 33\text{MHz)} \\ & < 4.9 + 2.3 \cdot (f-33)/33 \text{ dB} && \text{for } f \text{ (33MHz } \rightarrow 40\text{MHz)} \end{aligned}$$

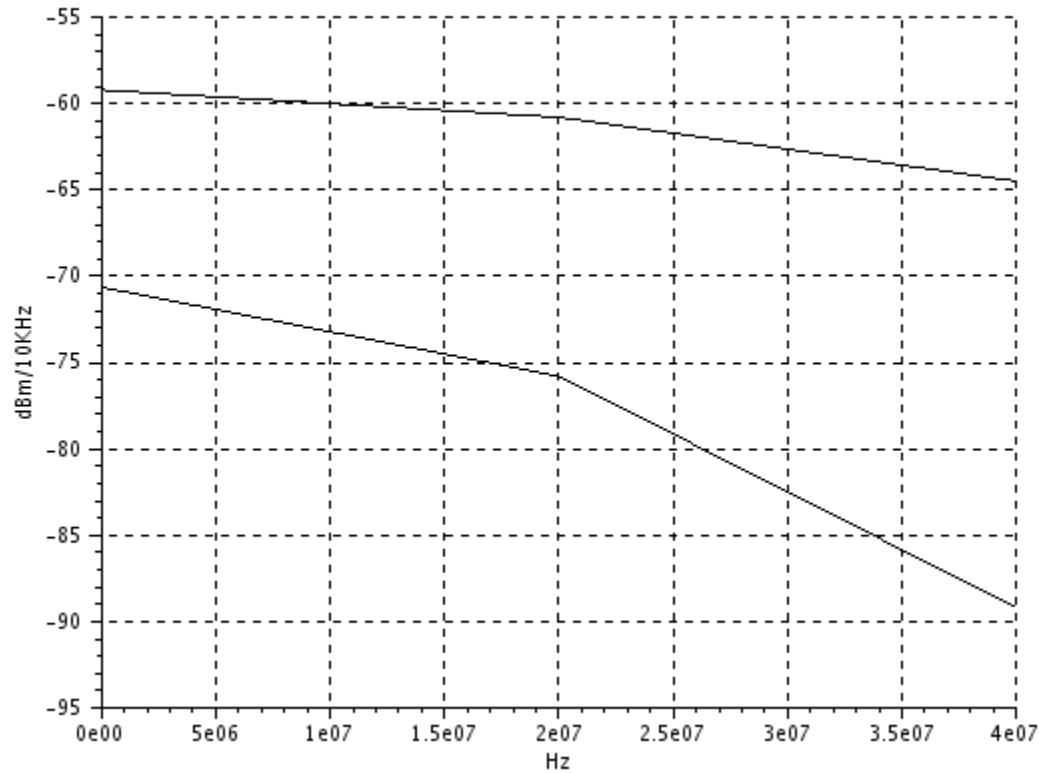
$$\begin{aligned} \text{Mode Conversion Loss } (f) & > 30 \text{ dB} && \text{for } f \text{ (0.3MHz } \rightarrow 20\text{MHz)} \\ & > 30 - 20 \cdot \log_{10}(f/20) \text{ dB} && \text{for } f \text{ (20MHz } \rightarrow 200\text{MHz)} \end{aligned}$$

Proposal for PSD mask

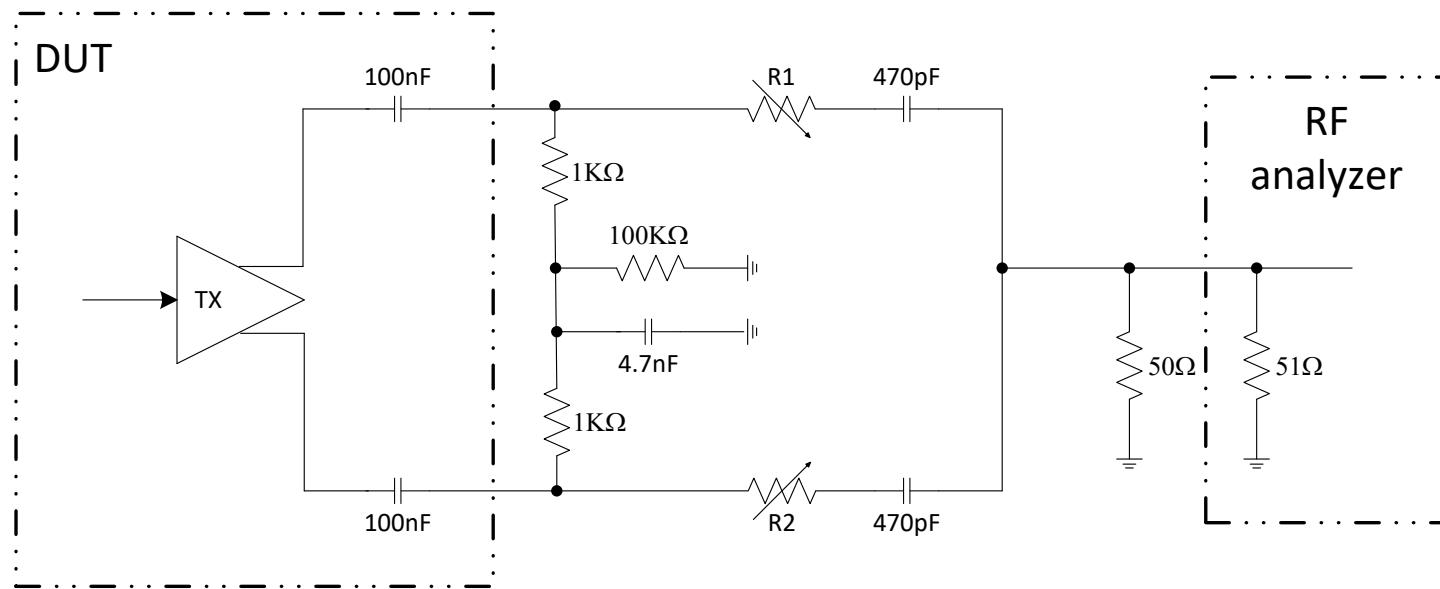
$$Upper\ PSD(f) = \begin{cases} -59.3 - 1.5 * \frac{f - 1}{19} & 300\text{KHz} < f < 20\ \text{MHz} \\ -60.8 - 3.7 * \frac{f - 20}{20} & 20\ \text{MHz} < f < 40\ \text{MHz} \\ -64.5 - 8.0 * \frac{f - 40}{17} & f > 40\ \text{MHz} \end{cases}$$

$$Lower\ PSD(f) = \begin{cases} -70.9 - 4.9 * \frac{f - 1}{19} & 300\text{KHz} < f < 20\ \text{MHz} \\ -75.8 - 13.4 * \frac{f - 20}{20} & f > 20\ \text{MHz} \end{cases}$$

Proposal for PSD mask



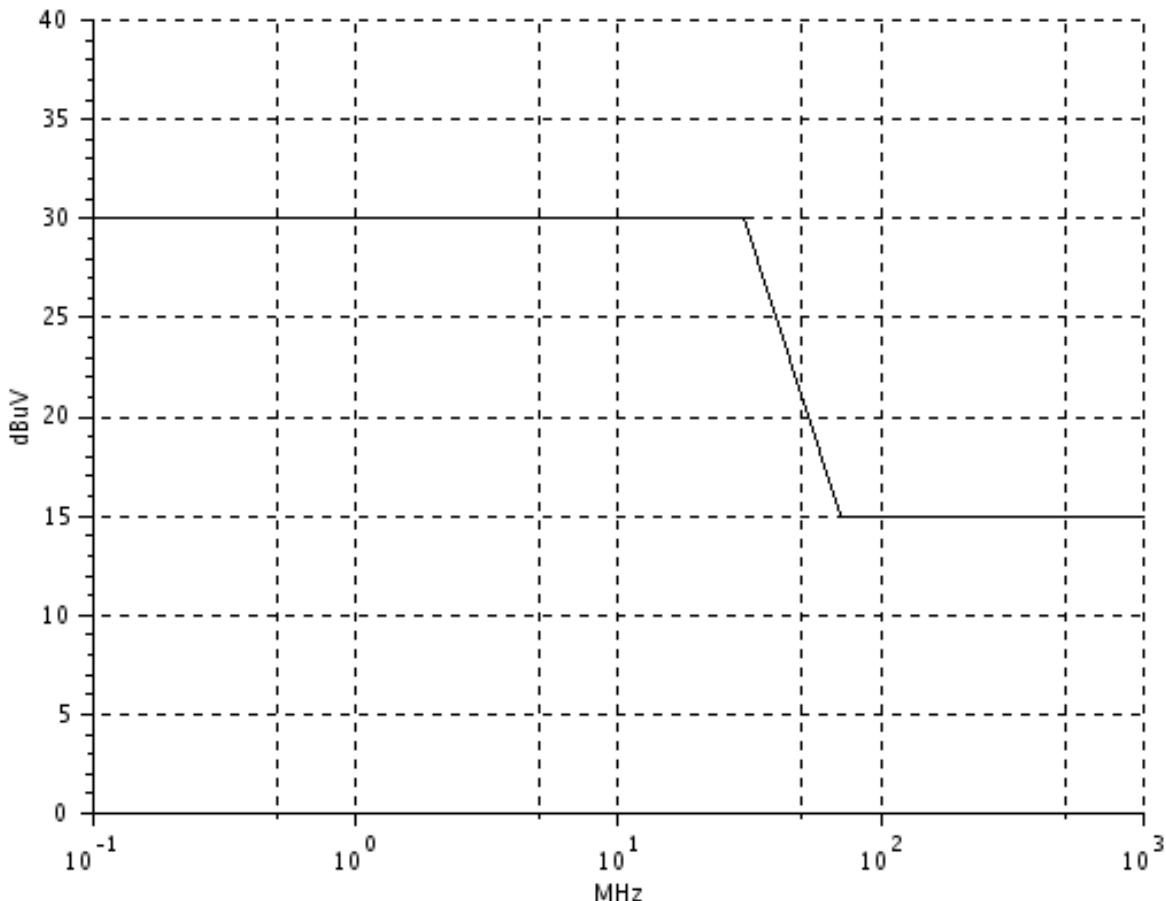
Simulations: EMC tests



- Direct Power Injection (DPI) and 150 Ohm emission tests for noise immunity and emission may be used to establish a baseline for PHY EMC performance

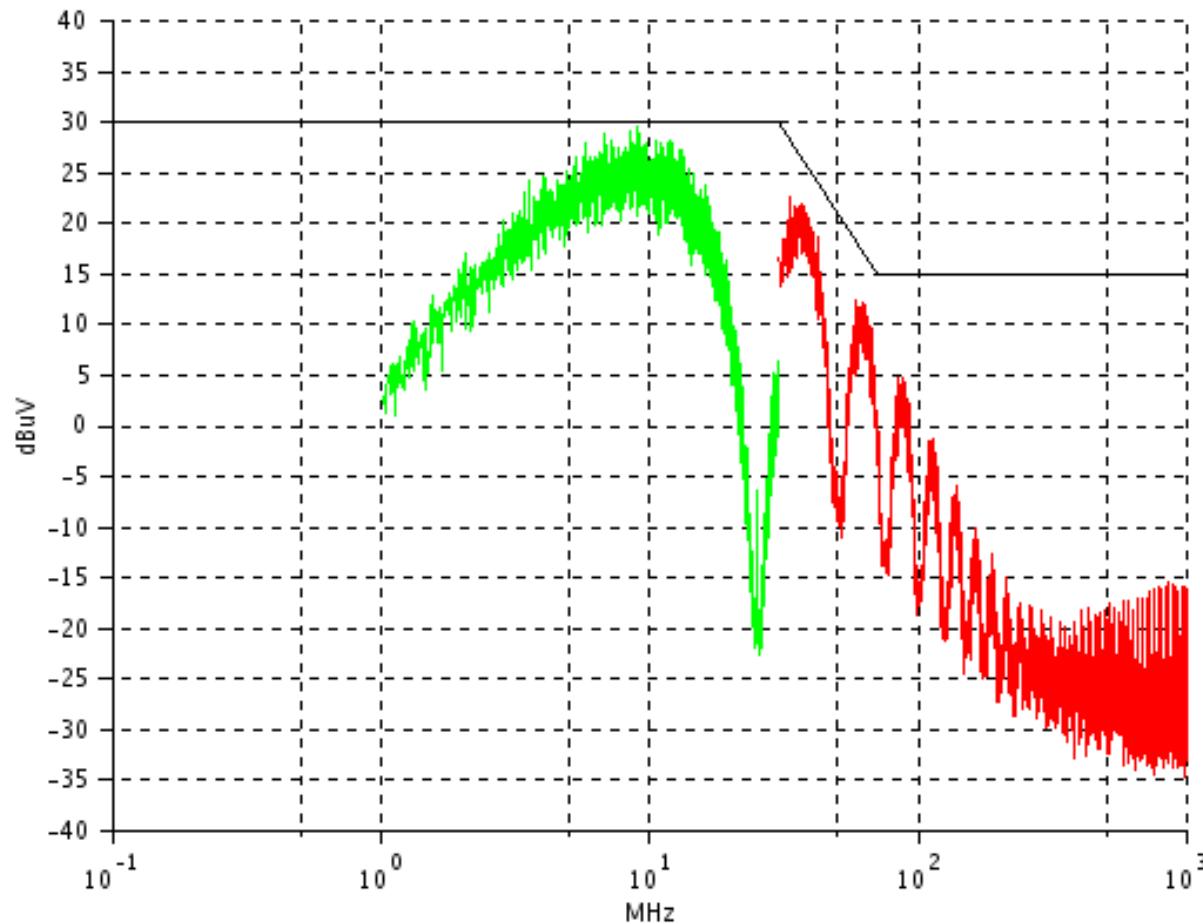
Parameter coupling	R1 [Ω] (MDI P)	R2 [Ω] (MDI N)
Symmetry	120	120
+ 2.5 % unbalance	121	118
- 2.5 % unbalance	118	121
+ 5.0 % unbalance	121	115
- 5.0 % unbalance	115	121

EMC Requirements

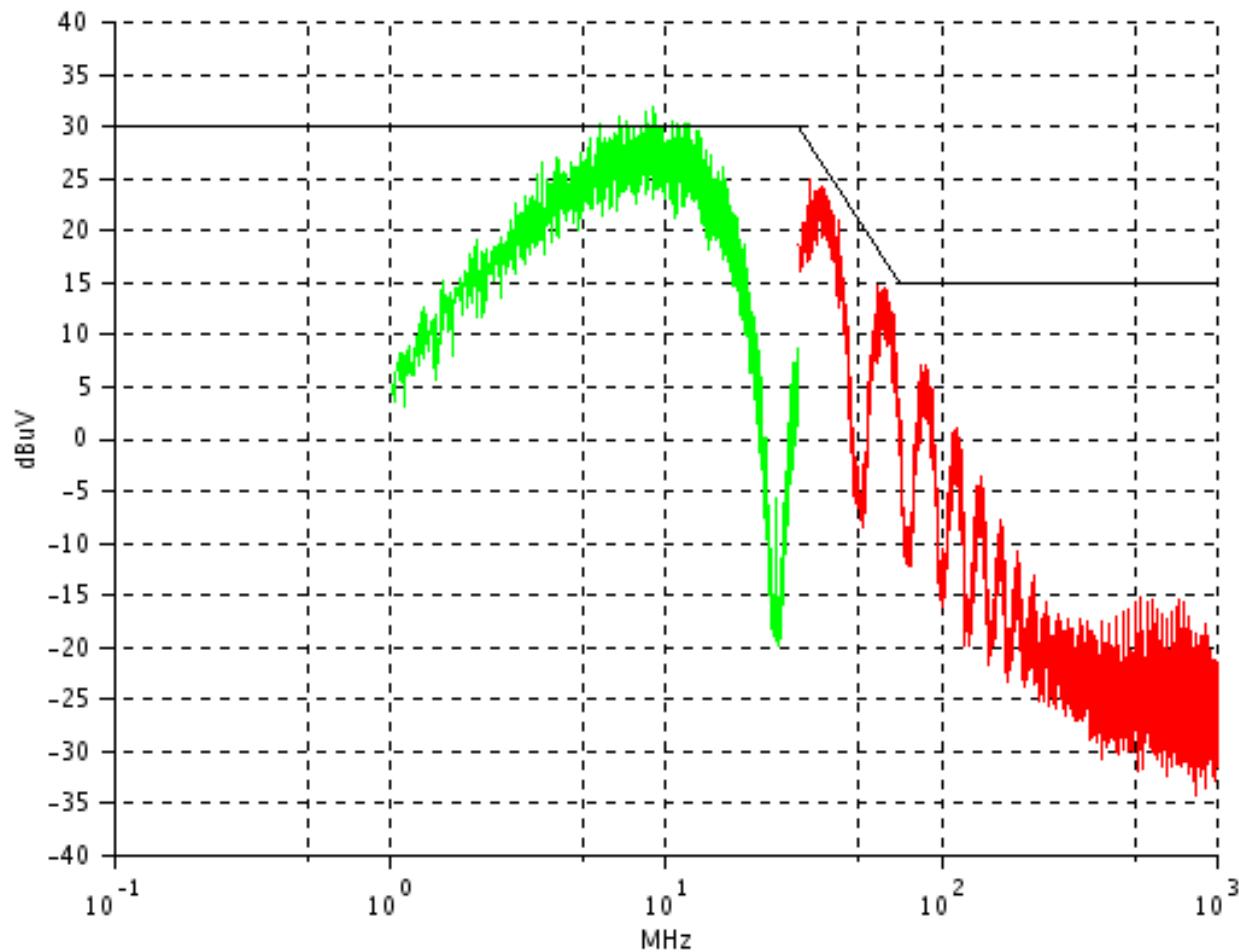


$f < 30\text{MHz} \Rightarrow 30\text{dBuV}$
 $f > 70\text{MHz} \Rightarrow 15\text{dBuV}$

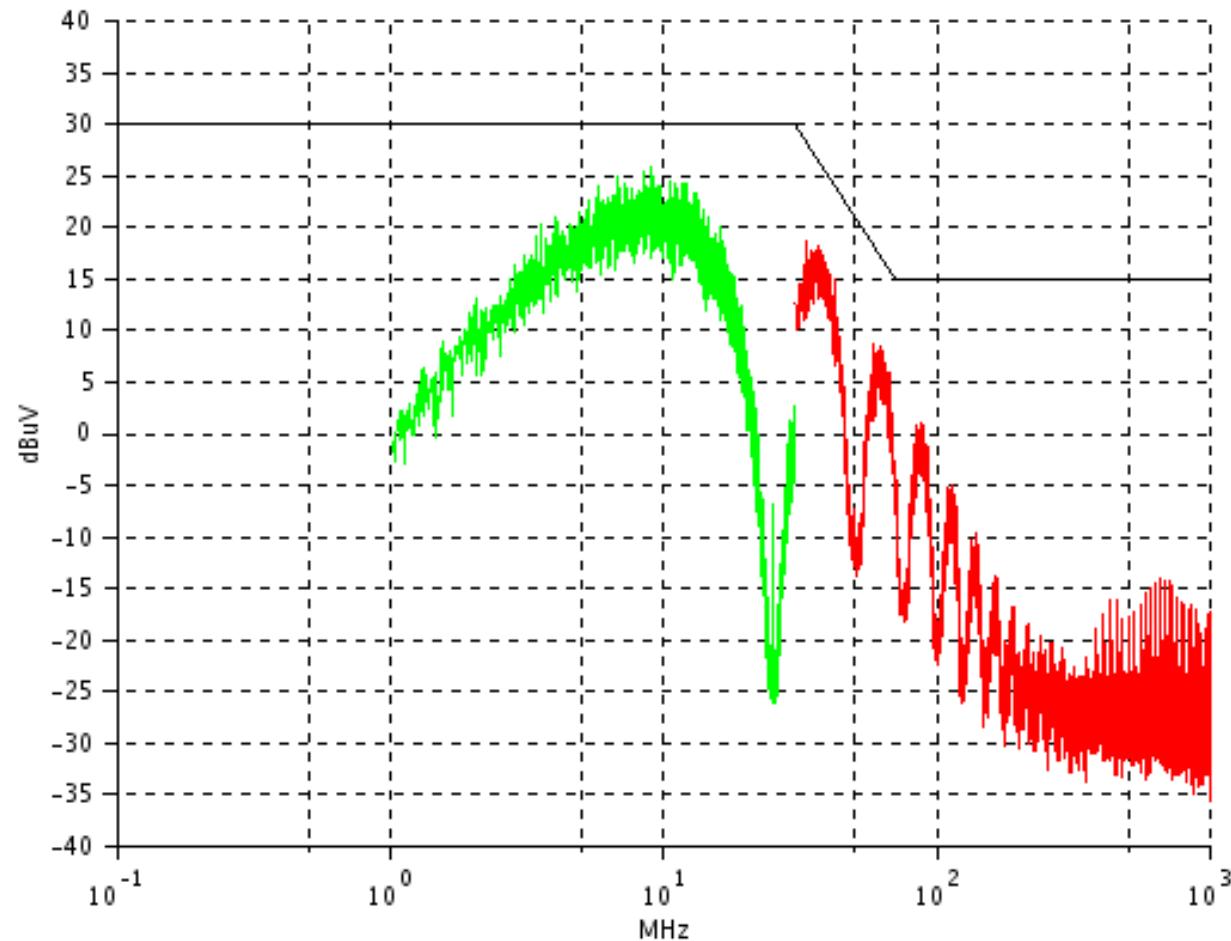
EMC Simulation $\pm 5\%$ unbalance, TX = 1 Vpp



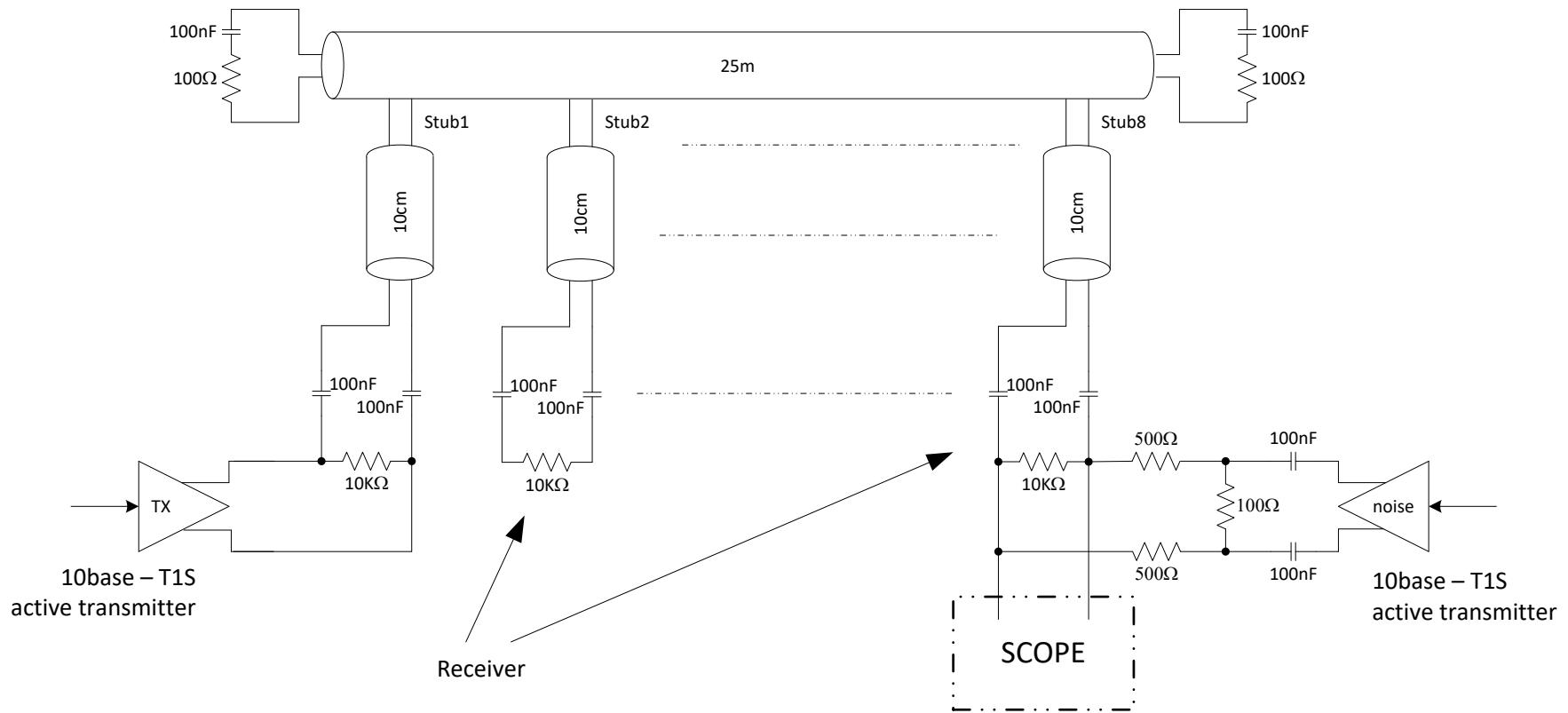
EMC Simulation $\pm 5\%$ unbalance, TX = 1.3 Vpp



EMC Simulation $\pm 2.5\%$ unbalance, TX = 1.3 Vpp

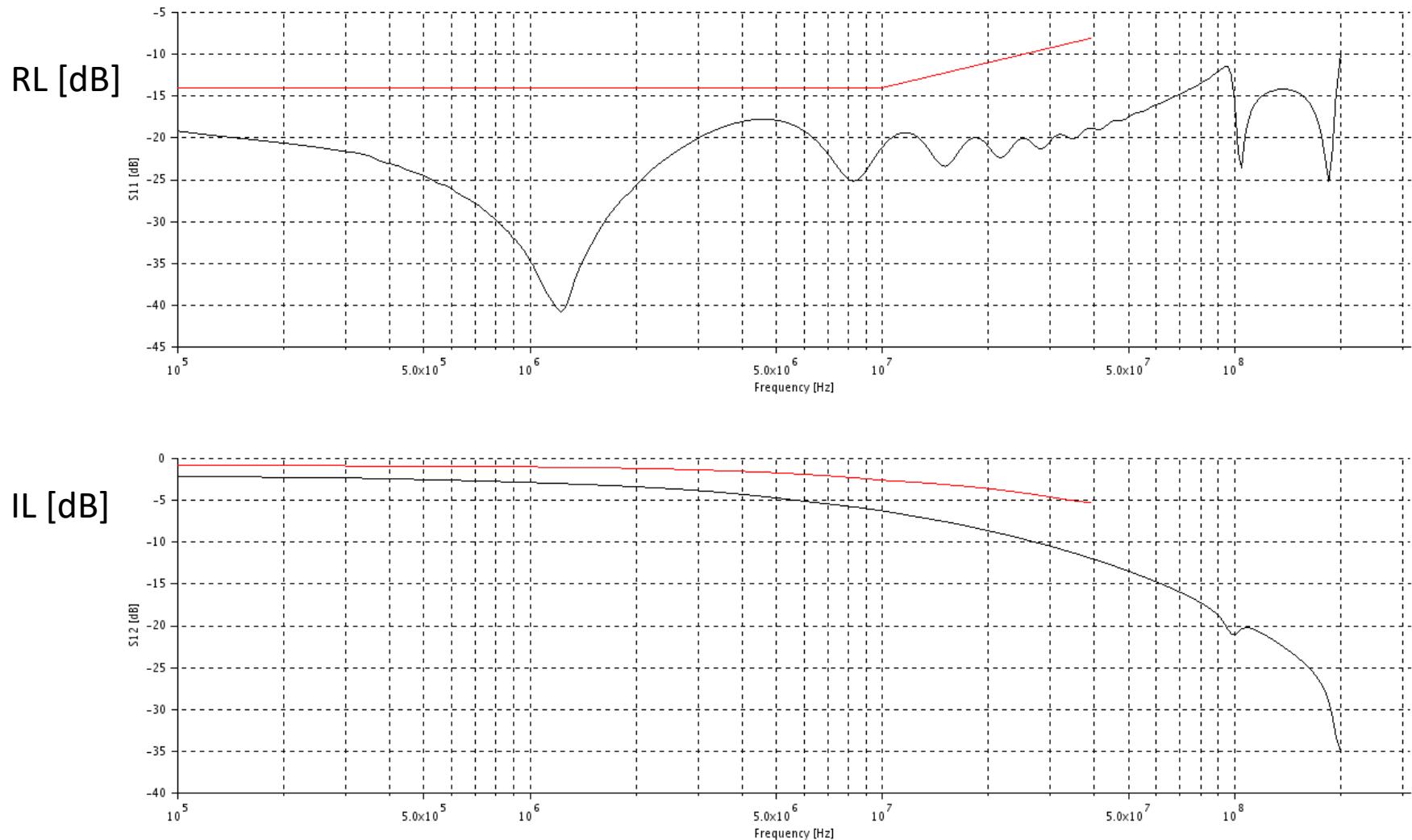


Simulations: Multidrop link segment Test Bench

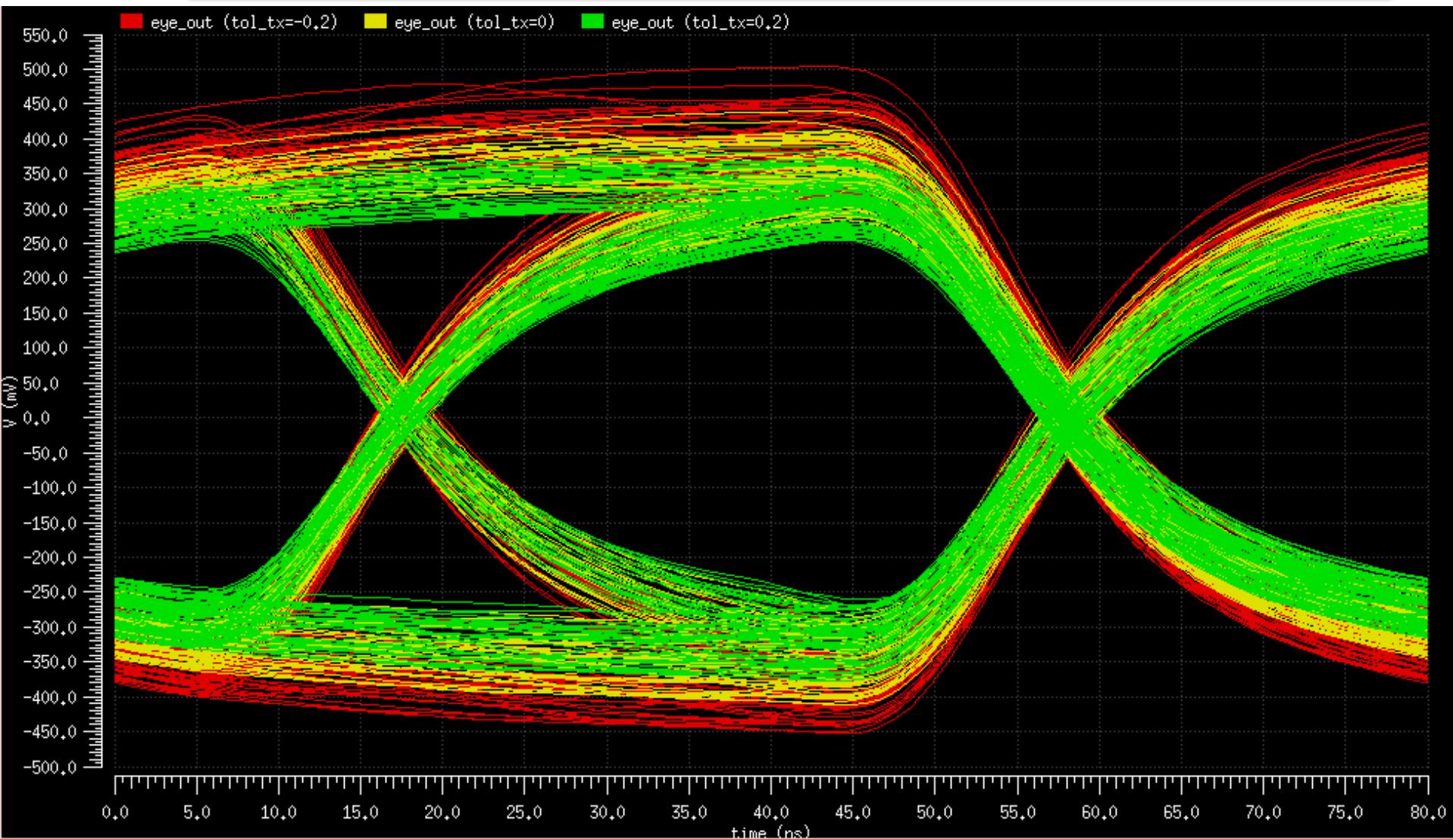


- 25 meter, 8 Nodes
- $100\Omega \pm 1\%$ line termination resistance
- $50\Omega \pm 20\%$ transmitters (high-Z when silent)
- Alien Crosstalk Noise (T1S transmitter, $\sim 50\text{mVpp}$)
- MC -43dB and -30dB (comparison)

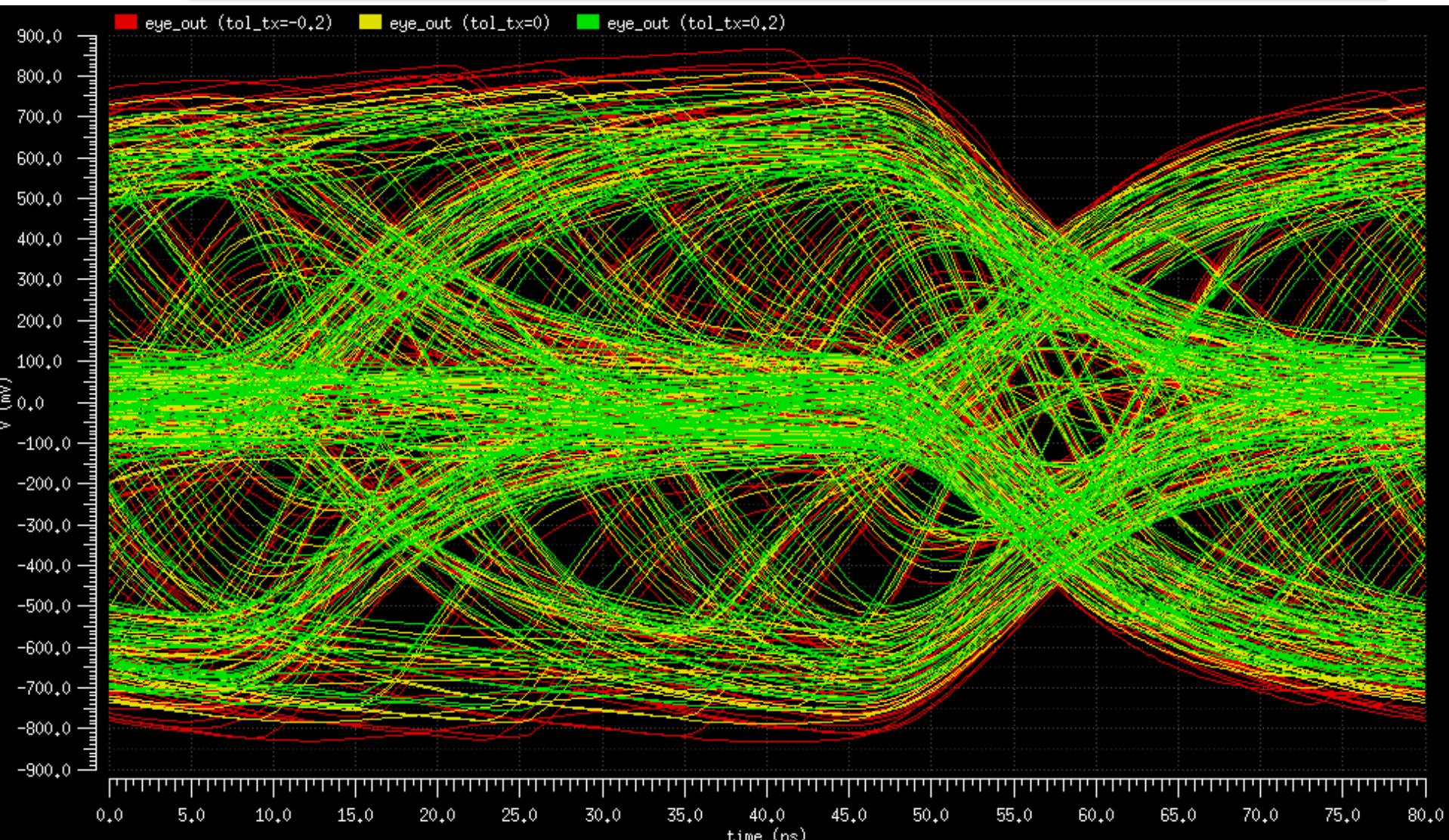
Simulations: link segment RL, IL



Link segment with 50mVpp Alien Noise



Link segment with 50mVpp Alien Noise + DPI (30dbm) and MC = -30dB



Link segment with 50mVpp Alien Noise + DPI (30dbm) and MC = -43dB

