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*IEEE 802.3cg* PLCA Burst mode *October 24<sup>th</sup>, 2018* 









- PLCA currently provides packet-level access fairness (round-robin) across all nodes on the mixing-segment network.
- In some cases, weighing the share of the media among the nodes is desirable
  - 802.3cg D2.0 comments:
    - #371, #372 (Kirsten Matheus, BMW)
    - #503, #504, #505 (Peter Jones, Cisco)
- This is a new feature request for PLCA: burst mode





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- Nodes can be individually configured for bursting from management entity by setting the max number of packets that can be sent in a single burst (max\_bc)
  - $\max_b c = 0$   $\rightarrow$  no burst, up to one packet per BEACON (default)
  - $\max\_bc = N$   $\rightarrow$  up to N additional packets per BEACON
- Bursting nodes can 'keep' their transmit opportunity by filling the IPG with IDLE (i.e. COMMIT from PLCA perspective)
- Problem is, a node cannot know in advance whether the local MAC has more packets to send after a transmission with no (logical) collisions.
  - Only option is to fill the IPG with COMMIT **regardless**, after any transmission.
    - Except when the max\_bc limit is met
  - This yields some negative impact on the throughput in case there are nodes awaiting their TO after a logical collision
    - In such case an additional IPG period (96 bits) is wasted at the beginning of the new transmission



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• PLCA burst mode could provide a simple solution for situations where communication is inherently heterogeneous among the nodes



bounded delays

PLCA burst mode (#0 configured with max\_bc = 1)





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## **Changes to State Diagrams**

- bc = counts the number of packets currently sent in a burst after the first transmission Value: integer from 0 to 255
- max\_bc = maximum number of packet the node is allowed to transmit in a single burst after the first transmission. Set by management via MDIO register TBD. Value: integer from 0 to 255
- BURST\_TIMER counts the time to wait for the MAC to send a new packet before yielding the transmit opportunity. This timer can be set via MDIO register TBD. For PLCA Burst mode to work properly this timer needs to be greater than one IPG period.





- BURST\_TIMER have to be set large enough to allow the local MAC to transmit a new packet
  - Assuming the MAC is capable of transmitting packets back-to-back, BURST\_TIMER have to be at least 96 bits (one IPG at 10Mb/s operation) + some margin
  - BURST\_TIMER needs not be configured equal across all nodes
  - Increasing BURST\_TIMER relaxes time requirements on the MAC at the expense of throughput.
- Throughput is not significantly affected by burst mode
  - Worst case scenario is when the BUS is at max load, every node send the smallest allowed packet (72 bytes) and all are allowed to burst.
    - In this case the throughput penalty is one IPG per node.
      - That is, 1 (72 \* 8 + 96) / (72 \* 8 + 96 \* 2) = 12.5 %

## THANK YOU!



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