P802.3ch Task Force Adopted Timeline
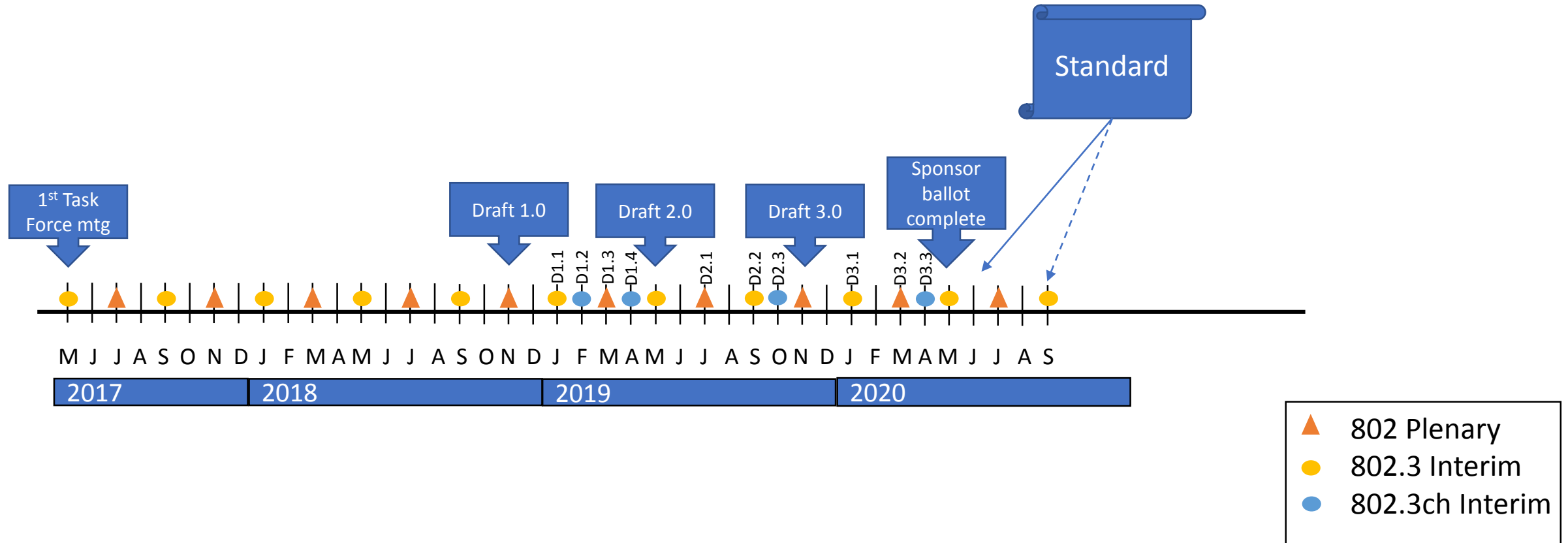
IEEE P802.3 P802.3ch NGAUTO Ethernet PHY DRAFT Timeline

- September 2017 - Review baseline proposals (Draft 0.1 skeleton)

- March 2018 – Draft 0.2 for TF review (link segment)

- September 2018 – baselines selected

- November 2018 – last technical feature, D1.0 for TF review
  - D1.1 (January), D1.2 (OOS February Interim), D1.3 (March), (OOS April Interim)?

- May 2019 – D2.0, start WG ballot
  - 2 recircs, July 2019 (D2.1), September 2019 (D2.2) (OOS interim in October to get to sponsor ballot in November 2019)

- November 2019 – D3.0, begin Sponsor ballot
  - D3.1 (January 2020), D3.2 (March) recirculations (Potential April OOS Interim)

- May/June 2020 SASB approval

Adopted March 2018

Goals to Draft 1.0

- March 2018
  - OEM topology Information and power consumption expectations
  - Cabling System Model (IL & RL at a minimum)
  - Signaling type and modulation (2.5 Gb/s and 10 Gb/s)
  - PHY vendors provide information on what type of EMC test data is preferred
  - Consider power budget and impact on PoDL

Goals to Draft 1.0

- May 2018
  - Signaling type and modulation (2.5 Gb/s and 10 Gb/s) – Sujan, Tom S., Peter, Ramin, Tazhi, Conrad
  - PHY vendors provide information on what type of EMC test data is preferred
    - To provide presentations at 3/21/18 Ad Hoc
  - Topology
    - Consolidated among OEMs – What is the max for 95% - 97% of topologies - Natalie
  - Cabling System Model Improvements (Coupling Attenuation, TCL, ELTCTL) – Thomas and Eric
    - Gaussian distribution of cable impedance instead of equal
    - Analyze other OEM topologies
    - 26 gauge up to 11? m and 24 gauge up to 15 m
  - Finalized: Signaling type and modulation (2.5 Gb/s and 10 Gb/s)
    - MDI RL limit (baseline to be provided to PoDL) -
  - Provide EMC test data

Goals to Draft 1.0

- July 2018
  - Line Coding and FEC initial proposals
  - PoDL definition (solicit help from bt)
  - OEMs provide results of Early EMC testing

Goals to Draft 1.0

- September 2018
  - Line Coding and FEC updated proposals
  - PoDL definition baseline proposals
  - Initial Register maps, EEE support, PICs
  - PHY Control State diagram proposals (take into account start up time requirements)

- November 2018
  - Line Coding and FEC baseline adopted
  - Complete Register maps, EEE support, PICs
  - PHY Control State diagram finalize