

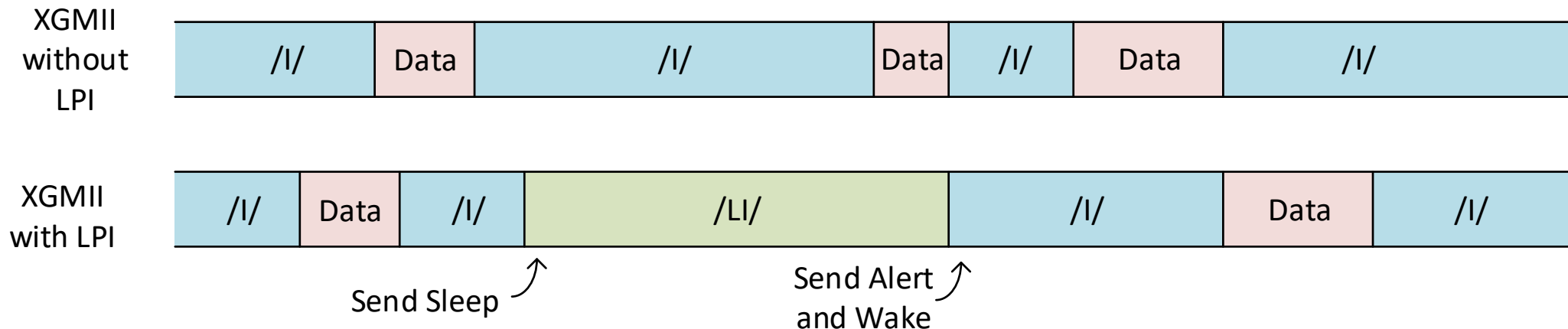
# Asymmetric Data Transmission Support

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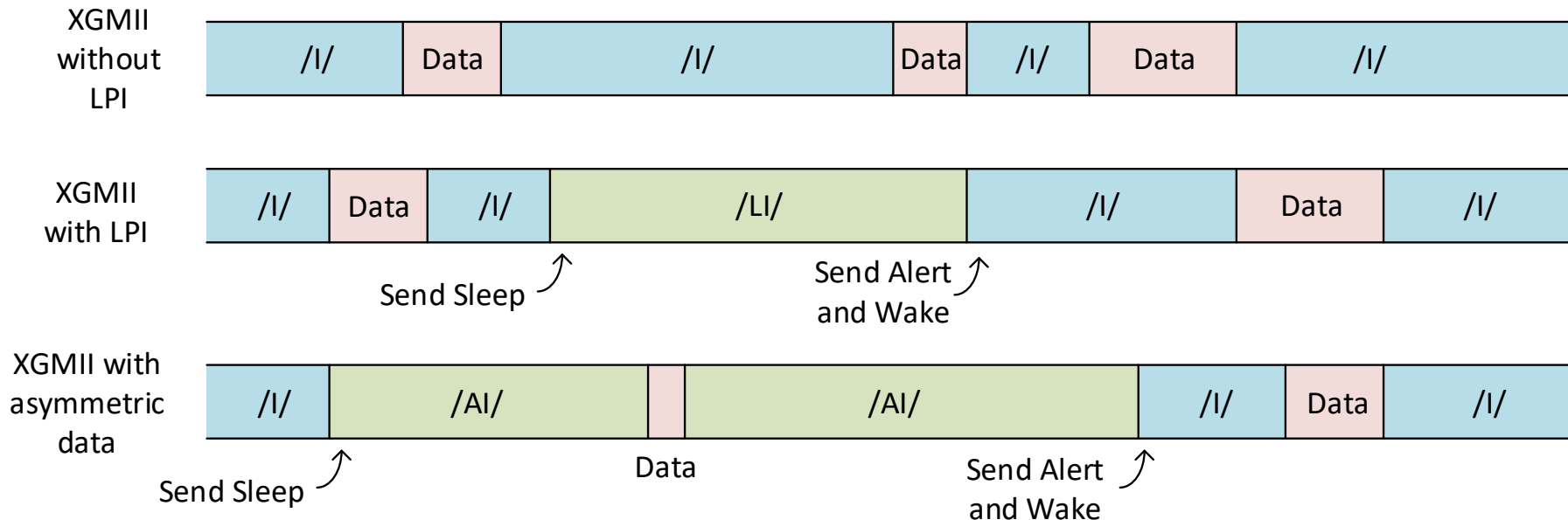
# Background

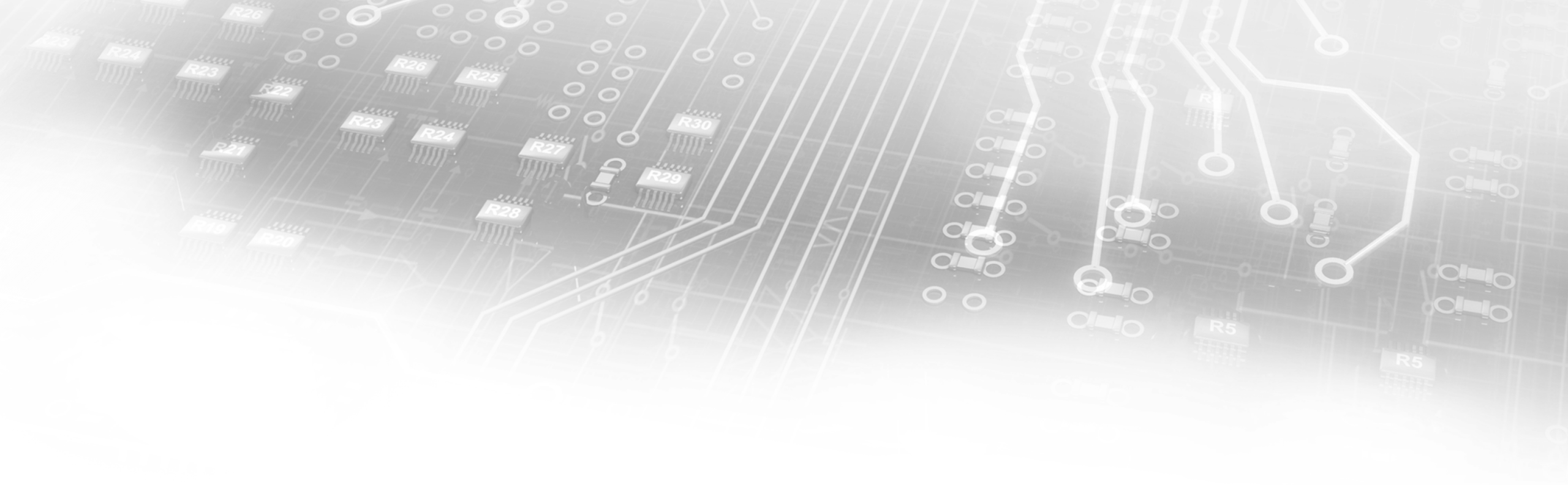
- We have proposed transmitting low rate data during LPI mode.
  - See [souvignier\\_3ch\\_01\\_0918.pdf](#).
- There was a concern about how the MAC could send low rate data to the PHY
- Here we propose a method overcoming the concern
- Note how data flows currently in > 2.5GBASE-T EEE
  - Idles (/I/s) interspersed with data
  - /LI/s and /I/s are used to start and end LPI mode for EEE capable PHYs



## Proposal – New control character, /AI/

- Define a new control character: asymmetric Idles
- Use EEE infrastructure
  - Possible to enter and exit asymmetric mode
  - /AI/s are used instead of /I/s for inter-frame gaps in this mode
- Burden is on the MAC to send data at the appropriate rate





# THANK YOU

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