OAM Status Bytes Proposal

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OAM Symbol 10 Proposal

D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
0	1	Status Valid	Power Supply warning	PHY internal temp warning	No MAC messages warning	Degraded link segment	Pair swapped	Clear REC	Reserved 0

This status symbol provides information on potential issues recognized by the PHY that may impact the PHY/Frame performance. Each status bit shall be updated to reflect the current PHY status.
Specific usage shown below defined in Informative Annex.

- Status Valid: Sent as 1 if the remaining bits are valid, sent as 0 if any bit is not valid
- Power Supply warning: Sent as 1 if the PHY power supply(s) is near the limit, otherwise sent as 0
- PHY internal temperature warning: Sent as 1 if the PHY internal temperature is near the shutdown temperature, otherwise sent as 0
- No MAC messages warning: Sent as 1 if there are no messages from MAC, otherwise sent as 0
- Degraded Link Segment: Sent as 1 if there is a potential issue with the link segment, otherwise sent as 0
- Pair swapped: Sent as 1 if the + and – signal lines are swapped, otherwise sent as 0
- Clear REC: Sent as 1 if REC<15:0> should be cleared, otherwise sent as 0

OAM Symbol 11 Proposal

D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
0	1	Reserved 0	Reserved 0	Reserved 0	Reserved 0	Reserved 0	Reserved 0	Reserved 0	Reserved 0

This symbol is reserved for future use.

OAM Symbols 12&13 Proposal

	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Symbol 12	0	1	REC<15>	REC<14>	REC<13>	REC<12>	REC<11>	REC<10>	REC<9>	REC<8>
Symbol 13	0	1	REC<7>	REC<6>	REC<5>	REC<4>	REC<3>	REC<2>	REC<1>	REC<0>

REC<15:0>: This 2 byte symbol indicates the number error RS-FEC block errors, both correctable and uncorrectable, that have been seen by the PHY's receiver since it was last cleared. This counter shall be cleared when Clear REC (Symbol<10><1>) is received as 1. If the counter reaches 0xFFFF, it shall stay there until cleared by Clear REC.