Backplane COM Analysis for Reference Rx Baseline

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May 2019

IEEE 802.3ck 100 Gb/s, 200 Gb/s and 400 Gb/s Electrical Interfaces Task Force

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Summary & Recommendations

- Objective: Develop reference equalizer recommendations for KR.
- Criteria: Choose equalizer to
 - maximize the percentage of contributed channels with ≥3dB COM.
 - get all of the 'highlighted' (a.k.a. 'must work') channels to ≥3dB COM.
- Proposed reference equalizer:
 - 80%-83% of sub-29dB channels meet 3dB COM (C_d dependent).
 - All but 3 of the 'highlighted' channels meet 3dB COM.
 None of the options analyzed achieved 3dB.
 - We also analyzed the impact of C_d , as part of the analysis to help the TF select the C_d baseline.
- To address the COM deficit for the failing channels we analyzed package trace loss & length mismatch.
 - Results were negative. They are included in backup.

Parameter	Recommendation
TxFFE $c(-3)$	[-0.06: 0.02: 0.00]
# Fixed DFE taps	16
# Floating DFE banks	2
# taps per bank	4
max bank span	40UI from cursor
$b_{max}(1)$	0.85

Channels

Channels – Full Set

# Ma	ain File	Folder	Files	Documentation	
1		Cable_BKP_16dB_0p575m.zip	Cable_BKP_16dB_0p575m_*.s4p		
2 .	AN CONTRACTOR OF	Cable_BKP_16dB_0p575m_more_isi.zip	Cable_BKP_16dB_0p575m_more_isi_*.s4p	1	
cat	ble_CKP_16dB.zip	Cable_BKP_16dB_0p995m_updated.zip	Cable_BKP_16dB_0p995m_updated_*.s4p	1	
4	8	Cable BKP 16dB 0p0p995m more isi updated.zip	Cable BKP 16dB 0p0p995m more isi updated *.s4p	1	
5		Cable_BKP_20dB_0p575m.zip	Cable_BKP_20dB_0p575m_*.s4p		
6		Cable_BKP_20dB_0p575m_more_isi.zip	Cable_BKP_20dB_0p575m_more_isi_*.s4p	1	
7 cat	ble_CKP_20dB.zip	Cable_BKP_20dB_0p995m_updated.zip	Cable_BKP_20dB_0p995m_updated_*.s4p		
8		Cable_BKP_20dB_0p0p995m_more_isi_updated.zip	Cable_BKP_20dB_0p0p995m_more_isi_updated_*.s4p	1	
9		Cable_BKP_24dB_0p575m.zip	Cable_BKP_24dB_0p575m_*.s4p	heck_3ck_02_0119.pdf	
0 .		Cable_BKP_24dB_0p575m_more_isi.zip	Cable_BKP_24dB_0p575m_more_isi_*.s4p	1	
1 cat	ble_CKP_24dB.zip	Cable_BKP_24dB_0p995m_updated.zip	Cable_BKP_24dB_0p995m_updated_*.s4p	1	
2	,	Cable_BKP_24dB_0p0p995m_more_isi_updated.zip	Cable_BKP_24dB_0p0p995m_more_isi_updated_*.s4p	1	
3		Cable_BKP_28dB_0p575m.zip	Cable_BKP_28dB_0p575m_*.s4p	1	
4	A PARTIE AND A PAR	Cable_BKP_28dB_0p575m_more_isi.zip	Cable_BKP_28dB_0p575m_more_isi_*.s4p	1	
5 cat	ble_CKP_28dB.zip	Cable_BKP_28dB_0p995m_updated.zip	Cable_BKP_28dB_0p995m_updated_*.s4p		
5		Cable_BKP_28dB_0p0p995m_more_isi_updated.zip	Cable_BKP_28dB_0p0p995m_more_isi_updated_*.s4p	1	
7		DPO_IL_12dB	DPO_4in_Meg7_*.s4p		
В		DPO_IL_24dB	DPO_10in_Meg7_*.s4p	1	
e tra	acy_3ck_02_0119_orthoBP.zip	DPO_IL_28dB	DPO 12in Meg7 *.s4p	tracy_3ck_01b_0119.pdf	
0		DPO_IL_32dB	DPO_14in_Meg7_*.s4p		
1 tra	acy_3ck_03_0119_tradBP.zip	To Control of the Con	Std_BP_12inch_Meg7_*.s4p	1	
2		Link_1			
3		Link_2			
4		Link_3			
5		Link_4			
6 zar	mbell_3ck_01_1118_links01to09.zip	Link_5			
7	Face) 1/10 M/09 Face 92%	Link_6			
8		Link_7			
.9	Link 8				
0	í	Link_9			
1		Link_10			
2		Link_11			
3		Link_12			
4		Link_13			
5 zar	mbell_3ck_01_1118_links10to18.zip	Link_14	See the folder	zambell_3ck_01_1118.pdf	
6	2540 C 1441 (2442 C 1044	Link_15		Section Management At	
7		Link_16			
В		Link_17			
9		Link_18			
0		Link_19			
1		Link_20			
2		Link_21			
3		Link_22			
4 zar	mbell_3ck_01_1118_links19to278.zip	Link_23			
5		Link_24			
5		Link_25			
7		Link_26			
В		Link 27			
•		CaBP_BGAVia_Opt1_24dB.zip	CaBP_BGAVia_Opt1_24dB_*.s4p		
5		CaBP_BGAVia_Opt1_28dB.zip	CaBP_BGAVia_Opt1_28dB_*.s4p	1	
	-Ultra 2-b adhar 02 001510 ashladhari 1	CaBP_BGAVia_Opt1_32dB.zip	CaBP_BGAVia_Opt1_32dB_*.s4p		
me 2	ellitz_3ck_adhoc_02_081518_cabledbackplane.zip	CaBP_BGAVia_Opt2_24dB.zip	CaBP_BGAVia_Opt2_24dB_*.s4p	mellitz_3ck_adhoc_02_081518.pd	
3		CaBP BGAVia Opt2 28dB.zip CaBP BGAVia Opt2 28dB *.s4p			
4		CaBP BGAVia Opt2 32dB.zip	CaBP BGAVia Opt2 32dB *.s4p	1	

#	Main File	Folder	Files	Documentation
55			Bch1_3p5	
56			Bch2_7	1
57			Bch2_a0_7	1
58			Bch2_a10_7	1
59			Bch2_a12p5_7	1
60			Bch2_a15_7	1
61			Bch2_a2p5_7	1
62			Bch2 a5 7	1
63			Bch2_a7p5_7	1
64	kareti_3ck_01_1118_backplane.zip		Bch2_b10_7	1
65			Bch2_b15_7	1
66			Bch2_b2p5_7	1
67			Bch2_b2_7	1
68			Bch2 b4 7	1
69			Bch2_b6_7	1
70			Bch2_b7p5_7	1
71			Bch2_b8_7	1
72			Bch3_14	1
73			Bch4_30	1
74			CAch1_b2	1
75	•		CAch1	-
76			CAch2_a0	1
77			CAch2_a10	-
78			CAch2_a2p5	-
79			CAch2_a5	-
80			CAch2 a7p5	+
81			CAch2_b10	kareti_3ck_01a_1118.pdf
82			CAch2_b2p5	kareti_5ck_01a_111a.pur
83	kareti_3ck_01_1118_cabledBP.zip		CAch2_b2	-
84	kareti_sck_o1_1116_cabledBP.2ip	(5)	CAch2_b4	-
85			CACh2_b4 CAch2_b6	-
10000			- N. 100 N. 11 - 100 N	-
86			CAch2_b7p5 CAch2_b8	-
88			CAch2_08	-
-				-
90			CAch3_b2 CAch3	+
91			CAch4_b2	1
92			CAch4	-
93			OAch1	+
94			OAch2	1
95			OAch3	-
				-
96 97			OAch4 OAch5	-
20.00			474000MOV.	-
98			OAch6 OAch7	-
2000	karati 2ck 01 1110 arthaco		1000000	1
100	kareti_3ck_01_1118_orthoBP.zip		Och1	-
101			Och2 Och3	-
102				-
103			Och4	-
104			Och5	-
105			Och6	-
106			Och7	-
107			Och8	

107 channels pulled from the p802.3ck repository.

COM spreadsheet is included in backup slides.

P802.3ck Highlighted Channel Subset

Contribution	Channel	#	Name	IL (dB)
hook 2ak 01 1110	28dB Cabled Backplane/Cable_BKP_28dB_0p575m_more_isi	14	Heck1	28.8
heck 3ck 01 1118	16dB Cabled Backplane/Cable_BKP_16dB_0p575m_more_isi	2	Heck2	15.2
mellitz 3ck adhoc 02 081518	24,28,30dB including BGA Via/CaBP_BGAVia_Opt2_28dB	53	Mellitz1	26.3
tracy 3ck 01 0119	Traditional Backplane Channels/Std_BP_12inch_Meg7	21	Tracy1	15.7
	Orthogonal Backplane Channels/DPO_IL_12dB	17	Tracy2	12.2
	Measured Orthogonal Backplane Channels/OAch4	96	Kareti1	27.7
1	Measured Orthogonal Backplane Channels/Och4	103	Kareti2	28.1
kareti 3ck 01a 1118	Measured Cabled Backplane Channels/CAch3_b2	89	Kareti3	28.5
	Measured Traditional Backplane Channels/Bch2_a7p5_7	63	Kareti4	28.4
I	Measured_Traditional_Backplane_Channels/Bch2_b7p5_7	70	Kareti5	28.9

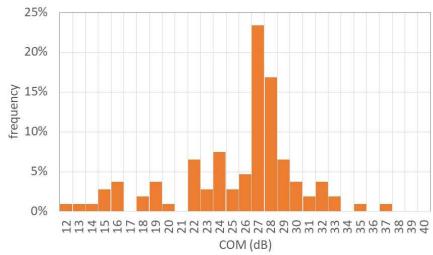
Source: kochuparambil_3ck_01a_0119.pdf.

Channel Selection

- Approach: Restrict analysis to sub-29dB channels.
- Reasoning:
 - —Our 28dB objective doesn't require that all channels with 28dB insertion loss meet COM≥3.0dB.
 - –Nor does it mean that some channels with more than 28dB insertion loss won't meet COM≥3.0dB.
 - –All of our highlighted channels have less than 29dB insertion loss. Three of them fall between 28dB & 29dB.

Channel Insertion Loss Statistics

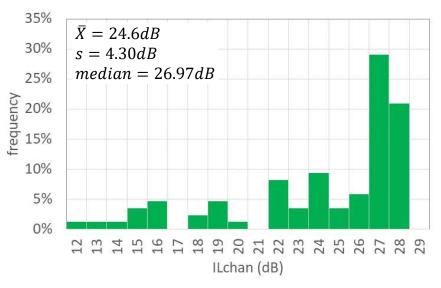






All 'highlighted' channels fit within 29dB.

Sub-29dB Channel Distribution



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Floating Tap Algorithm

- 1) Get the ISI waveform vector, $X = \begin{bmatrix} x_1 & x_2 & x_3 & ... & x_n \end{bmatrix}$.
- 2) Compute the norm, \overline{X} , of the ISI vector.
- 3) Apply a bank (e.g. 3 taps) to each successive ISI sample set: $[x_1 \ x_2 \ x_3], [x_2 \ x_3 \ x_4], ..., [x_{n-2} \ x_{n-1} \ x_n]$
- 4) Compute the norm for each location from step 3): \bar{X}_1 , \bar{X}_2 ,..., \bar{X}_{n-2}
- 5) Select the location that gives max difference in the norm: $\max_{i}(\bar{X} \bar{X}_{i})$.
- 6) Repeat steps 1-5 for each bank.

Sub-29dB Contributed Channel Analysis

Experiment Definition

Case	Ыпах	Npb	UI_{mf}	C _d (fF)	c(-3)	$ m L_{pkg}(mm)$
1	0.85	3	40	110	0	12,32
2	0.85	3	80	110	0	12,32
3	0.85	3	40	110	1	12,32
4	0.85	3	80	110	1	12,32
5	0.85	3	40	120	0	12,32
6	0.85	3	80	120	0	12,32
7	0.85	3	40	120	1	12,32
8	0.85	3	80	120	1	12,32
9	0.85	3	40	130	0	12,32
10	0.85	3	80	130	0	12,32
11	0.85	3	40	130	1	12,32
12	0.85	3	80	130	1	12,32
13	0.85	4	40	110	0	12,32
14	0.85	4	80	110	0	12,32
15	0.85	4	40	110	1	12,32
16	0.85	4	80	110	1	12,32
17	0.85	4	40	120	0	12,32
18	0.85	4	80	120	0	12,32
19	0.85	4	40	120	1	12,32
20	0.85	4	80	120	1	12,32
21	0.85	4	40	130	0	12,32
22	0.85	4	80	130	0	12,32
23	0.85	4	40	130	1	12,32
24	0.85	4	80	130	1	12,32

Case	blmax	Npb	ПmI	C _d (fF)	c(-3)	$ m L_{pkg}(mm)$
25	0.7	3	40	110	0	12,32
26	0.7	3	80	110	0	12,32
27	0.7	3	40	110	1	12,32
28	0.7	3	80	110	1	12,32
29	0.7	3	40	120	0	12,32
30	0.7	3	80	120	0	12,32
31	0.7	3	40	120	1	12,32
32	0.7	3	80	120	1	12,32
33	0.7	3	40	130	0	12,32
34	0.7	3	80	130	0	12,32
35	0.7	3	40	130	1	12,32
36	0.7	3	80	130	1	12,32
37	0.7	4	40	110	0	12,32
38	0.7	4	80	110	0	12,32
39	0.7	4	40	110	1	12,32
40	0.7	4	80	110	1	12,32
41	0.7	4	40	120	0	12,32
42	0.7	4	80	120	0	12,32
43	0.7	4	40	120	1	12,32
44	0.7	4	80	120	1	12,32
45	0.7	4	40	130	0	12,32
46	0.7	4	80	130	0	12,32
47	0.7	4	40	130	1	12,32
48	0.7	4	80	130	1	12,32

Symbol	Description	Min	Mid	Max
$b_{max}(1)$	Max coefficient value for DFE 1st postcursor			0.85
N_{pb}	# of taps per floating bank	3		4
UI _{mf}	Max distance of floating bank from cursor	40		80
C_d	Device capacitance	110 fF	120 fF	130 fF
c(-3)	3 rd precursor for TxFFE5	-0.06		None
L_{pkg}	Package main route length	12 mm		32 mm

All cases use 16 fixed taps and 2 banks of floating taps.

$$b_{max}(n) = 0.3 \text{ for } n > 1.$$

The 3rd precursor tap range is from -0.06 to 0 w/ 0.02 step size.

'Baseline' Case

Variable	Value	Units
$b_{max}(1)$	0.85	
N_{pb}	4	# taps
UI _{mf}	40	UI
C_d	130	fF
TxFFE	4	# taps

Notes: Each case is run with both package lengths and with 107 contributed backplane channels (see the "channels" worksheet). Case 21 is treated as the baseline: $b_{max}(1)$ =0.85, N_{tpb} =4, UI_{mf} =40, C_d =130fF, no 3rd TxEQ precursor.

Parameter Significance

- Statistical model fitted to raw data.
 - Identify significant effects & use them to select cases of interest.
- % meeting 3dB COM primarily depends on 1st order terms:

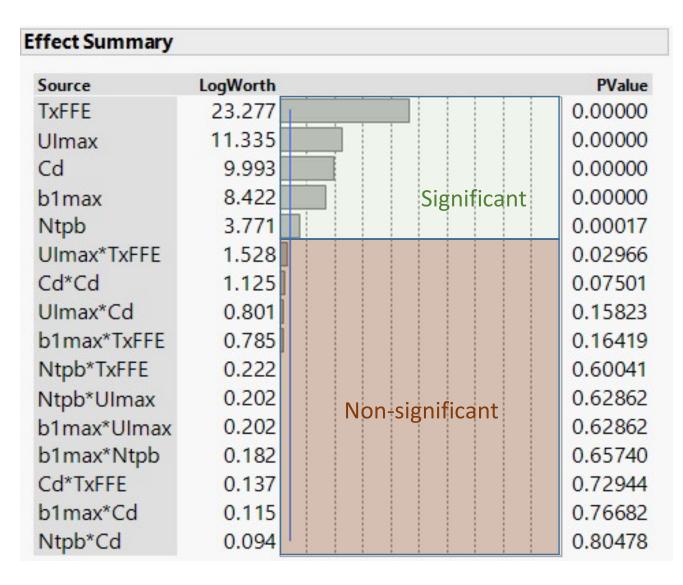
TxFFE # of Tx taps

Ulmax max bank span from cursor

Cd device capacitance

b1max max coefficient, 1st Rx postcursor

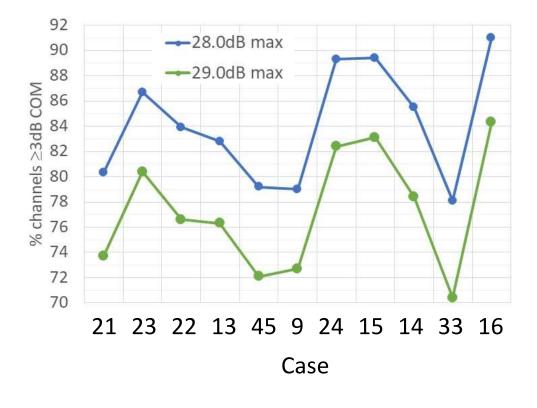
Ntpb # of taps per bank



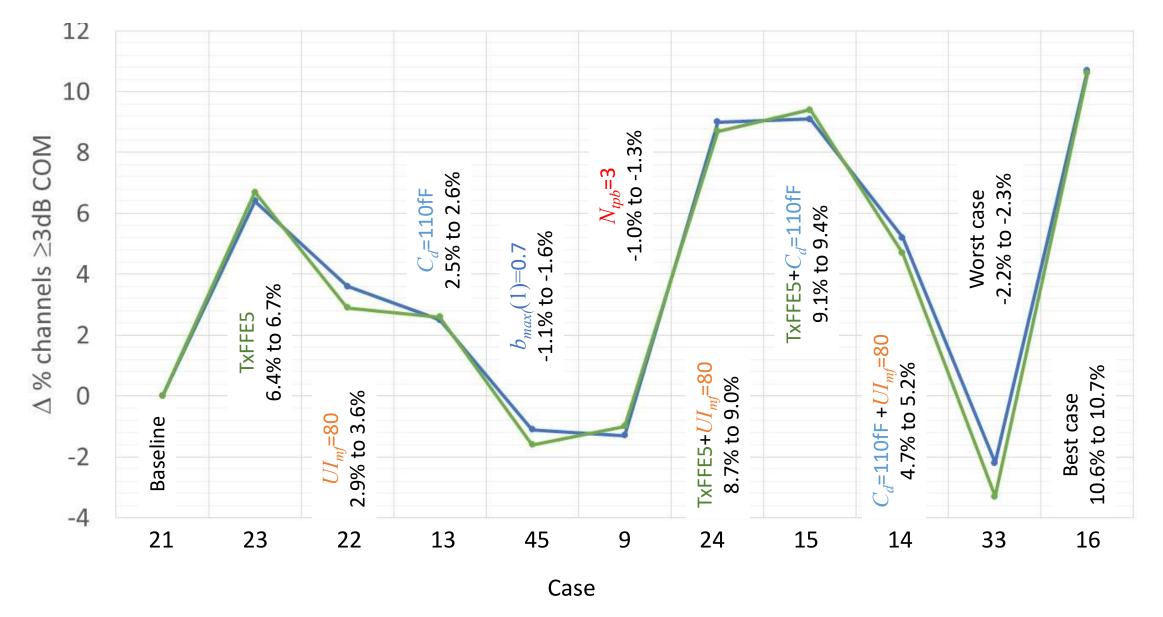
Summarized Results for Select Cases

% Channels Meeting 3.0dB Com for Selected Cases

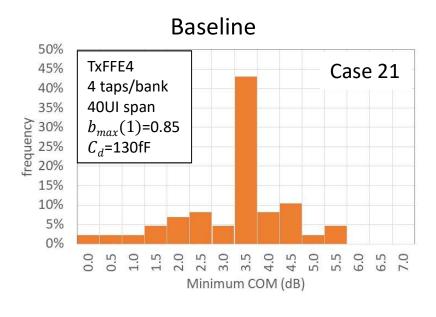
	Case		Baseline	+TvFFE5	$+UI_{mf}$	$+C_d$	-bI _{max}	$-N_{pb}$	+TNFFE5 +UImf	+TvFFE5 +C _d	$+C_d$ $+Ui_{mf}$	Worst Case	Best Case
	Sim Case	y .	21	23	22	13	45	9	24	15	14	33	16
	b1 max		0.85	0.85	0.85	0.85	0.7	0.85	0.85	0.85	0.85	0.7	0.85
N	tpb (# taps	5)	4	4	4	4	4	3	4	4	4	3	4
1	UI mf (#UI)		40	40	80	40	40	40	80	40	80	40	80
	C_d (fF)		130	130	130	110	130	130	130	110	110	130	110
Tx.	FFE (# taj	os)	4	5	4	4	4	4	5	5	4	4	5
%	ILchan	28.0	80.3	86.7	83.9	82.8	79.2	79.0	89.3	89.4	85.5	78.1	91.0
Pass	(dB)	29.0	73.7	80.4	76.6	76.3	72.1	72.7	82.4	83.1	78.4	70.4	84.3
Δ	ILchan	28.0	0.0	6.4	3.6	2.5	-1.1	-1.3	9.0	9.1	5.2	-2.2	10.7
Δ	(dB)	29.0	0.0	6.7	2.9	2.6	-1.6	-1.0	8.7	9.4	4.7	-3.3	10.6
			Baseline	TxFFE5	U_{mf} =80	C_d =110fF	$bI_{max} = 0.7$	N_{tpb} =3	TxFFE5+ UI_{mf} =80	$TxFFE5+C_d \mathtt{=} 110fF$	$_{d}$ =110fF + UI_{mf} =80	Worst case	Best case

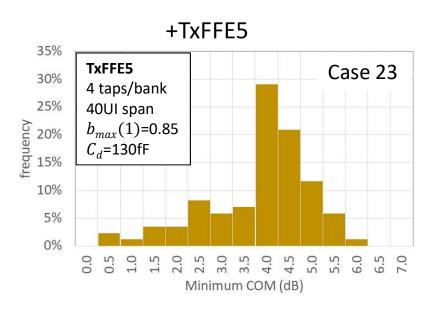


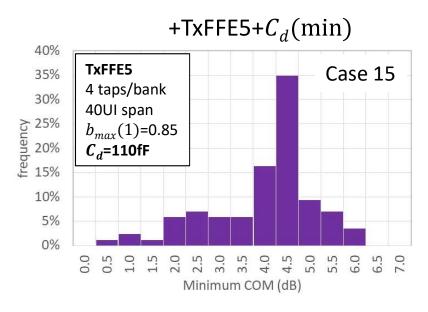
Selected Case Impact

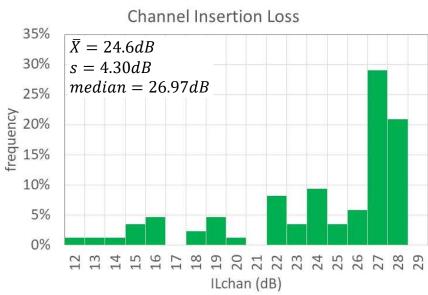


Statistics for Select Cases



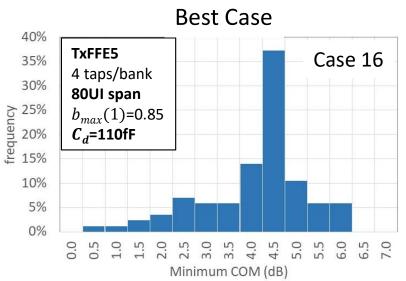




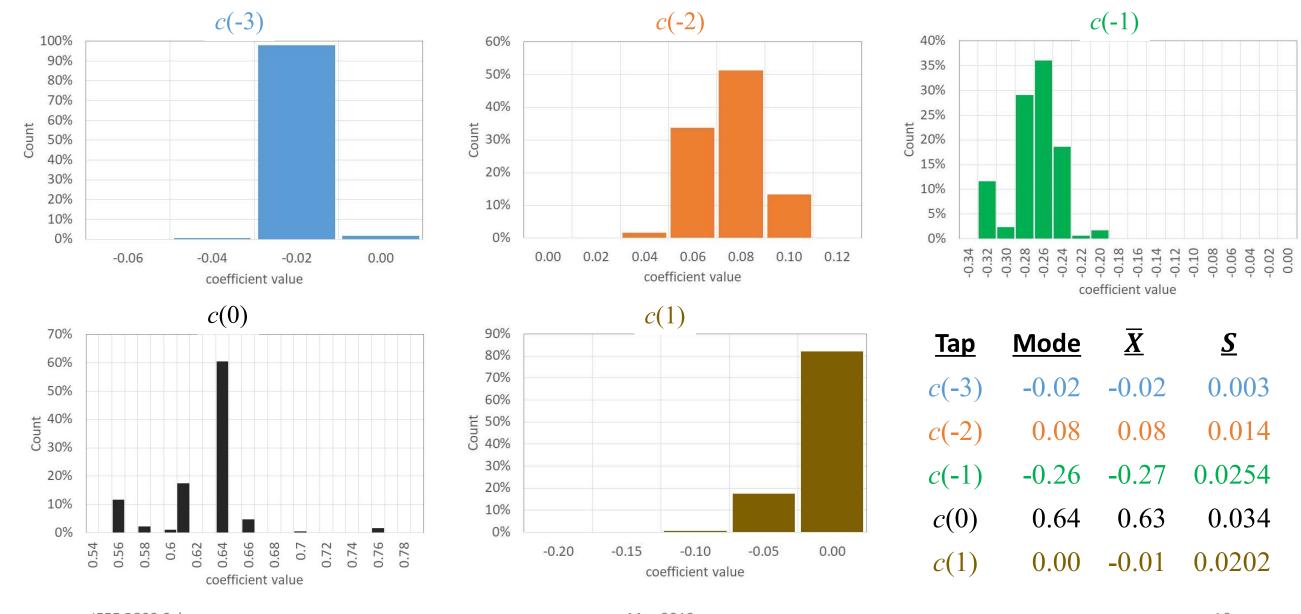


COM Summary

	<u>21</u>	<u>23</u>	<u>15</u>	<u>16</u>
$ar{X}$	3.55	4.07	4.24	4.35
\boldsymbol{S}	1.16	1.16	1.16	1.14
mode	3.70	4.31	4.54	4.54
median	3.75	4.35	4.54	4.62



TxFFE5 Coefficient Statistics for sub-29dB Channels



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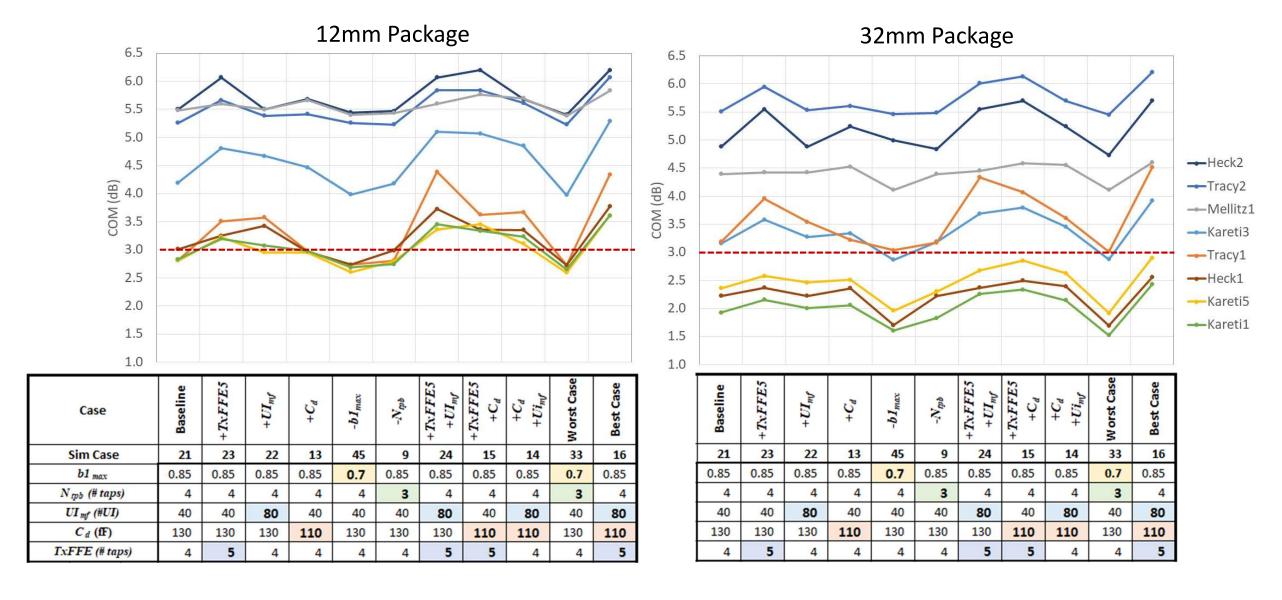
'Highlighted' Channel Analysis

P802.3ck Highlighted Channel Subset

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I	Measured_Traditional_Backplane_Channels/Bch2_b7p5_7	70	Kareti5	28.9

Source: kochuparambil_3ck_01a_0119.pdf.

Highlighted Channel Experiment Results



Summary & Recommendations

- Objective: Develop reference equalizer recommendations for KR.
- Criteria: Choose equalizer to
 - maximize the percentage of contributed channels with ≥3dB COM.
 - get all of the 'highlighted' (a.k.a. 'must work') channels to ≥3dB COM.
- Proposed reference equalizer:
 - 80%-83% of sub-29dB channels meet 3dB COM (C_d dependent).
 - All but 3 of the 'highlighted' channels meet 3dB COM.
 None of the options analyzed achieved 3dB.
 - We also analyzed the impact of \mathcal{C}_d , as part of the analysis to help the TF select the \mathcal{C}_d baseline.
- To address the COM deficit for the failing channels we analyzed package trace loss & length mismatch.
 - Results were negative. They are included in backup.

Parameter	Recommendation
TxFFE $c(-3)$	[-0.06: 0.02: 0.00]
# Fixed DFE taps	16
# Floating DFE banks	2
# taps per bank	4
max bank span	40UI from cursor
$b_{max}(1)$	0.85

Thank you!

Additional data

COM Template w/ 2 Floating Banks

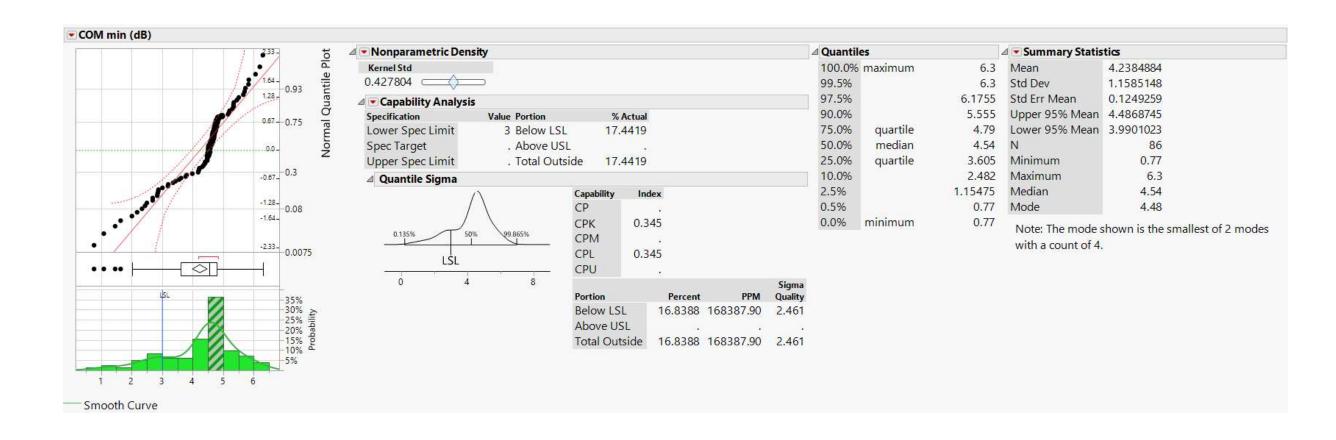
Table 93A-1 parameters				I/O control			Table 93A–3 parameters			
Parameter	Setting	Units	Information	DIAGNOSTICS	0	logical	Parameter	Setting	Units	
fb	53.125	GBd		DISPLAY WINDOW	0	logical	package tl gamma0 a1 a2	[0 0.0009909 0.0002772]		
f min	0.05	GHz		CSV REPORT	1	logical	package tl tau	6.141E-03	ns/mm	
Delta f	0.01	GHz		RESULT DIR	.\TestCaseFloatingBank\		package_Z_c	[87.5 87.5 ; 92.5 92.5]	Ohm	
Cd	[1.3e-4 1.3e-4]	nF	[TX RX]	SAVE FIGURES	0	logical				
z p select	[12]		[test cases to run]	Port Order	[1324]		Table 92–12 parameters			
z_p (TX)	[12 32; 1.8 1.8]	mm	[test cases]	RUNTAG	testBank		Parameter	Setting		
z_p (NEXT)	[12 32; 1.8 1.8]	mm	[test cases]	COM CONTRIBUTION	0	logical	board tl gamma0 a1 a2	[0 3.8206e-04 9.5909e-05]		
z_p (FEXT)	[12 32; 1.8 1.8]	mm	[test cases]		Operational		board_tl_tau	5.790E-03	ns/mm	
z_p (RX)	[12 32; 1.8 1.8]	mm	[test cases]	COM Pass threshold	3	dB	board_Z_c	90	Ohm	
Ср	[0.87e-4 0.87e-4]	nF	[TX RX]	ERL Pass threshold	10.5	dB	z_bp (TX)	119	mm	
R 0	50	Ohm		DER 0	1.00E-04		z bp (NEXT)	119	mm	
R d	[50 50]	Ohm	[TX RX]	Tr	6.16E-03	ns	z bp (FEXT)	119	mm	
Av	0.413	V	vp/vf=.694	FORCE TR	1	logical	z_bp (RX)	119	mm	
A_fe	0.413	V	vp/vf=.694	Include PCB	0	logical				
A_ne	0.608	V		TDR and ERL options						
L	4			TDR	1	logical				
M	32			ERL	0	logical				
	filter and Eq			ERL_ONLY	0	logical				
f_r	0.75	*fb		TR_TDR	0.01	ns				
c(0)	0.54		min	N	1000					
c(-1)	[-0.34:0.02:0]		[min:step:max]	TDR Butterworth	1	logical				
c(-2)	[0:0.02:0.12]		[min:step:max]	beta x	1.70E+09					
c(-3)	[-0.06:0.02:0]		[min:step:max]	rho x	0.18					
c(1)	[-0.1:0.05:0]		[min:step:max]	fixture delay time	0	enter sec				
N b	20	UI			ceiver testing					
b_max(1)	0.85			RX_CALIBRATION	0	logical				
b_max(2N_b)	0.3			Sigma BBN step	5.00E-03	V				
g DC	[-20:1:0]	dB	[min:step:max]		Noise, jitter					
f z	21.25	GHz		sigma RJ	0.01	UI				
f p1	21.25	GHz		A DD	0.02	UI				
f p2	53.125	GHz		eta 0	8.20E-09	V^2/GHz				
g DC HP	[-6:1:0]		[min:step:max]	SNR TX	33	dB				
f_HP_PZ	0.6640625	GHz	Ç	R LM	0.95					
ffe pre tap len	0	UI								
ffe post tap len	0	UI								
ffe tap step size	0.02									
ffe main cursor min	0.7									
ffe pre tap1 max	0.3									
ffe post tap1 max	0.3									
ffe tapn max	0.125									
ffe backoff	0									
Nb_floatingbank	2									
Nb tapperbank	3								=	
Floating maxUI	100	UI	>N b+Nb floating				 Used with ve 	rsion 2.58 of th	ne tool	
Floating_maxBound	0.3		2.,,,,,_,,,,,,,,,,,,,,,,,,,,,,,,,,,				_ SSCA WICH VC	13.311 2.33 31 11		

COM Template – 24 Fixed Taps

Table 93A-1 parameters					I/O control			Table 93A–3 parameters			
Parameter	Setting	Units	Information	DIAGNOSTICS	0	logical	Parameter	Setting	Units		
f_b	53.125	GBd		DISPLAY_WINDOW	0	logical	package_tl_gamma0_a1_a2	[0 0.0009909 0.0002772]			
f_min	0.05	GHz		CSV_REPORT	1	logical	package_tl_tau	6.141E-03	ns/mm		
Delta_f	0.01	GHz		RESULT_DIR	.\TestCaseFloatingBank\		package_Z_c	[87.5 87.5 ; 92.5 92.5]	Ohm		
C_d	[1.3e-4 1.3e-4]	nF	[TX RX]	SAVE_FIGURES	0	logical					
z_p select	[12]		[test cases to run]	Port Order	[1324]		Table 92–12 parameters				
z_p (TX)	[12 32; 1.8 1.8]	mm	[test cases]	RUNTAG	testBank		Parameter	Setting			
z_p (NEXT)	[12 32; 1.8 1.8]	mm	[test cases]	COM_CONTRIBUTION	0	logical	board_tl_gamma0_a1_a2	[0 3.8206e-04 9.5909e-05]			
z_p (FEXT)	[12 32; 1.8 1.8]	mm	[test cases]		Operational			5.790E-03	ns/mm		
z_p (RX)	[12 32; 1.8 1.8]	mm	[test cases]	COM Pass threshold	3	dB	board_Z_c	90	Ohm		
C_p	[0.87e-4 0.87e-4]	nF	[TX RX]	ERL Pass threshold	10.5	dB	z_bp (TX)	119	mm		
R_0	50	Ohm		DER_0	1.00E-04		z_bp (NEXT)	119	mm		
R_d	[50 50]	Ohm	[TX RX]	T_r	6.16E-03	ns	z_bp (FEXT)	119	mm		
A_v	0.413	V	vp/vf=.694	FORCE_TR	1	logical	z_bp (RX)	119	mm		
A_fe	0.413	V	vp/vf=.694	Include PCB	0	logical					
A_ne	0.608	V		TDR	TDR and ERL options						
Ĺ	4			TDR	1	logical					
M	32			ERL	0	logical					
	filter and Eq				0	logical					
f_r	0.75	*fb		TR_TDR	0.01	ns					
c(0)	0.54		min	N	1000						
c(-1)	[-0.34:0.02:0]		[min:step:max]	TDR_Butterworth	1	logical					
c(-2)	[0:0.02:0.12]		[min:step:max]	beta_x	1.70E+09						
c(-3)	[-0.06:0.02:0]		[min:step:max]	rho_x	0.18						
c(1)	[-0.1:0.05:0]		[min:step:max]	fixture delay time	0	enter sec					
N_b	20	UI		Re	Receiver testing						
b_max(1)	0.85			RX_CALIBRATION	0	logical					
b_max(2N_b)	0.3			Sigma BBN step	5.00E-03	V					
g_DC	[-20:1:0]	dB	[min:step:max]	,	Noise, jitter						
f_z	21.25	GHz		sigma_RJ	0.01	UI					
f_p1	21.25	GHz		A_DD	0.02	UI					
f_p2	53.125	GHz		eta_0	8.20E-09	V^2/GHz					
g_DC_HP	[-6:1:0]		[min:step:max]	SNR_TX	33	dB					
f_HP_PZ	0.6640625	GHz		R_LM	0.95						
ffe_pre_tap_len	0	UI									
ffe_post_tap_len	0	UI									

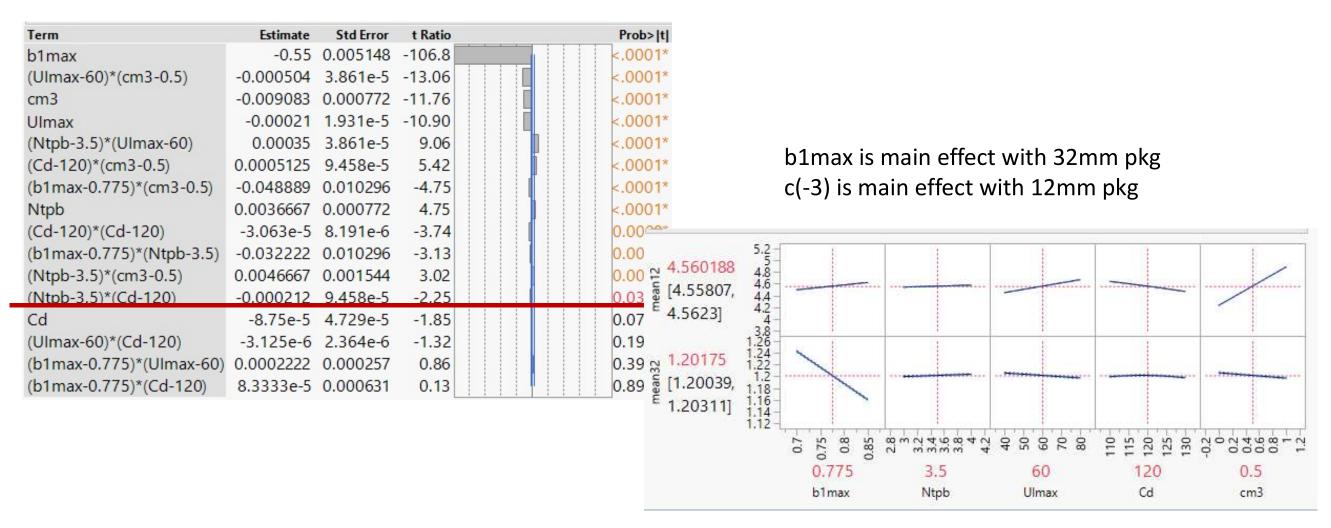
Used with version 2.58 of the tool

Distribution for Case 15 w/ Sub-29dB Channels

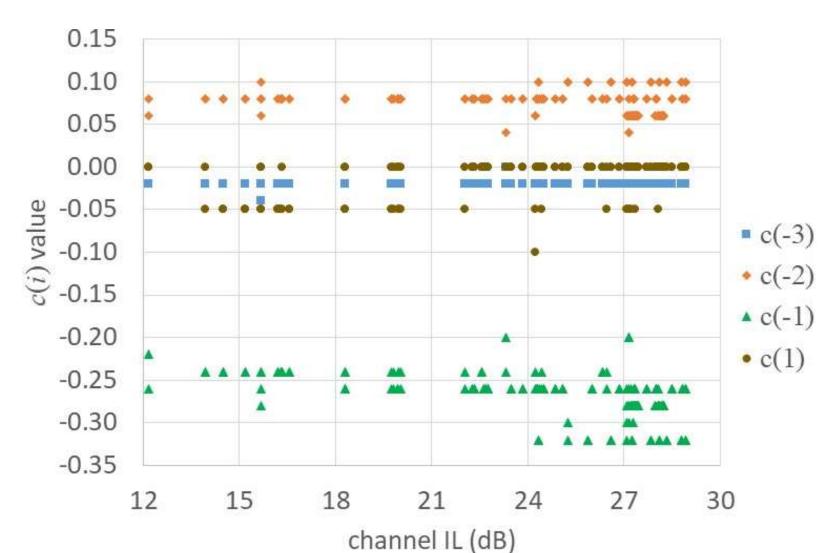


Parameter Significance 2

From fit of mean COM to the 5 variables



TxFFE5 Coefficients for sub-29dB Channels



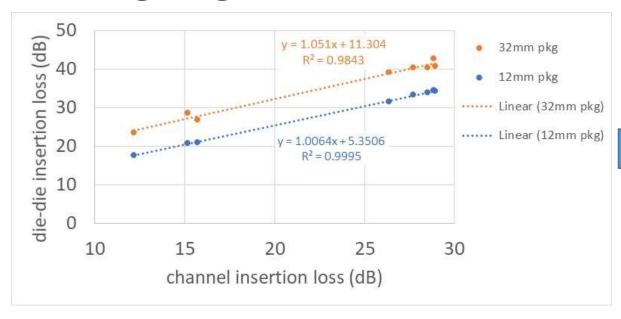
Includes coefficients obtained with 12 mm & 32 mm package routes.

Package 'Improvement' Analysis

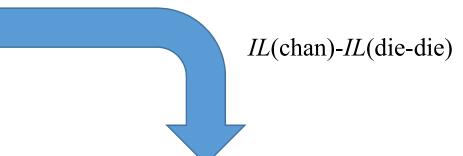
Highlighted Channel COM vs. Die-Die IL



Highlighted Channel Die-Die Insertion Loss



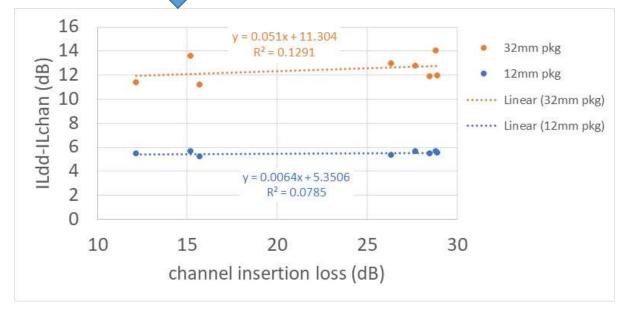
Two 32mm packages contribute ~11.3dB.



Estimated package contribution to insertion loss:

- 12mm pkg: 5.35dB/2 = 2.68dB
- 32mm pkg: 11.30dB/2 = 5.65dB

Estimated package trace loss ~0.15dB/mm

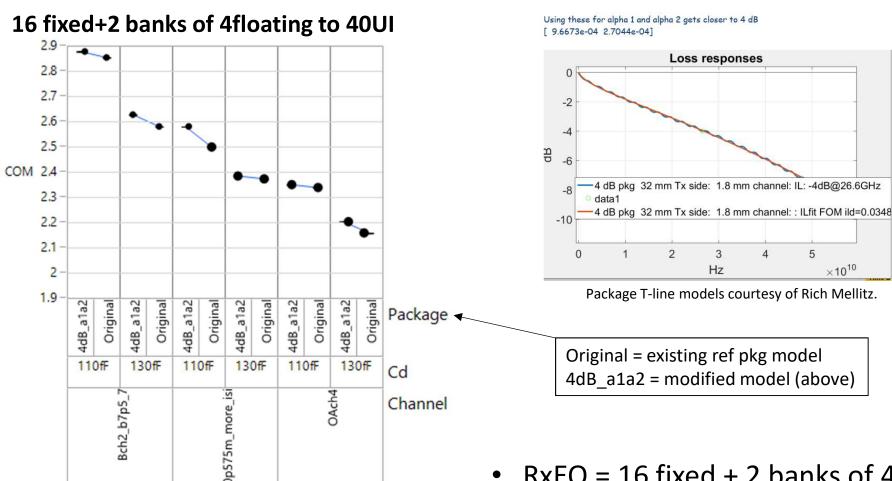


Reduced Reference Package Loss

2

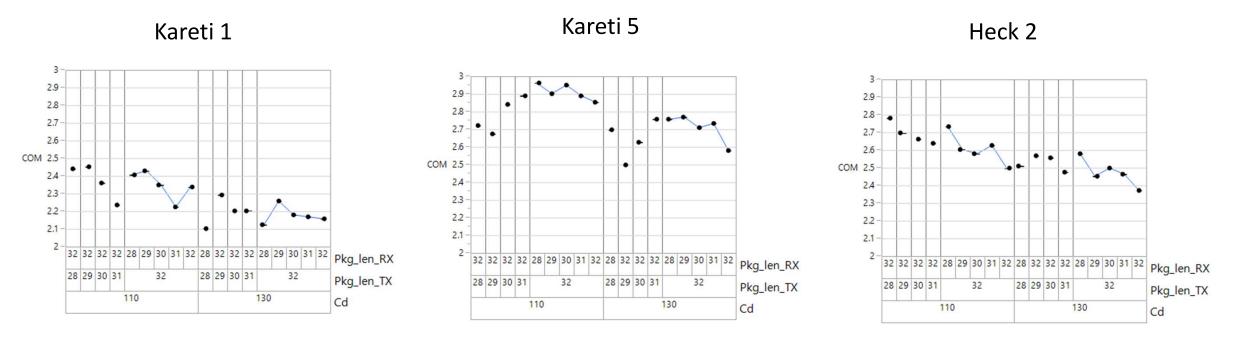
Kareti

Heck



- RxEQ = 16 fixed + 2 banks of 4 taps with 40UI span.
- Reducing package loss to 4dB gives <0.1dB COM benefit;
 not enough to close gap to 3dB COM.

Package Length Mismatch



- RxEQ: 16 fixed taps + 2 banks of 4 floating taps with 40UI span.
- Tx/Rx package length mismatch can give COM improvement, but not enough to close the gap to 3dB.