

# Summary - Straw Poll - Options - "Towards a baseline specification for 400GBASE-ZR transmitter clock phase jitter"

## Joint IEEE P802.3ct / P802.3cw Task Force Interim Meeting

John D'Ambrosia,  
Chair, IEEE P802.3ct / IEEE P802.3cw Task Forces  
Futurewei, U.S. Subsidiary of Huawei  
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# Strawpoll Summary (Doodle)

- **Strawpoll text –**

- *During the 17 Sept Joint TF Interim Mtg, Bo Zhang gave a presentation, entitled "Towards a baseline specification for 400GBASE-ZR transmitter clock phase jitter," which had three proposed options for developing a baseline targeting 400GBASE-ZR Tx clock phase jitter spec. We were going to do a straw poll on the group's thoughts about the different approaches on the 21 Sept Joint TF Interim, but the straw poll was inadvertently left off the agenda. This straw poll is to assess the .3cw TF's feelings towards the various approaches. [Please note that the presentation may be found at the 17 Sept Joint Interim Webpage ]*

- **Summary of Choices -**

- *Option A – Adopt traditional IEEE 802.3 Tx jitter specifications (J4u, JRMS, even-odd jitter) .....*
- *Option B – Leverage IEEE 802.3-2018 Clause 124 400GBASE-DR4 Tx quality metric for coherent Tx output.....*
- *Option C – Adopt existing OIF 400ZR IA Tx clock phase noise mask and jitter specs.....*
- *Option D – Do not support any of the Options in 1 - 3*
- *Option E – Need More Information*

## Results

- *Option A – 3*
- *Option B – 3*
- *Option C – 7*
- *Option D – 1*
- *Option E - 9*

Ref: <https://www.ieee802.org/3/ct/email/msg00502.html>