IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ FM	SC FM	P1	L <b>29</b>	# 490	C/ FM S	SC FM	P <b>12</b>	L <b>52</b>	# 493
Grow, Rob	pert	RMG Consul	ting		Grow, Robert		RMG Consulti	ing	
Comment	Туре Е	Comment Status D		bucket	Comment Typ	e E	Comment Status D		bucke
		dment order. It also lists five	previous amend	dments yet P802.3cx is	The descr	iption of 80	02.3cs does not agree with the t	ext in P802.3cs	/D3.2.
identifi	ied as Amendme	ent 5.			SuggestedRer	nedy			
Suggested	Remedy				Update for	r consisten	cy with P802.3cs/D3.2.		
		nbers are assigned for the ga			Proposed Res	ponse	Response Status W		
remain	n unchanged fror	n September, obviously use t n the last amendment numbe nendment number order.			PROPOSI	•	,		
Proposed	Response	Response Status W			C/FM S	SC FM	P13	L <b>8</b>	# 494
PROP	OSED ACCEPT	IN PRINCIPLE.			Grow, Robert		RMG Consult	ing	
Chara		0.14.000.			Comment Typ	e E	Comment Status D		bucke
IEEE S		300-202x, x, IEEE Std 802.3cs-202x, IE E Std 802.3dd-202x,	EE Std 802.3db	-202x, and IEEE Std			ords, P802.3db was designated ent 4 by Mr. Law on 25 January		and P802.3ck was
		x, IEEE Std 802.3db-202x, ar	nd IEEE Std 802	.3ck-202x"	SuggestedRer	nedy			
C/ FM	SC FM	P11	L17	# 491	Interchang	ge IEEE St	d 802.3db and IEEE Std 802.3d	k descriptions a	and numbers.
Grow, Rob	pert	RMG Consul	tina		Proposed Res	ponse	Response Status W		
Comment		Comment Status D	ling	bucket	PROPOSI	ED ACCEF	PT.		
The ch	nanges to the en in P802.3/D3.2.	d of this paragraph are incon	sistent with the o	current front matter as					
Update	e for consistency	with P802.3/D3.2.							
Proposed I PROP	Response OSED ACCEPT	Response Status W							
C/ FM	SC FM	P <b>12</b>	L <b>39</b>	# 492					
Grow, Rob	pert	RMG Consul	ting						
Comment	Туре Е	Comment Status D	C C	bucket					
The se P802.3		n is not consistent with the cu	irrent front matte	er as found in					
Suggested	Remedy								
	•	with P802.3/D3.2.							
Proposed	Response	Response Status W							
r roposeu .									

C/ FM SC FM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

3 SC 3.13.1.14 P23 L53 # 477	C/ 30 SC 30.13.1.13 P23 L22 # 476
Richard Microchip Technology	Tse, Richard Microchip Technology
nment Type T Comment Status D	Comment Type T Comment Status D
The limiting condition (based on DDMP capabilities of the PCS and DTE XS) on the configuration of the aTimeSyncSelectionDdmp management object needs to be added.	The special condition (per 45.2.3.67.1) when all DDMP capability registers 3.1800.12, 3.1800.13, 5.1800.12, and/or 5.1800.13 are zeros has to be included in the description of the article condition of the article condition.
ggestedRemedy	the aTimeSyncCapabilityDdmp management object.
Change:	SuggestedRemedy
"The registers 3.1813.13 and 5.1813.13 are expected to be set to the same value.;"	Change "The value of 'sfd' indicates that the registers 3.1800.13 and 5.1800.13 (see 45.2.3.67 a 45.2.5.28) are both set to 1."
to	to
"The registers 3.1813.13 and 5.1813.13 are expected to be set to the same value and can only be set to a value that corresponds to the capabilities of the PCS and DTE XS	"The value of 'sfd' indicates that the registers 3.1800.13 and 5.1800.13 (see 45.2.3.67 a
instances (see 45.2.3.69a.1 and 45.2.5.31.1).;"	45.2.5.28) are both set to 1 or that all registers 3.1800.12, 3.1800.13, 5.1800.12, and
oposed Response Response Status W	5.1800.13 are set to 0."
PROPOSED ACCEPT IN PRINCIPLE.	Proposed Response Response Status W
Change:	PROPOSED ACCEPT.
	C/ 30 SC 30.13.1.16 P25 L27 # 489
"The registers 3.1813.13 and 5.1813.13 are expected to be set to the same value.,"	Tse, Richard Microchip Technology
to	Comment Type T Comment Status D
"The registers 3.1813.13 and 5.1813.13 are expected to be set to the same value that	In the right-most column of Table 30-6, there should not be "X" for the new optional (i.e., non-mandatory for TImeSync) features.
corresponds to the capabilities of the PCS and DTE XS instances (see 45.2.3.69a.1 and 45.2.5.31.1).;"	SuggestedRemedy
	Remove the "X" for all the management objects below aTimeSyncDelayNsRXmin
	Proposed Response Response Status W PROPOSED ACCEPT IN PRINCIPLE.
	Add a new column to Table 30-6 "Support for Time Sync (optional)" and move X for all the management objects below aTimeSyncDelayNsRXmin to the new optional column.
	management objects below aTimeSyncDelayNsRXmin to the new optional column.
	management objects below aTimeSyncDelayNsRXmin to the new optional column. Cl 45 SC 45.2 P26 L4 # 539
	management objects below aTimeSyncDelayNsRXmin to the new optional column.         Cl       45       SC       45.2       P 26       L4       # 539         Grow, Robert       RMG Consulting       RMG Consulting       but the set of

COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SC 45.2 4/10/2022 7:54:39 PM SORT ORDER: Clause, Subclause, page, line

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

CI 45	SC 45.2.1.175	P <b>26</b>	L <b>32</b>	# 554	C/ 45	SC 45.2.1.176	P <b>27</b>	L <b>28</b>	# 540
Dawe, Pie	ers	Nvidia			Grow, Robert		RMG Consulti	ng	
Comment	Type E	Comment Status D		bucket	Comment Typ	e E	Comment Status D		bucke
Suggeste I won Proposed	dRemedy der if some or all of	a delay" 550 times and "dat the few "data path delay" s <i>Response Status</i> <b>W</b> I PRINCIPLE.			proposed response: Replace " a sentenc not preced	response, whic "Editors to ch Register" with ' e, is not part of ded by "(" as pa	esolution (#i-42) became mo ch may have been the basis ange the capitalization of re 'register" throughout the dra a phrase that is a proper mo art of a Clause 22 or Clause (Individual comments entered	for correcting P gister as follows ft where "Regis oun (e.g., a para 45 heading. All	2802.3de: Approved s: ter" is not at the start of ameter name), and is with editorial license."
Chan	ge all instances of "	data path delay" to "path d	ata delav". incluc	ling names of	SuggestedRei				
primit	ives, i.e., "PCS Dyn	amic Data Path Delay" bec			"register 1	.1"			
and "I	PDDPD" becomes "	PDPDD", globally.			Proposed Res	sponse	Response Status W		
C/ <b>45</b>	SC 45.2.1.175	P <b>26</b>	L <b>32</b>	# 475	PROPOS	ED ACCEPT.			
ſse, Rich	ard	Microchip Teo	chnology		C/ 45	SC 45.2.1.176	P <b>27</b>	L <b>32</b>	# 541
Comment	51	Comment Status D		bucket	Grow, Robert		RMG Consulti	-	
"data	path delay" should	be "path data delay"			Comment Typ	e E	Comment Status D		bucke
Total	of 13 instances of "	data path delay" in the draf	t. All should be	changed except	51	capitalization o			
(perha	aps) the two instanc	es related to the name of t	he PDDPD primi	itive.	SuggestedRei	•	0		
Suggeste	dRemedy				"(register				
from:	ge all instances (ex path delay"	cept possibly the two relate	d to the name of	f the PDDPD primitive)	Proposed Res		Response Status W		
to "path	data delay"				C/ 45	SC 45.2.1.176	P <b>27</b>	L <b>42</b>	# 542
Proposed	Response	Response Status W			Grow, Robert		RMG Consulti	ng	
PROF	POSED ACCEPT IN	PRINCIPLE.			Comment Typ		Comment Status D	-	bucke
	omment #554					capitalization o	i Register		
See c					SuggestedRei "(register				
See c					( 3				

C/ 45 SC 45.2.1.176

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ <b>45</b>	SC 45.2.1.177	P <b>28</b>	L38	# 543		C/ <b>45</b>	SC 45.2.1.17	P28	L <b>52</b>	# 495	
Grow, Rob		RMG Consultin		<i>"</i> 070		Grow, Rot		RMG Consulting		400	
Comment 1		Comment Status D	5		bucket	Comment		Comment Status D			bucket
Suggestedi "registe	•					S <i>uggested</i> "(regis	dRemedy ster 1.1812"				
Proposed F PROP(	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W			
CI 45	SC 45.2.1.177	P <b>28</b>	L <b>42</b>	# 544		C/ 45	SC 45.2.2.21	P <b>30</b>	L <b>49</b>	# 496	
Grow, Rob	ert	RMG Consultin	g			Grow, Rob	bert	RMG Consulting			
Comment 7 Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D f "Register"			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D			bucket
Suggested "registe	<i>Remedy</i> er 1.1805…"					S <i>uggested</i> "regist	dRemedy ter 2.1"				
Proposed F PROP	Response OSED ACCEPT.	Response Status W				,	Response POSED ACCEPT.	Response Status W			
C/ 45	SC 45.2.1.177	P <b>28</b>	L <b>43</b>	# 545		C/ 45	SC 45.2.2.21	P <b>30</b>	L <b>52</b>	# 497	
Grow, Rob	ert	RMG Consultin	g			Grow, Rob	bert	RMG Consulting			
Comment 7 Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D f "Register"			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"			bucket
Suggestedi "registe	<i>Remedy</i> er 1.1811…"					S <i>uggested</i> "regist	dRemedy ters 2.1891…"				
Proposed F PROP	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W			
C/ 45	SC 45.2.1.177	P <b>28</b>	L <b>51</b>	# 546		C/ <b>45</b>	SC 45.2.2.21	P <b>31</b>	L1	# 498	
Grow, Rob	ert	RMG Consultin	g			Grow, Rob	bert	RMG Consulting			
Comment 7 Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D f "Register"			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status <b>D</b> of "Register"			bucket
Suggestedi "(regist	<i>Remedy</i> ter 1.1807…"					S <i>uggested</i> "regist	dRemedy ter 2.1809…"				
Proposed F	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W			

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 C/ 45

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
 SC 45.2.2.21

 SORT ORDER: Clause, Subclause, page, line
 C/ 45
 C/ 45

Page 4 of 17 4/10/2022 7:54:39 PM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ <b>45</b>	SC 45.2.2.21	P <b>31</b>	L <b>8</b>	# 499	(	CI <b>45</b>	SC 45.2.2.22	P <b>32</b>	L6	# 503
Grow, Rob	pert	RMG Consultir	ng		(	Grow, Robe	ərt	RMG Consu	Ilting	
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization o	Comment Status D		bu	ucket C	Comment 7 Incorre	<i>Type</i> <b>E</b> ct capitalization	Comment Status D		buck
S <i>uggested</i> "(regis	IRemedy ters 2.1803…"				S	S <i>uggestedl</i> "(regist	R <i>emedy</i> er 2.1811…"			
Proposed PROP	Response OSED ACCEPT.	Response Status W			F	Proposed F PROPC	Response DSED ACCEPT.	Response Status W		
C/ <b>45</b>	SC 45.2.2.21	P <b>31</b>	L10	# 500		CI <b>45</b>	SC 45.2.2.22	P <b>32</b>	L13	# 504
Grow, Rob	pert	RMG Consultir	ng		(	Grow, Robe	ert	RMG Consu	Ilting	
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization o	Comment Status D		bi	ucket (	Comment 7 Incorre	<i>Type</i> <b>E</b> ct capitalization	Comment Status D of "Register"		buck
S <i>uggested</i> "(regis	<i>IRemedy</i> ter 1810…"				ક	S <i>uggestedl</i> "registe	Remedy ers 2.1805…"			
Proposed PROP	Response OSED ACCEPT.	Response Status W			F	Proposed F PROPC	Response DSED ACCEPT.	Response Status W		
CI <b>45</b>	SC 45.2.2.22	P <b>31</b>	L1	# 501		C/ <b>45</b>	SC 45.2.2.22	P <b>32</b>	L15	# 505
Grow, Rob	pert	RMG Consultir	ng		(	Grow, Robe	ərt	RMG Consu	Ilting	
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization o	Comment Status D		bi	ucket C	Comment 7 Incorre	<i>Type</i> <b>E</b> ct capitalization	Comment Status D		buck
S <i>uggested</i> "regist	<i>IRemedy</i> er 2.1"				ક	S <i>uggestedl</i> "(regist	Re <i>medy</i> er 2.1812…"			
Proposed PROP	Response OSED ACCEPT.	Response Status W			F	Proposed F PROPC	Response DSED ACCEPT.	Response Status W		
CI <b>45</b>	SC 45.2.2.22	P <b>32</b>	L <b>4</b>	# 502						
Grow, Rob	pert	RMG Consultir	ng							
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization o	Comment Status D		bu	ucket					
S <i>uggested</i> "regist	<i>IRemedy</i> ers 2.1805…"									
Proposed	Response OSED ACCEPT.	Response Status W								

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general COMMENT STATUS: D/dispatched A/accepted R/rejected RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn SORT ORDER: Clause, Subclause, page, line

C/ **45** SC **45.2.2.22**  Page 5 of 17 4/10/2022 7:54:39 PM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ 45 SC 45.2.	3.67.1	P <b>34</b>	L <b>33</b>	# 478	Cl <b>45</b>	SC	45.2.3.67.3	P	35	L <b>3</b>	# 563	
Tse, Richard		Microchip Te	chnology		Dawe, Pie	ers		Nvic	lia			
Comment Type T	Comm	ent Status D			Comment	Туре	Е	Comment Statu	s D			bucket
path data delays.				e transmit and receive				upports the measu using the method				Ł
The statements in delay.	45.2.3.67.1 an	nd 45.2.3.67.2 only	mention the PCS	S transmit path data	Suggeste	dRemed	dy					
The statements in delay.	45.2.5.28.1 an	nd 45.2.5.28.2 only	mention the DTE	XS transmit path data	PCS I	anes us		able to report tran thod described in			ta delays for mu	ltiple
SuggestedRemedy Change the four in to "PCS path data		CS transmit path da	ata delay" in 45.2	.3.67.1 and 45.2.3.67.2	Proposed	Respor	•	Response Status	w			
Change the four in 45.2.5.28.2 to "DT			h data delay" in 4	5.2.5.28.1 and	<i>Cl</i> <b>45</b> Dawe, Pie		45.2.3.67.4	P Nvic	<b>35</b> lia	L11	# 555	
Proposed Response	Respon	nse Status W			Comment		Е	Comment Statu				bucket
PROPOSED ACC	EPT.				indica	tes that	_	upports the calculation		ne TX_NUM_BIT_	_CHANGE and	buonor
C/ 45 SC 45.2.	3.67.2	P <b>34</b>	L <b>48</b>	# 480	Suggeste	_ dRemed	_ dv					
Tse, Richard		Microchip Te	chnology		••		-	able to report PD	DPD as <sup>-</sup>	ГХ NUM ВІТ СІ	HANGE and	
Comment Type T Register bit 3.1800		ent Status <b>D</b> referenced here ir	nstead of 3.1800.	13.	RX_N	UM_BI	T_CHANGE					
SuggestedRemedy					Proposed	Respor	nse	Response Status	w			
Change					PROF	POSED	ACCEPT IN	N PRINCIPLE.				
"When read as a z	ero, bit 3.1800	0.13 indicates that	the PCS"					indicates that the and RX_NUM_B			OPD as	
to												
"When read as a z	ero, bit 3.1800	0.12 indicates that	the PCS"									
Proposed Response		nse Status W										

PROPOSED ACCEPT.

C/ 45 SC 45.2.3.67.4

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ <b>45</b> SC	C 45.2.3.67.4	P <b>35</b>	L12	# 556	CI 45 S	SC <b>45.2.3.68</b>	P <b>36</b>	L16	# 508
Dawe, Piers		Nvidia			Grow, Robert		RMG Consulti	ing	
		Comment Status <b>D</b> together: reporting PDDI S.	PD, and doing it	over xMII using		capitalization of "F	Comment Status D Register"		bucket
SuggestedRem Should ther	•	gisters for each ability?			SuggestedRer "(register : Proposed Res	3.1809"	esponse Status W		
Proposed Resp PROPOSEI	onse Ri D ACCEPT IN P	esponse Status W RINCIPLE.			PROPOSI	ED ACCEPT.			
		PD and NUM_BIT_CHAN ion to work. Also, the PI			Grow, Robert	SC 45.2.3.68	P <b>36</b> RMG Consulti Comment Status <b>D</b>	L <b>23</b> ing	# 509
NUM_BIT_( 3.1800.10 ir and RX_NU <new text=""> TS_TX.indic When read</new>	CHANGE ability ndicates that the JM_BIT_CHANG The gRS also si cation and TS_R as a zero, bit 3.	dd a reference to the PE register's description as PCS supports the calcu E values, passed from th upports the correspondin X.indication primitives. 1800.10 indicates that th<br IGE and RX_NUM_BIT_(	shown: "When r lation of the TX_ he PCS across t g PDDPD paran new text> e PCS does not	read as a one, bit NUM_BIT_CHANGE he xMII to the gRS. neter in its support the calculation	SuggestedRer "registers Proposed Res	capitalization of "F <i>medy</i> 3.1803"			bucket
CI <b>45</b> SC	C 45.2.3.68	P36	L11	# 506	C/ 45 S	SC 45.2.3.68	P <b>36</b>	L <b>25</b>	# 510
Grow, Robert		RMG Consulti Comment Status D	ng	bucket	Grow, Robert Comment Typ	e E C	RMG Consulti Comment Status <b>D</b>	ng	bucket
Comment Type Incorrect ca	<b>E</b> C Apitalization of "F			DUCKEI		capitalization of "F	Register"		
SuggestedRem register 3.1	-				SuggestedRer (register :	•			
Proposed Resp PROPOSEI	oonse Ri D ACCEPT.	esponse Status W			Proposed Res PROPOSI	ponse R ED ACCEPT.	esponse Status W		
	C 45.2.3.68	P36	L14	# 507	C/ 45 S Grow, Robert	SC 45.2.3.69	P <b>37</b> RMG Consulti	L <b>13</b>	# 511
Grow, Robert Comment Type Incorrect ca	E C	RMG Consulti Comment Status D Register"	ng	bucket	Comment Typ Incorrect o	e E C capitalization of "F	Comment Status D		bucket
SuggestedRem	nedy				SuggestedRer register 3	•			
"(registers 3					Proposed Res		esponse Status W		

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 C/ 45

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
 SC 45.2.3.69

 SORT ORDER: Clause, Subclause, page, line
 SC 45.2.3.69
 SC 45.2.3.69

Page 7 of 17 4/10/2022 7:54:39 PM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ <b>45</b>	SC 45.2.3.69	P <b>37</b>	L16	# 512		C/ <b>45</b>	SC 45.2.4.29	P <b>39</b>	L <b>52</b>	# 516
Grow, Rob		RMG Consulting	g			Grow, Rol		RMG Consulting		
Comment T Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		buck
Suggested "registe	<i>Remedy</i> ers 2.1807…"					S <i>uggested</i> "(regis	dRemedy sters 4.1809…"			
Proposed I PROP	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT	Response Status W		
C/ 45	SC 45.2.3.69	P <b>37</b>	L <b>27</b>	# 513		C/ 45	SC 45.2.4.29	P <b>40</b>	L <b>6</b>	# 517
Grow, Rob	ert	RMG Consulting	g			Grow, Rol	bert	RMG Consulting		
Comment T Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D of "Register"			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		buck
Suggested "(regist	<i>Remedy</i> ter 3.1812…"					Suggested "(regis	dRemedy sters 4.1803…"			
Proposed I PROP	Response OSED ACCEPT.	Response Status W				•	Response POSED ACCEPT	Response Status W		
C/ 45	SC 45.2.4.29	P <b>39</b>	L <b>47</b>	# 514		C/ 45	SC 45.2.4.30	P <b>40</b>	L <b>48</b>	# 518
Grow, Rob	ert	RMG Consulting	g			Grow, Rol	bert	RMG Consulting		
Comment T Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D			bucket	Comment Incorr	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		buck
Suggested "registe	<i>Remedy</i> er 4.1…"					S <i>uggested</i> "regist	dRemedy ter 4.0"			
Proposed I PROP	Response OSED ACCEPT.	Response Status W				•	Response POSED ACCEPT	Response Status W		
C/ <b>45</b>	SC 45.2.4.29	P <b>39</b>	L <b>5</b> 1	# 515		C/ 45	SC 45.2.4.30	P <b>40</b>	L <b>52</b>	# 519
Grow, Rob	ert	RMG Consulting	g			Grow, Rol	bert	RMG Consulting		
Comment T Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D			bucket	Comment Incorr	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		buck
Suggested "(regist	<i>Remedy</i> ters 4.1801…"					Suggested "(regis	dRemedy sters 4.1805…"			
Proposed I	Response	Response Status W				Proposed	Response	Response Status W		

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 C/ 45

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
 SC 45.2.4.30

 SORT ORDER: Clause, Subclause, page, line
 SC
 SC 45.2.4.30

Page 8 of 17 4/10/2022 7:54:39 PM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ 45 SC 45.2.4.30	P <b>40</b>	L <b>53</b>	# 520	C/ 45 SC 45.2.5.28.2 P43 L36 # 479
Grow, Robert	RMG Consultir			Tse, Richard Microchip Technology
Comment Type E Incorrect capitalization	Comment Status D	.9	bucket	Comment Type <b>T</b> Comment Status <b>D</b> Register bit 5.1800.12 should be referenced here instead of 5.1800.13.
SuggestedRemedy "(register 4.1811"				SuggestedRemedy Change
Proposed Response PROPOSED ACCEPT.	Response Status W			"When read as a zero, bit 5.1800.13 indicates that the DTE XS"
C/ 45 SC 45.2.4.30	P <b>41</b>	L <b>7</b>	# 521	to
Grow, Robert	RMG Consultir			"When read as a zero, bit 5.1800.12 indicates that the DTE XS"
Comment Type E Incorrect capitalization	Comment Status D	.9	bucket	Proposed Response Response Status W PROPOSED ACCEPT.
SuggestedRemedy	-			C/ 45 SC 45.2.5.28.3 P43 L46 # 481
"registers 4.1807"				Tse, Richard Microchip Technology
Proposed Response PROPOSED ACCEPT.	Response Status W			Comment Type E Comment Status D bucket "PCS" should be replaced by "DTE XS" in 45.2.5.28.3, 45.2.5.28.4, and 45.2.5.31.
C/ 45 SC 45.2.4.30 Grow, Robert	P <b>41</b> RMG Consultir	L <b>9</b>	# 522	SuggestedRemedy Replace six instances of "PCS" with "DTE XS" in 45.2.5.28.3, 45.2.5.28.4, and 45.2.5.31.
Comment Type E Incorrect capitalization	Comment Status D	IJ	bucket	Proposed Response Response Status W PROPOSED ACCEPT.
SuggestedRemedy				C/ 45 SC 45.2.5.29 P44 L37 # 523
"(register 4.1812"				Grow, Robert RMG Consulting
Proposed Response PROPOSED ACCEPT.	Response Status W			Comment Type E Comment Status D bucket
				SuggestedRemedy "register 5.0"
				Proposed Response Response Status W PROPOSED ACCEPT.

C/ **45** SC **45.2.5.29** 

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

CI <b>45</b>	SC 45.2.5.29	P <b>44</b>	L <b>41</b>	# 524		C/ <b>45</b>	SC 45.2.5.29	P <b>44</b>	L <b>51</b>	# 527
Grow, Rol		RMG Consult	ng			Grow, Rol	bert	RMG Consulting	1	
Comment Incorr	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		bucke
S <i>uggester</i> "(regis	dRemedy sters 5.1801…"					S <i>uggested</i> "(regis	dRemedy ster 5.1810…"			
	Response POSED ACCEPT.	Response Status W				•	Response POSED ACCEPT.	Response Status W		
CI <b>45</b>	SC 45.2.5.29	P <b>44</b>	L <b>42</b>	# 525		C/ <b>45</b>	SC 45.2.5.30	P <b>45</b>	L <b>41</b>	# 528
Grow, Rol	pert	RMG Consult	ng			Grow, Rol	bert	RMG Consulting	1	
Comment Incorr	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D of "Register"			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		bucke
Suggested "(regis	dRemedy ster 5.1809…"					S <i>uggested</i> "regist	dRemedy ter 5.1…"			
	Response POSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W		
C/ 45	SC 45.2.5.29	P <b>44</b>	L <b>42</b>	# 482		C/ <b>45</b>	SC 45.2.5.30	P <b>45</b>	L <b>44</b>	# 529
Tse, Rich	ard	Microchip Tec	hnology			Grow, Rol	bert	RMG Consulting	1	
Comment "PHY		Comment Status <b>D</b> laced by "DTE XS" in 45.2.5	.29		bucket	Comment Incorr	<i>Type</i> <b>E</b> ect capitalization	Comment Status <b>D</b> of "Register"		bucke
S <i>uggested</i> Repla		"PHY XS" with "DTE XS" in	45.2.5.29			S <i>uggested</i> "regist	dRemedy ters 5.1805…"			
	Response POSED ACCEPT.	Response Status W				•	Response POSED ACCEPT.	Response Status W		
C/ 45	SC 45.2.5.29	P <b>44</b>	L <b>50</b>	# 526		C/ 45	SC 45.2.5.30	P <b>45</b>	L <b>46</b>	# 530
Grow, Rol	pert	RMG Consult	ng			Grow, Rol	bert	RMG Consulting	1	
Comment Incorr	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D			bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"		bucke
Sugaester	dRemedy					S <i>uggested</i> "(regis	dRemedy ster 5.1811…"			
	sters 5,1803…"					( 0				

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 C/ 45

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
 SC 45.2.5.30

 SORT ORDER: Clause, Subclause, page, line
 SC
 SC 45.2.5.30

Page 10 of 17 4/10/2022 7:54:39 PM

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

Cl <b>45</b>	SC 45.2.5.30	P <b>45</b>	L <b>53</b>	# 504		C/ <b>45</b>	SC 45.2.6.15	P48	L <b>46</b>	# 505	<u>1</u>
Grow, Rob		P45 RMG Consulting		# 531		Grow, Rol		P48 RMG Consulting	L40	# 535	
Comment		Comment Status D	9	k	bucket	Comment		Comment Status D			bucket
S <i>uggested</i> "(regis	IRemedy ters 5.1807…"					S <i>uggested</i> "(regis	dRemedy ster 6.1810…"				
Proposed PROP	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W			
C/ 45	SC 45.2.5.30	P <b>46</b>	L1	# 532		C/ 45	SC 45.2.6.16	P <b>49</b>	L <b>36</b>	# 536	
Grow, Rob	pert	RMG Consulting	g			Grow, Rol	pert	RMG Consulting			
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D		Ł	oucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D of "Register"			bucket
Suggested "(regis	<i>IRemedy</i> ter 5.1812"					S <i>uggested</i> "regist	dRemedy ter 6.0…"				
Proposed PROP	Response OSED ACCEPT.	Response Status W				-	Response POSED ACCEPT.	Response Status W			
CI <b>45</b>	SC 45.2.6.15	P <b>48</b>	L <b>37</b>	# 533		C/ 45	SC 45.2.6.16	P <b>49</b>	L <b>39</b>	# 537	
Grow, Rob	pert	RMG Consulting	g			Grow, Rol	pert	RMG Consulting			
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D		k	bucket	Comment Incorre	<i>Type</i> <b>E</b> ect capitalization	Comment Status D			bucket
Suggested "(regis	<i>IRemedy</i> ter 6.1809…"					S <i>uggested</i> "regist	dRemedy ters 6.1805…"				
Proposed PROP	Response OSED ACCEPT.	Response Status W				-	Response POSED ACCEPT.	Response Status W			
C/ <b>45</b>	SC 45.2.6.15	P <b>48</b>	L <b>44</b>	# 534		C/ <b>45</b>	SC 45.2.6.16	P <b>49</b>	L <b>41</b>	# 538	
Grow, Rob	pert	RMG Consulting	g			Grow, Rol	bert	RMG Consulting			
Comment Incorre	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D		Ł	oucket	Comment Incorr	<i>Type</i> <b>E</b> ect capitalization	Comment Status D			bucket
S <i>uggested</i> "(regis	IRemedy ters 6.1803…"					Suggested "(regis	dRemedy ster 6.1811…"				
Proposed	Response OSED ACCEPT.	Response Status W					Response POSED ACCEPT.	Response Status W			

 TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
 C/ 45

 COMMENT STATUS: D/dispatched A/accepted R/rejected
 RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
 SC 45.2.6.16

 SORT ORDER: Clause, Subclause, page, line
 SC
 SC 45.2.6.16

Page 11 of 17 4/10/2022 7:54:39 PM

Cl <b>45</b>	SC 45.2.6.16	P <b>49</b>	L <b>48</b>	# 547	C/ 90	SC 90.5.4	P <b>60</b>	L <b>38</b>	# 484
Grow, Ro	bert	RMG Consulti	ing		Tse, Richard		Microc	hip Technology	
Comment Incorr	<i>Type</i> <b>E</b> ect capitalization c	Comment Status D		bucket	Comment Typ "number o		Comment Status	D	
00	dRemedy sters 6.1807…"				C C		uld be "PHY to the gRS	5"	
Proposed	Response POSED ACCEPT.	Response Status W			SuggestedRe Change	medy			
CI 45	SC <b>45.2.6.16</b>	P <b>49</b>	L <b>50</b>	# 548			nber of bits of dynamic I from the gRS to the P		elay that are experienced
Grow, Ro	bert	RMG Consulti	ing		to				
	ect capitalization of	Comment Status D of "Register"		bucket			nber of xMII bit times o ta transferred from the		ath data delay that are
	dRemedy sters 6.1812…"				Proposed Res	sponse ED ACCEPT	Response Status	w	
'	Response POSED ACCEPT.	Response Status W				SC 90.7	Р63	L <b>4</b>	# 557
C/ 90	SC 90.5.3	P60	L11	# 483	Dawe, Piers		Nvidia		
rse, Rich		Microchip Tec		# 405	Comment Typ		Comment Status		
Comment "numt	<i>Type</i> <b>T</b> per of bits" needs f dRemedy	Comment Status D	anology		of data de measurer beginning from the b	elay in the tra ment point sh of the first s base standar	nsmit and receive path all be either the beginr ymbol after the SFD (s	is, as shown in Figur ing of the start of fra ee 45.2.3.69a)". The between two points,	ity requires measuremen e 90–5. The data delay me delimiter (SFD) or the e figure is unchanged the bottom of the gRS
					SuggestedRe	medy			
		per of bits of dynamic transm rom the gRS to the PHY"	nit path data de	ay that are experienced	this from this from the thick the	the real point be better to ch	marker in a signal that is in static space, which nange "data delay mea marker" or "data delay	n are also relevant to surement point" to "d	this clause. lata delay reference
"The v	alue reports numb	per of xMII bit times of dynan	nic transmit nat	h data delav that are	Proposed Res		Response Status	-	001.
		transferred from the gRS to		וו טמומ טכומץ ווומו מוכ	•	ED REJECT	•	**	
Proposed	Response	Response Status W							
PROF	POSED ACCEPT.				timestam	p point", whic	measurement point" is h is used by IEEE 158 he draft needed.		

CI 90 SC 90.7 Page 12 of 17 4/10/2022 7:54:39 PM

### IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ 90	SC 90.7	P <b>63</b>	L <b>6</b>	# 558
Dawe, Pi	iers	Nvidia		

#### Comment Type **TR** Comment Status **D**

This proposes to change the base text to: "The data delay measurement point shall be either the beginning of the start of frame delimiter (SFD) or the beginning of the first symbol after the SFD (see 45.2.3.69a)".

I checked clauses 3 and 4: the SFD field is 1 octet long (Clause 3) or 8 MAC bits long (Clause 4), and the SFD field and the Destination Address field which follows it are "fields". I checked a couple of RS clauses - they don't have "symbol"s. But see the definitions 1.4.545 symbol, 1.4.546 symbol period, 1.4.547 symbol rate (SR), and 1.4.548 symbol time (ST). So a symbol is a unit interval on the line, which doesn't relate simply to MAC octets at the gRS because of line coding overhead, multilevel coding, FEC, and alignment markers. Also, there are 10-bit symbols in Reed-Solomon FEC clauses.

To the same text: the regular clauses are responsible for specifying; Clause 45 MDIO is only an optional way of implementing it.

I see that 1.5 says "SFD start-of-frame delimiter" and "3.2.2 Start Frame Delimiter (SFD) field The SFD field is..."

#### SuggestedRemedy

I believe what is meant is "octet" as used in Clause 3 and 90A.3 or "8 Change this to:

"the beginning of the Start Frame Delimiter field (SFD) or the beginning of the first field after the SFD (see Figure 3-1) An implementation may be capable of one or both methods; this may be advertised and configured with MDIO registers (the beginning of the start of frame delimiter (SFD) or the beginning of the first symbol after the SFD (see 45.2.3.67 and 45.2.3.69a)"

Replace "symbol" with "field" throughout the document. It seems it is used as "the first symbol after the SFD" so we don't need to discuss the duration of this field, only when it starts.

### Proposed Response Response Status W

PROPOSED REJECT.

The timestamping of the data delay measurement point is supposed to take place at the MDI so "symbol" is correct, per the definitions given by the commenter.

The The capturing of the timestamp at the xMII is just an implementation model used by 802.3 to enable estimation of the timestamp at the MDI.

No changes to the draft needed.

CI 90	SC 90.7	P <b>63</b>	L15	# 559
Dawe. Pier	rs	Nvidia		

Comment Type T Comment Status D

Confusion between points and events.

Also, the delay exists whether measured or not.

#### SuggestedRemedy

#### Change

The transmit path data delay is measured from the data delay measurement point at the xMII input to the data delay measurement point at the MDI output. The receive path data delay is measured from the data delay measurement point at the MDI input to the data delay measurement point at the xMII output.

#### to

The transmit path data delay is defined from the time the data delay measurement point passes the xMII input to the time {it | the data delay measurement point} passes the MDI output. The receive path data delay is measured from the time the data delay measurement point passes the MDI input to the time it passes the xMII output.

Check the document for other occurrences of "data delay measurement point" when an event is meant, such as at line 38 (suggestion in another comment).

Proposed Response Response Status W

PROPOSED ACCEPT IN PRINCIPLE.

#### Change

The transmit path data delay is measured from the data delay measurement point at the xMII input to the data delay measurement point at the MDI output. The receive path data delay is measured from the data delay measurement point at the MDI input to the data delay measurement point at the xMII output.

#### to

The transmit path data delay is defined from the time the data delay measurement point passes the xMII input to the time it passes the MDI output. The receive path data delay is measured from the time the data delay measurement point passes the MDI input to the time it passes the xMII output.

Apply similar change on page 63, line 38.

C/ 90 SC 90.7

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ 90	SC	90.7	P63	L18	# 566	C/ 90	SC	90.7	P <b>63</b>	L29	# 560	
Dawe, Pier	s		Nvidia			Dawe, Pie	ers		Nvidia			
Comment T	Гуре	Е	Comment Status D		bucket	Comment	Туре	т	Comment Status D			
parse, o	could r	mean mul	es an FEC and/or multiple PC tiple PCSs or multiple function tiple PCS lanes. How many f	ns. We don't ha	ve PCS lane	point a	at the x	MII input)			-	
Suggested		ly					ceive pa xMII ou		delay measurement ending po	oint (the data dela	ay measurement p	oint
Change For a P		at include:	s an FEC and/or a PCS lane o	distribution funct	ion	Suggestee	dRemed	dy				
Similarl For PH	ly, cha Ys with	nge h both FE	C and multiple PCS lane distr with the start of a multiple PC	ibution, the star	t of the FEC block is				delay measurement starting e sses the xMII input)	event (when the	data delay	
to For PH	Ys with	h both FE	C and PCS lane distribution, t	the start of the F	EC block is			ath data o the xMII	delay measurement ending ev output)	ent (when the da	ata delay measurer	ment
U			with the start of a PCS lane of	listribution sequ	ence.	Proposed	Respor	nse	Response Status W			
Proposed F PROPC	•	nse ACCEPT.	Response Status W			PROF	POSED	ACCEPT				
~ ~ ~		~~ -		1.04	"	C/ 90	SC	90.7	P <b>64</b>	L <b>42</b>	# 485	
C/ 90	SC	90.7	P <b>63</b>	L <b>21</b>	# 567	Tse, Rich	ard		Microchip Teo	chnology		
Dawe, Pier		_	Nvidia			Comment	Туре	т	Comment Status D			
Comment 7 FEC b got "blo	lock":		Comment Status <b>D</b> nat you mean, but in 802.3 it's	"FEC codeword	<i>bucket</i> I" because 64B/66B	existe		nanoseco	ata delays are no longer repor nd and optional sub-nanosec			
Suggested	Remea	lv				Suggestee	dRemed	dv				
Change	e "bloc	k" to "cod	leword", three times			Chang						
Proposed F PROPC		nse ACCEPT.	Response Status W			the m data c	aximum lelay, aı	n transmit	lay measurement shall be rep data delay, the minimum trar nimum receive data delay, as "	nsmit data delay,	the maximum rece	eive
						to						
						the m data c corres	aximum lelay, ai sponding	n transmit nd the mi g manage	lay measurement shall be rep data delay, the minimum trar nimum receive data delay, ea ed objects with nanosecond re n, as defined for the oTimeSyr	nsmit data delay, ch of which can esolution and, op	the maximum rece be derived from tionally, also with s	eive sub-

Proposed Response Response Status W

PROPOSED ACCEPT.

Page 14 of 17 4/10/2022 7:54:39 PM

# IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/90A SC	C 90A.2	P68	L <b>31</b>	# 565	C/ 90A	SC 90A.3	P69	L <b>8</b>	# 561
Dawe, Piers		Nvidia			Dawe, Piers	6	Nvidia		
Comment Type	Е	Comment Status D		bucket	Comment T	ype E	Comment Status D		bucket
		ng sublayer (PCS) lane distr			Gratuito	ous capitals in	table		
		s from the draft, we aren't di have lane distribution/mergi			Suggested	Remedy			
Capitals.					Etherne				
SuggestedReme	edy						I timestamp accuracy impairm ay measurement point	ents per transm	it or receive port (ns)
		l Coding Sublayer (PCS) lar g of multiple Physical Coding			Idle inse	ertion / remova		oval	
Proposed Respo	onse	Response Status W				ne distribution	/ merging		
PROPOSED	O ACCEPT I	N PRINCIPLE.			Proposed R	lesponse	Response Status W		
Change to re	aad "and Ph	ysical Coding Sublayer (PC	S) lane distributio	on/merging"	PROPC	SED ACCEP	Т.		
-			,		C/ 90A	SC 90A.3	P <b>69</b>	L <b>44</b>	# 549
C/ 90A SC	C 90A.3	P <b>68</b>	L <b>5</b> 1	# 562	Dawe, Piers	5	Nvidia		
Dawe, Piers	_	Nvidia			Comment T	ype E	Comment Status D		bucker
Comment Type		Comment Status D					Comment Status <b>D</b> support sub-nanosecond accu	racy path delay	
Comment Type "For impleme	entations the	Comment Status <b>D</b> at do not use the NUM_BIT_				entations that		racy path delay	
Comment Type "For impleme registers (se uses MDIO t	nentations the ee Table 45-2 to report the	Comment Status D	is not whether th	he implementation	Implem capabili S <i>uggestedF</i>	entations that ties Remedy	support sub-nanosecond accu		measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita	nentations the ee Table 45- to report the tal.	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion	is not whether th	he implementation	Implem capabili S <i>uggestedF</i>	entations that ties Remedy			measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme	nentations the se Table 45-: to report the tal. edy	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is	is not whether the using the abilitie	he implementation es themselves.	Implem capabili S <i>uggestedF</i>	entations that ties Remedy entations with	support sub-nanosecond accu		measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "li	nentations the se Table 45- to report the tal. edy If the NUM_	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed F	entations that ties Remedy entations with	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b>		measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "li	nentations the se Table 45 to report the tal. edy If the NUM_ 5.4, Table 45-	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed F	entations that ties Remedy entations with Response	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b>		measurement
Comment Type "For impleme registers (set uses MDIO t Rogue capita SuggestedReme Change to "I 90.5.3, 90.5.	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili SuggestedF Implem Proposed F PROPC	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T.	ath data delay re	measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "It 90.5.3, 90.5. Proposed Respo	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed R PROPC C/ <b>90A</b>	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T. <i>P</i> <b>69</b>	ath data delay re	measurement
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "It 90.5.3, 90.5. Proposed Respo	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed R PROPC Cl 90A Dawe, Piers Comment T	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3 S Sype E	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T. <i>P</i> <b>69</b> Nvidia	uth data delay re	measurement eporting abilities # <u>550</u>
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "It 90.5.3, 90.5. Proposed Respo	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed R PROPC Cl 90A Dawe, Piers Comment T	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3 S Sype E ifer? rather that	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T. <i>P</i> 69 Nvidia <i>Comment Status</i> <b>D</b>	uth data delay re	measurement eporting abilities # <u>550</u>
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "It 90.5.3, 90.5. Proposed Respo	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed R PROPO Cl 90A Dawe, Piers Comment T only sut Suggestedf Change	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3 S Sype E ifer? rather the Remedy e "only suffer a	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T. <i>P</i> 69 Nvidia <i>Comment Status</i> <b>D</b>	th data delay re <i>L</i> 46	measurement eporting abilities # <u>550</u> bucket
Comment Type "For impleme registers (se uses MDIO t Rogue capita SuggestedReme Change to "It 90.5.3, 90.5. Proposed Respo	entations the ee Table 45-: to report the tal. <i>edy</i> If the NUM_ 5.4, Table 45 onse	Comment Status <b>D</b> at do not use the NUM_BIT_ 293)": I suspect the criterion se abilities, but whether it is BIT_CHANGE ability and m -293, and Table 45-295a)"	is not whether the using the abilitie	he implementation es themselves.	Implem capabili Suggestedf Implem Proposed R PROPO Cl 90A Dawe, Piers Comment T only sut Suggestedf Change	entations that ties Remedy entations with Response DSED ACCEP SC 90A.3 S Sype E ifer? rather the Remedy of only suffer a mp accuracy in	support sub-nanosecond accu sub-nanosecond resolution pa <i>Response Status</i> <b>W</b> T. <i>P</i> 69 Nvidia <i>Comment Status</i> <b>D</b> an pay a penalty or be disquali	th data delay re <i>L</i> 46	measurement eporting abilities # <u>550</u> bucket

C/ 90A SC 90A.3

IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

C/ 90A	SC 90A.4	P <b>70</b>	L <b>4</b>	# 553	C/ 90A SC 90A.5.	1 P <b>70</b>	L <b>53</b>	# 486
Dawe, Pie	S	Nvidia			Tse, Richard	Microchip Te	chnology	
Comment	Type <b>TR</b>	Comment Status D			Comment Type T	Comment Status D		
	the first mention ic" means.	of "intrinsic delay variation" a	and I don't see	an explanation of what	PDDPD is a primitiv	e, not a parameter		
Suggested	Remedy				Make the same cha	nge to both 90A.5.1 and 90A.5.1	2	
Explair	n or delete. It ap	pears that anything "intrinsic"	' is a delay varia	ation or a varying delay,	SuggestedRemedy			
so dele	ete may work.		-		change the following	in both 90A.5.1. and 90A.5.2:		
Proposed I PROP	Response OSED REJECT.	Response Status W			"in which the PDD	PD parameter"		
					to			
adjecti	ve emphasizes th	is used in the meaning of "b nat certain types of delays an y implementation-dependent	e intrinsic to a s		"in which the PDD	PD primitive"		
					Proposed Response	Response Status W		
CI 90A	SC 90A.5.1	P <b>70</b>	L <b>52</b>	# 564	PROPOSED ACCE	PT IN PRINCIPLE.		
Dawe, Pie	S	Nvidia			See comment #564			
Comment	Туре Т	Comment Status D				8		
		, which mirrors the correspor E": but it's the other way rour			C/ 90A SC 90A.6	P <b>72</b>	L12	# 487
_		DDPD, as 90.4.3.1.1 says.		M_DIT_CHANGE Signals	Tse, Richard	Microchip Te	chnology	
Suggested	Remedv				Comment Type E	Comment Status D		buck
	•	parameter, which is conveyed	ed by TX NUM	BIT CHANGE".	"Skew" in the headi	ng of 90A.6 should not have a c	apitalized "S"	
	ly in 90A.5.2.		,		SuggestedRemedy			
Proposed I	Response	Response Status W			Change "Considerations for	ironomit Chour		
PROP	OSED ACCEPT	IN PRINCIPLE.			to	Tansmit Skew		
Comm	ant tuna waa aha	nand to T			"Considerations for	ransmit skew"		
Comm	ent type was cha	inged to T			Proposed Response	Response Status W		
Chang	e to "the PDDPD ly in 90A.5.2.	primitive, which is conveyed	by TX_NUM_E	BIT_CHANGE".	PROPOSED ACCE	PT.		

C/ 90A SC 90A.6

# IEEE P802.3cx D2.3 ITSA Task Force 3rd Working Group recirculation ballot comments

	SC 90A.7	P <b>74</b>	L <b>3</b>	# 551
Dawe, Pier	5	Nvidia		
Preferre	ont is preferred. ad font size is 9	Comment Status D points (can be 8 or 10 point alibri. There is plenty of spa	,	bucke
SuggestedI Change	Re <i>medy</i> e to 9 point Arial	(in black)		
Proposed F PROPC	Response DSED ACCEPT.	Response Status W		
C/ 90A	SC 90A.7	P <b>74</b>	L <b>26</b>	# 552
Dawe, Pier	5	Nvidia		
Comment 7 Not hou	<i>ype</i> E use style	Comment Status D		bucke
	90A-3 to 5 wou or possibly "PH	Ild be better using black text Y path data delay". If there		
	e good too.			, , , , , , , , , , , , , , , , , , ,
would b Proposed F	-	Response Status W		
would b Proposed F	Response		L <b>4</b>	# [488
would b Proposed R PROPC	Response DSED ACCEPT. SC <b>999</b>	·		
would b Proposed F PROPC Cl 999 Tse, Richar Comment T	Response DSED ACCEPT. SC <b>999</b> rd rd ype <b>E</b>	P16	chnology	
would b Proposed F PROPO Cl 999 Tse, Richau Comment T Subclau Suggested	Response DSED ACCEPT. SC 999 rd ype E use 90A.1 doest Remedy	P <b>16</b> Microchip Te <i>Comment Status</i> D	tents	# 488
would b Proposed F PROPC Cl 999 Tse, Richar Comment T Subclar Suggested Update Proposed F	Response DSED ACCEPT. SC 999 rd ype E use 90A.1 does Remedy table of content	P16 Microchip Te <i>Comment Status</i> D n't appear in the table of con ts so subclause 90A.1 is inc <i>Response Status</i> W	tents	# 488

C/ 999 SC 999