

Approved Responses

IEEE P802.3cx D1.1 ITSA Task Force 2nd Task Force review comments

CI 30 SC 30.13.1.1 P27 L48 # 45

Lv, Jingfei Huawei

Comment Type TR Comment Status A

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.1, 2.1800.1, 3.1800.1, 4.1800.1, 5.1800.1, and 6.1800.1 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

The capability of fine resolution transmit data delay needs to be added also.

SuggestedRemedy

Propose to change

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.1, 2.1800.1, 3.1800.1, 4.1800.1, 5.1800.1, and 6.1800.1 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

as

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.1, 1.1800.3, 2.1800.1, 2.1800.3, 3.1800.1, 3.1800.3, 4.1800.1, 4.1800.3, 5.1800.1, 5.1800.3, 6.1800.1 and 6.1800.3 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

Response Response Status C

ACCEPT IN PRINCIPLE.

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.1, 2.1800.1, 3.1800.1, 4.1800.1, 5.1800.1, and 6.1800.1 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

as

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.1, 1.1800.3, 2.1800.1, 2.1800.3, 3.1800.1, 3.1800.3, 4.1800.1, 4.1800.3, 5.1800.1, 5.1800.3, 6.1800.1, and 6.1800.3 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

CI 30 SC 30.13.1.2 P28 L8 # 46

Lv, Jingfei Huawei

Comment Type TR Comment Status A

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 2.1800.0, 3.1800.0, 4.1800.0, 5.1800.0, and 6.1800.0 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

The capability of fine resolution receive data delay needs to be added also.

SuggestedRemedy

Propose to change

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 2.1800.0, 3.1800.0, 4.1800.0, 5.1800.0, and 6.1800.0 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

as

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 1.1800.2, 2.1800.0, 2.1800.2, 3.1800.0, 3.1800.2, 4.1800.0, 4.1800.2, 5.1800.0, 5.1800.2, 6.1800.0 and 6.1800.2 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

Response Response Status C

ACCEPT IN PRINCIPLE.

change

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 2.1800.0, 3.1800.0, 4.1800.0, 5.1800.0, and 6.1800.0 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

as

"the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 1.1800.2, 2.1800.0, 2.1800.2, 3.1800.0, 3.1800.2, 4.1800.0, 4.1800.2, 5.1800.0, 5.1800.2, 6.1800.0, and 6.1800.2 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively)"

Approved Responses

IEEE P802.3cx D1.1 ITSA Task Force 2nd Task Force review comments

Cl 45 **SC 45.2.1.147** **P33** **L47** # **43**
 Lv, Jingfei Huawei
Comment Type **TR** **Comment Status** **A**
 "If both sets of registers are valid (see Table 45–110), the maximum PMA/PMD transmit path data delay is the sum of the values from these two sets of registers."

 Since, the table 45-110 has defined registers to indicate the capability of transmit path data delay and fine resolution transmit path data delay, the reference of registers are more clear instead of the table 45-110, e.g., "the register 1.1800.1 and 1.1800.3 of table 45-110."
SuggestedRemedy
 Propose to change

 "If both sets of registers are valid (see Table 45–110), the maximum PMA/PMD transmit path data delay is the sum of the values from these two sets of registers."

 as

 "If both sets of registers are valid (see the register 1.1800.1 and 1.1800.3 of Table 45–110), the maximum PMA/PMD transmit path data delay is the sum of the values from these two sets of registers."

 If the proposal is agreed, do the similar change for the 3rd paragraph of Clause 45.2.1.147, and for WIS, PCS, PHY, DTE and TC layers.
Response **Response Status** **C**
 ACCEPT.

Cl 45 **SC 45.2.1.147** **P33** **L48** # **44**
 Lv, Jingfei Huawei
Comment Type **TR** **Comment Status** **A**
 "If any of the two register sets are not valid, then the corresponding value is not included in the maximum PMA/PMD transmit path data delay."

 Here, the corresponding value should be the set of registers which the capability register is invalide. Propose to make it clear, e.g., "the value corresponding to the invalid register set"
SuggestedRemedy
 Propose to change

 "If any of the two register sets are not valid, then the corresponding value is not included in the maximum PMA/PMD transmit path data delay."

 as

 "If any of the two register sets are not valid, then the value corresponding to the invalid register set is not included in the maximum PMA/PMD transmit path data delay."

 If the proposal is agreed, do the similar change for the 3rd paragraph of Clause 45.2.1.147, and for WIS, PCS, PHY, DTE and TC layers.
Response **Response Status** **C**
 ACCEPT.

Approved Responses

IEEE P802.3cx D1.1 ITSA Task Force 2nd Task Force review comments

CI 90 SC 90.4.3 P58 L42 # 52

He, Xiang Huawei
 Comment Type TR Comment Status A

90.4.3.3 & 90.4.3.4.
 TX_num_unit_change and RX_num_unit_change may not be available where MAC and PHY are separated devices connected by AUIs, and sub-layers lower than the AUI could have the capability for idle insertion and deletion. Figure 118-1 and Figure 155-1 are two good examples.

SuggestedRemedy

Adding notes restricting the PHYs from inserting/deleting idles for such cases.

“TX_num_unit_change and RX_num_unit_change may not be available over physical interfaces such as instantiated xMII or AUI. In order to achieve high accuracy timestamping, it is recommended to avoid idle insertion/deletion in sublayers lower than these interfaces.”

Response Response Status C

ACCEPT IN PRINCIPLE.

Apply changes tse_law_3cx_01a_0721.pdf

Add the following note under new 90.4.4.1

“TX_num_unit_change is intended for the use with intra-chip interfaces. TX_num_unit_change is not available over physical interfaces such as instantiated xMII or AUI. In order to achieve high accuracy timestamping, it is recommended to avoid AM insertion, CWM insertion, and Idle insertion/removal in sublayers lower than these interfaces.”

Add the following note under new 90.4.4.2

“RX_num_unit_change is intended for the use with intra-chip interfaces. RX_num_unit_change is not available over physical interfaces such as instantiated xMII or AUI. In order to achieve high accuracy timestamping, it is recommended to avoid AM removal, CWM removal, and Idle insertion/removal in sublayers lower than these interfaces.”

CI 90 SC 90.5.1 P60 L14 # 42

Lv, Jingfei Huawei
 Comment Type TR Comment Status A

"when the beginning of the symbol after the SFD"

The timestamp point of this reference should be the beginning of the first symbol after the SFD.

SuggestedRemedy

Propose to change

"when the beginning of the symbol after the SFD"

as

"when the beginning of the first symbol after the SFD"

If this is agreed, do the same change for the first line of the second paragraph of 90.5.2

Response Response Status C

ACCEPT.

CI 90 SC 90.7 P63 L8 # 50

Lv, Jingfei Huawei
 Comment Type T Comment Status R

Propose to add the reference number for 802.1AS and 1588

SuggestedRemedy

Propose to change

"IEEE Std 802.1AS and IEEE Std 1588"

as

"IEEE Std 802.1AS [B41] and IEEE Std 1588 [B43]"

If this is agreed, add the reference number for 802.1AS and 1588 throughout the whole document.

Response Response Status C

REJECT.

It just needs to be done on the first use and it is already covered in 90.2 Overview. No changes to the draft needed.

Approved Responses

IEEE P802.3cx D1.1 ITSA Task Force 2nd Task Force review comments

Cl **90** *SC* **90.7** *P* **63** *L* **21** # **47**

Lv, Jingfei Huawei

Comment Type **TR** *Comment Status* **A**

"if the message timestamp point is at the start of the FEC block and multi-PCS lane distribution sequence"

The "and" should be appended with "or", due that some interfaces could have FEC and/or multi-PCS lane distribution functions.

SuggestedRemedy
 Propose to change

"if the message timestamp point is at the start of the FEC block and multi-PCS lane distribution sequence"

as

"if the message timestamp point is at the start of the FEC block and/or multi-PCS lane distribution sequence"

Response *Response Status* **C**
 ACCEPT.

Cl **90** *SC* **90.8.3** *P* **66** *L* **8** # **48**

Lv, Jingfei Huawei

Comment Type **TR** *Comment Status* **A**

"TS_TX.indication is generated only when the SFD is detected on the transmit signals of the xMII"

TS_TX.indication includes two timestamp points, and the SFD should be replaced, e.g., the timestamp reference point

SuggestedRemedy
 Propose to change

"TS_TX.indication is generated only when the SFD is detected on the transmit signals of the xMII"

as

"TS_TX.indication is generated only when the timestamp reference point is detected on the transmit signals of the xMII"

Response *Response Status* **C**
 ACCEPT IN PRINCIPLE.

change

"TS_TX.indication is generated only when the SFD is detected on the transmit signals of the xMII"

as

"TS_TX.indication is generated only when the message timestamp point is detected on the transmit signals of the xMII"

Approved Responses

IEEE P802.3cx D1.1 ITSA Task Force 2nd Task Force review comments

Cl **90** *SC* **90.8.3** *P* **66** *L* **12** # **49**

Lv, Jingfei Huawei

Comment Type **TR** *Comment Status* **A**

"TS_RX.indication is generated only when the SFD is detected on the receive signals of the xMII"

TS_RX.indication includes two timestamp points, and the SFD should be replaced, e.g., the timestamp reference point

SuggestedRemedy
 Propose to change

"TS_RX.indication is generated only when the SFD is detected on the receive signals of the xMII"

as

"TS_RX.indication is generated only when the timestamp reference point is detected on the receive signals of the xMII"

Response *Response Status* **C**

ACCEPT IN PRINCIPLE.

change

"TS_RX.indication is generated only when the SFD is detected on the receive signals of the xMII"

as

"TS_RX.indication is generated only when the message timestamp point is detected on the receive signals of the xMII"

Cl **90A** *SC* **90A.2** *P* **69** *L* **32** # **51**

Lv, Jingfei Huawei

Comment Type **TR** *Comment Status* **A**

"the beginning of the start of frame delimiter, the SFD, and the beginning of the SFD"

And the second "the beginning of the SFD" should be "the beginning of the first symbol after the SFD"

SuggestedRemedy
 Propose to change

"the beginning of the start of frame delimiter, the SFD, and the beginning of the SFD"

as

"the beginning of the start of frame delimiter, the SFD, and the beginning of the first symbol after the SFD"

Response *Response Status* **C**

ACCEPT.