This boxed paragraph is part of the published standard, so the self reference should be IEEE Std, not a project designation.

Suggested Remedy
Replace "P802.3cy" with "IEEE Std 802.3cy-202x"

Proposed Response

Comment Type: E  Comment Status: X

This paragraph contains a grammatical error. "a" should have been deleted in editing out "network".

Suggested Remedy
"...specification for 25 Gb/s Ethernet ..."

Proposed Response

Comment Type: E  Comment Status: X

The defined terms master PHY and slave PHY are lower case in 1.4.389 and 1.4.535 definitions. This amendment should follow that precedent. Reconsider if MASTER and SLAVE should be all caps.

Suggested Remedy
Change MASTER PHY and SLAVE PHY to master PHY and SLAVE PHY throughout. (Pages 45, 63, 65, 81, 91, 97, 117.)

Proposed Response

---

Dawe, Piers  Nvidia

Comment Type: T  Comment Status: X

"In the training mode (see 165.4.2.4), the PCS transmits and receives PAM2 training frames to synchronize to the PHY frame..." but "PHY frame" is not defined.

Suggested Remedy
Change to "synchronize to the PHY frame..." to "synchronize to the RS-FEC superframes that follow, ..."

Proposed Response

---

Grow, Robert  RMG Consulting

Comment Type: E  Comment Status: X

25GBASE-T1 Physical Layer device (PHY)
Comments Received

IEEE P802.3cy D2.21 10G+ Auto Task Force 2nd Working Group recirculation ballot comments

Cl 165 SC 165.2.2.4.3 P 46 L 26 # 819
Dawe, Piers
Nvidia

Comment Type E Comment Status X
This says that the effect of receipt of this primitive, PMA_UNITDATA.indication(rx_symb), is unspecified. That's not correct.

Suggested Remedy
Change "is unspecified" to "is specified in 165.3.2.3.1".

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.16 P 57 L 34 # 810
Dawe, Piers
Nvidia

Comment Type E Comment Status X
pL.33

Suggested Remedy
pL.89?

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.16 P 58 L 3 # 811
Dawe, Piers
Nvidia

Comment Type E Comment Status X
#1 #2 #L

Suggested Remedy
1 2 L (as in other figures, e.g. 65B block, 165B block 2 ...)

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 58 L 27 # 815
Dawe, Piers
Nvidia

Comment Type E Comment Status X
Galois Field

Suggested Remedy
Galois field

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 58 L 39 # 812
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Need to define all the items in the equation (except well-known functions and operators, and j here which is just a counter). Also, "alpha is a primitive element of the finite field defined by the primitive polynomial x^409 = x^10 + x^3 + 1" is too vague; it's not clear if it means that alpha is defined by 0x409 (how), or that the finite field is defined by 0x409, or that alpha is 0x409, or what.

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 58 L 49 # 809
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Add: "In this subclause, x is the indeterminate variable."
Change "In Equation (165-1), alpha is a primitive element of the finite field defined by the primitive polynomial x^409 = x^10 + x^3 + 1." to an unambiguous definition, e.g.
"In Equation (165-1), alpha, a primitive element of the finite Galois field GF(2^10), is the primitive polynomial x^409 = x^10 + x^3 + 1."

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 59 L 34 # 814
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Maybe this means: For each 10-bit message symbol mi, mi.0 is the first bit transmitted. Similarly for pi.0 on the next page.

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 61 SC 165.3.2.2.17 P 58 L 39 # 812
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Please define or give a reference for Galois field addition and Galois field multiplication.

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 61 SC 165.3.2.2.17 P 58 L 39 # 812
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Please define or give a reference for Galois field addition and Galois field multiplication.

Proposed Response Response Status O

Cl 165 SC 165.3.2.2.17 P 61 SC 165.3.2.2.17 P 58 L 39 # 812
Dawe, Piers
Nvidia

Comment Type E Comment Status X

Suggested Remedy

Please define or give a reference for Galois field addition and Galois field multiplication.

Proposed Response Response Status O
IEEE P802.3cy D2.21 10G+ Auto Task Force 2nd Working Group recirculation ballot comments

Comments Received

<table>
<thead>
<tr>
<th>Cl</th>
<th>SC</th>
<th>P</th>
<th>L</th>
<th>#</th>
<th>Dawe, Piers</th>
<th>Nvidia</th>
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<tbody>
<tr>
<td>165</td>
<td>165.3.2.2.17</td>
<td>59</td>
<td>54</td>
<td>808</td>
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<td>Unfortunate page break splitting so many columns in the table.</td>
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<td><strong>Suggested Remedy</strong> Increase the orphan rows setting so the table stays on one page</td>
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<td><strong>Proposed Response</strong></td>
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<td>165</td>
<td>165.3.2.3.1</td>
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<td>&quot;It obtains block lock to the PHY frames during PAM2 training using synchronization bits provided in the training frames&quot; but &quot;PHY frame&quot; is not defined. As we are in training, there will be training frames present.</td>
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<td><strong>Suggested Remedy</strong> Change &quot;PHY frames&quot; to &quot;training frames&quot;</td>
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<td>165.3.7.3</td>
<td>68</td>
<td>21</td>
<td>817</td>
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<td>Following D2.1 comment 785, there are three more 65B blocks to be changed to 64B/5B blocks. Names can be more consistent. Also, &quot;65B RS-FEC&quot; is a confusing name, as the FEC doesn't really operate on 65-bit blocks but on a 9360-bit payload, and 165.3.2.17 says 'the particular Reed-Solomon code is denoted as RS-FEC(936,846)'. There are two &quot;64B/65B RS-FEC&quot;; three &quot;65B RS-FEC frame&quot; and 4 other &quot;65B RS-FEC&quot;</td>
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<td><strong>Suggested Remedy</strong> Change &quot;65B transmitted blocks&quot; to &quot;64B/65B transmit(ted) blocks&quot;, &quot;65B transmit block&quot; to &quot;64B/65B transmit(ted) block&quot;, &quot;65B received blocks&quot; to &quot;64B/65B received blocks&quot;. Here, &quot;65B RS-FEC&quot; can be changed to &quot;RS-FEC&quot;. Change the three &quot;65B RS-FEC frame&quot; to &quot;RS-FEC frame&quot; Rename the remaining &quot;65B RS-FEC&quot; e.g. to RS-FEC(936,846). With editorial licence.</td>
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<td><strong>Proposed Response</strong> Response Status O</td>
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SORT ORDER: Clause, Subclause, page, line
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<th>CI</th>
<th>SC</th>
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<th>Line</th>
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<th>Comment Status</th>
<th>Comment Details</th>
<th>Proposed Response</th>
<th>Response Status</th>
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<tr>
<td>165</td>
<td>165.5.3.3</td>
<td>95</td>
<td>6</td>
<td>E</td>
<td>X</td>
<td>In the explanation &quot;this is equivalent...&quot;, &quot;at least&quot; should be deleted following the change to make the bandwidth at line 5 a value rather than a one-sided limit.</td>
<td>Delete &quot;at least&quot;</td>
<td>O</td>
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<td>165</td>
<td>165.11.4.2.5</td>
<td>118</td>
<td>10</td>
<td>E</td>
<td>X</td>
<td>PICS uses &quot;frame&quot; twice and &quot;PHY frame&quot; 4 times. The normative material it refers to in 165.3.6.1 uses &quot;RS-FEC frame&quot; 10 times or more, &quot;frame&quot; once.</td>
<td>Here, change all &quot;frame&quot; and &quot;PHY frame&quot; to &quot;RS-FEC frame&quot;. In 165.3.6.1, change &quot;four frames after&quot; to &quot;four RS-FEC frames after&quot;.</td>
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<tr>
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<td>165.11.4.5</td>
<td>128</td>
<td>28</td>
<td>E</td>
<td>X</td>
<td>Maximum link delay in PICS is out of date</td>
<td>Change 94 to 60</td>
<td>O</td>
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</tbody>
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