

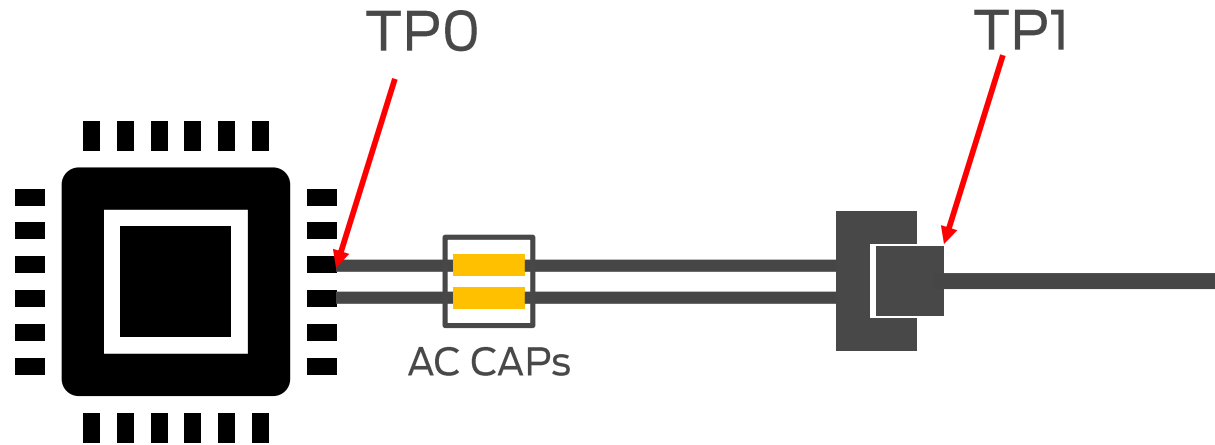
IEEE 802.3CY – BEYOND 10G ELECTRICAL AUTOMOTIVE ETHERNET PHY TF

MDI PARAMETERS

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MDI Test Points



- These are the proposed TPs for the MDI
- Based on the analysis from [Kadry_3cy_01a_03_01_21.pdf](#), The recommended PCB insertion loss is 3dB from TP0 to TP1.

Discussion Points

- Should we specify TPs for the AC coupling and PoDL?
- With power requirements is PoDL still a feasible option to include for this specification?
- Need to specify MDI electrical parameters
- Do we specify test fixtures and test fixture performance?

Thank you!

