

P802.3cy To-do list usage

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IEEE P802.3cy

What is a to-do list?

- A list of items needed to get to D1.0 – Task Force review
 - Updated throughout the process, at least every 2 months
- A living list of items needed to get to D2.0 – Working Group ballot
 - Updated after creation of each D1.x
- Helps to keep the Task Force on track
- Ensures volunteers are working on content when it is needed
 - Requires agreement on order of tasks

What does the to-do list include?

- High level description of contributions needed
- Volunteer(s) that will provide the contributions
- Due dates – e.g. ad hoc at which the contribution will be presented
- Example of progress in P802.3ch:
<http://www.ieee802.org/3/ch/todo/index.html>

How to start?

- Create a list of “big ticket” items, put in logical order, and assign completion dates
- Example from P802.3ch
http://www.ieee802.org/3/ch/public/mar18/P802_3ch_timeline_20180308.pdf
- The following slides show the list created by P802.3ch
- P802.3cy will need to create our own list

P802.3ch list of tasks to D1.0

(To Be Discussed – P802.3cy list of tasks to D1.0)

- OEM topology Information
- Cabling System Model (IL & RL at a minimum)
- Signaling type and modulation (2.5 Gb/s and 10 Gb/s)
- PHY vendors provide information on what type of EMC test data is preferred
- Consider power budget and impact on PoDL
- Signaling type and modulation (2.5 Gb/s and 10 Gb/s)
- PHY vendors provide information on what type of EMC test data is preferred
- Cabling System Model Improvements (Coupling Attenuation, TCL, ELTCTL)
 - Gaussian distribution of cable impedance instead of equal
 - Analyze other OEM topologies
 - 26 gauge up to 11? m and 24 gauge up to 15 m
- Finalized: Signaling type and modulation (2.5 Gb/s and 10 Gb/s)
 - MDI RL limit (baseline to be provided to PoDL) -
- Provide EMC test data

P802.3ch list of tasks to D1.0

(To Be Discussed – P802.3cy list of tasks to D1.0)

- Line Coding and FEC initial proposals
- PoDL definition (solicit help from bt)
- OEMs provide results of Early EMC testing
- Line Coding and FEC updated proposals
- PoDL definition baseline proposals
- Initial Register maps, EEE support, PICs
- PHY Control State diagram proposals (take into account start up time requirements)
- Line Coding and FEC baseline adopted
- Complete Register maps, EEE support, PICs
- PHY Control State diagram finalize

Next Steps

- Participants review P802.3ch list for applicability to P802.3cy
- Use the P802.3cy reflector to share your thoughts on the list, e.g. additions, deletions, order, etc.
- Discuss list during upcoming ad-hoc calls

THANK YOU