

Multi-drop node distribution challenges

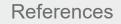


Wojciech Koczwara • Scott Griffiths • David Brandt • | 2nd Dec 2020

Multi-drop link node distribution challenges

Authors:

Wojciech Koczwara, Rockwell Automation David Brandt, Rockwell Automation Scott Griffiths, Rockwell Automation





Node distribution topologies



Node distribution comparison in frequency domain



Node distribution comparison in time domain



Summary and next steps

References

This presentation attempts to build upon:

• [1] SPE Multidrop Enhancements Mixing Segment Considerations Update

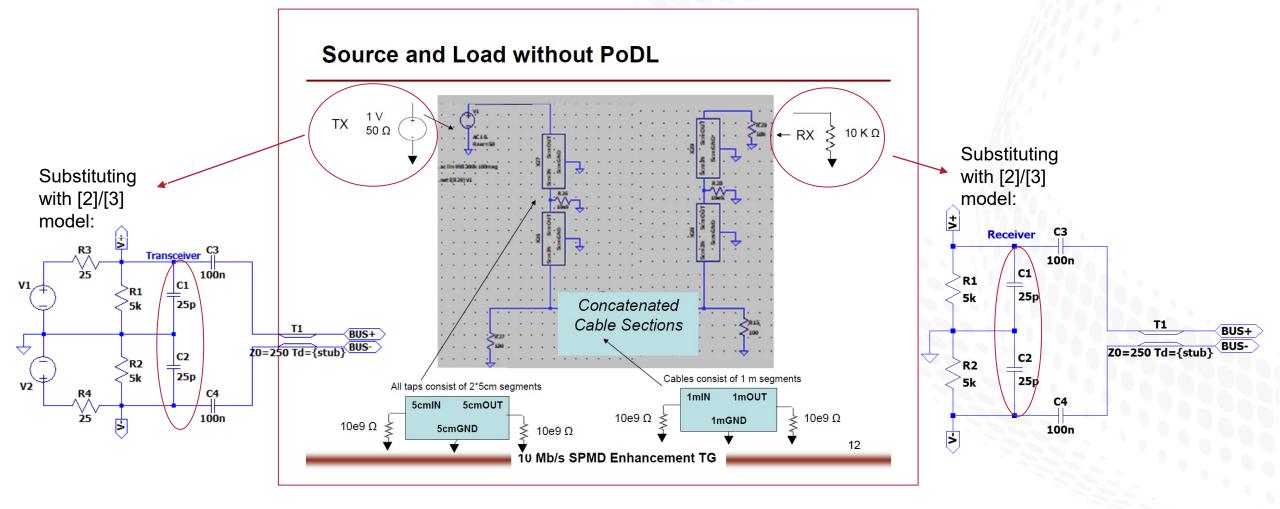
https://www.ieee802.org/3/da/public/111820/diminico_SPMD_01_1120.pdf

by Chris DiMinico, Bob Voss, and Paul Wachtel

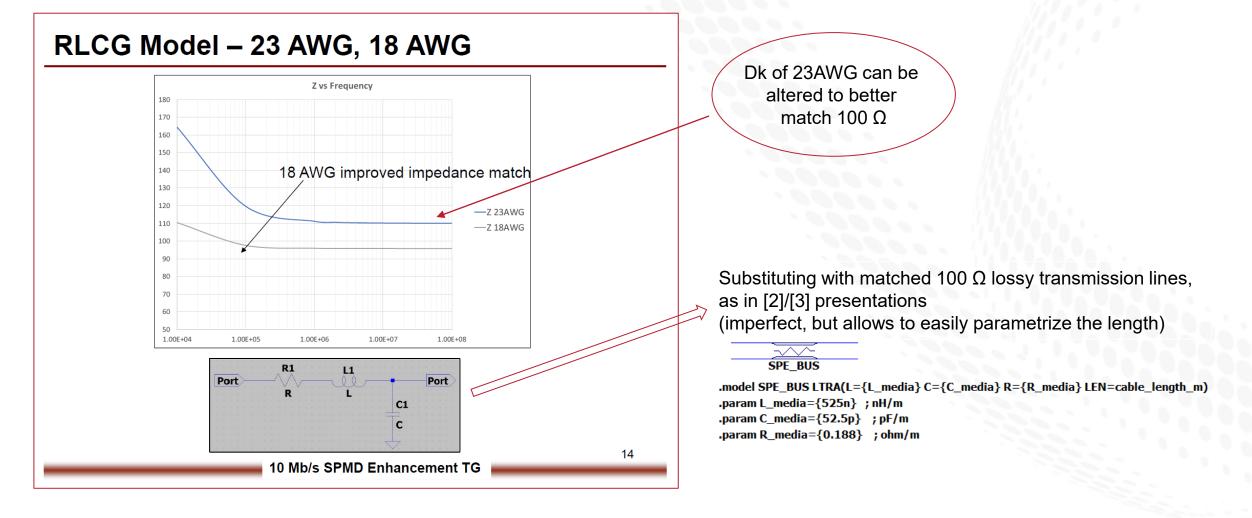
- previous related SPMD Mixing Segment Considerations from Chirs DiMinico
- 3cg presentations by David Brandt and Scott Griffiths:
 - [2] https://www.ieee802.org/3/cg/public/Sept2018/griffiths_3cg_01a_0918.pdf
 - [3] https://www.ieee802.org/3/cg/public/Mar2018/brandt_cg_01a_0318.pdf

Kudos to all contributors for addressing this complex topic!

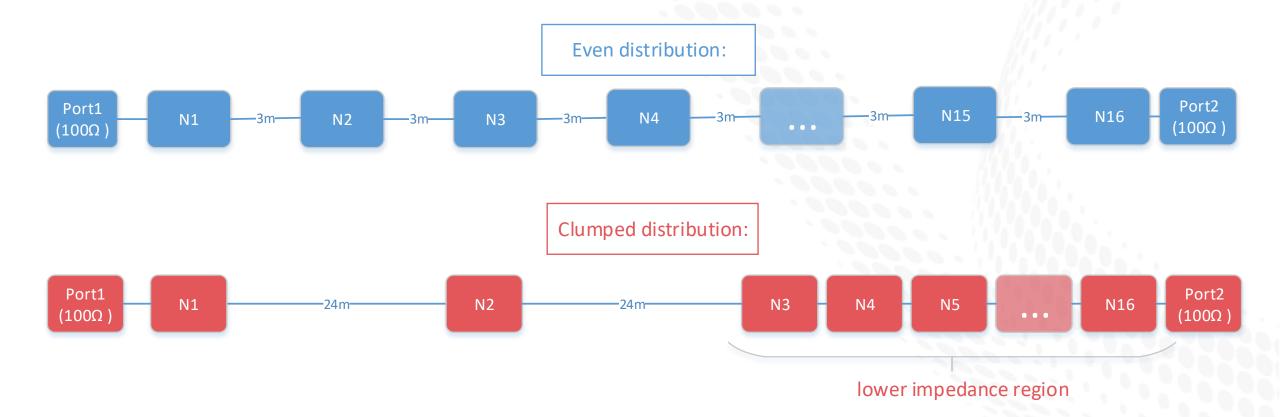
[1] Node model without PoDL – need to include MDI capacitance



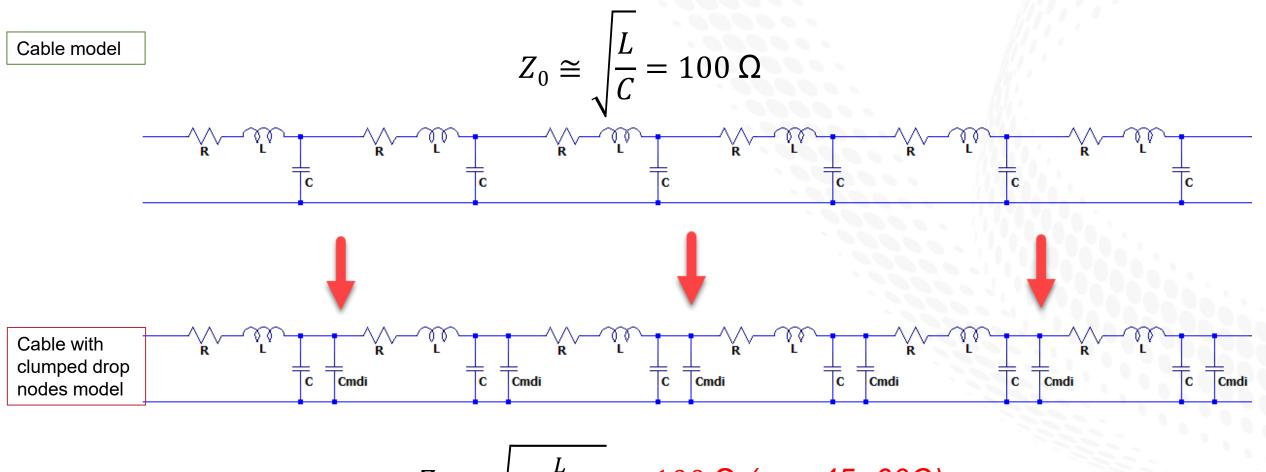
[1] Cable impedance mismatch and workaround



Main node distribution topologies in two-port simulation



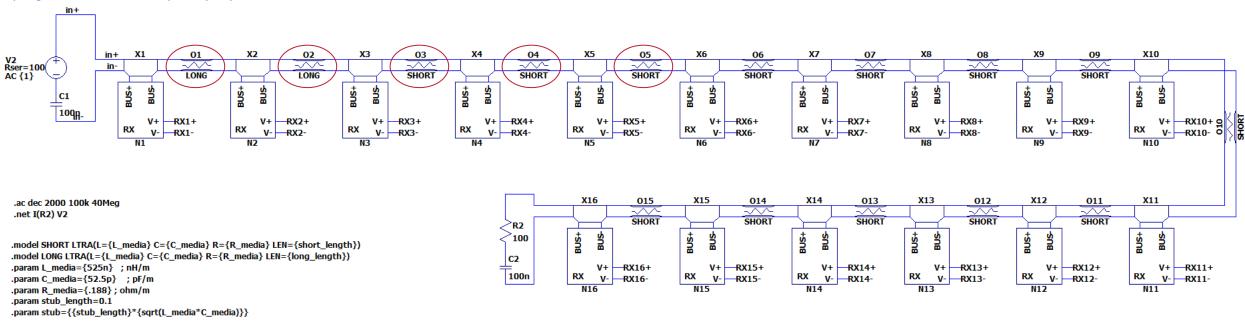
Impedance drop in clumped nodes regions



$$Z_0 \cong \sqrt{\frac{L}{C+CMDI}} \ll 100 \ \Omega$$
 (e.g. 45–60 Ω)

Node distributions comparison – SPICE .AC simulation setup

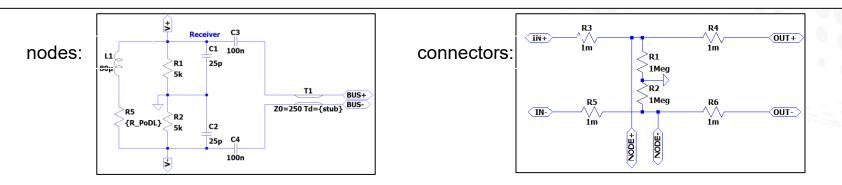
To make IL < 3dB possible in multidrop, SPE bus needs to be driven directly. Replacing left terminator with source impedance (100R).



parametrizing by Rswitch: even spacing (0.1m), even spacing with PoDL (0.5m), clumped spacing (2m), clumped spacing with PoDL(2.2m)

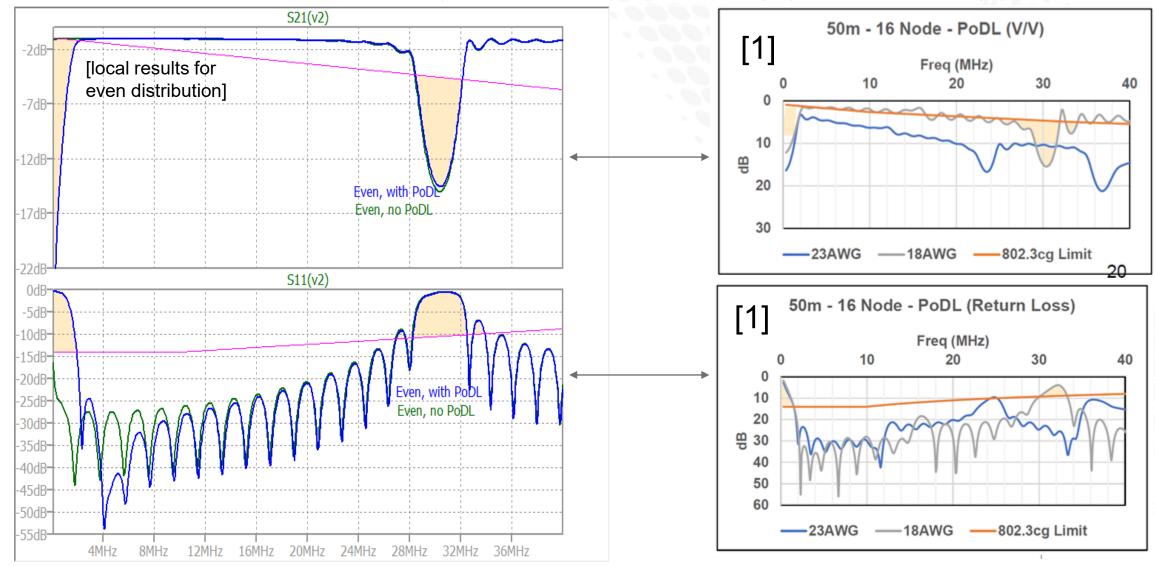
.step param Rswitch list 0.1m 0.5m 2m 2.2m .param short_length={if(Rswitch>1m,0.045,3)} .param long_length {if(Rswitch>1m,24,short_length)}

.param R_PoDL={if((Rswitch > 0.4m & Rswitch <1m)|(Rswitch > 2.1m & Rswitch <2.5m), 1m, 1Meg)}

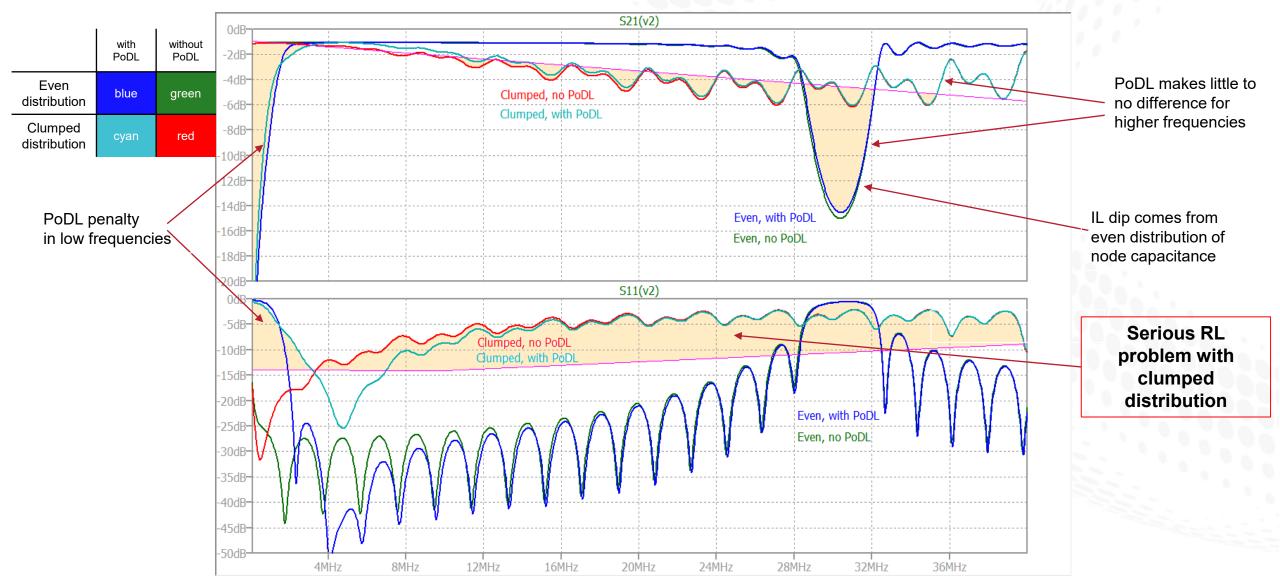


Even distribution vs. results reported in presentation [1] :

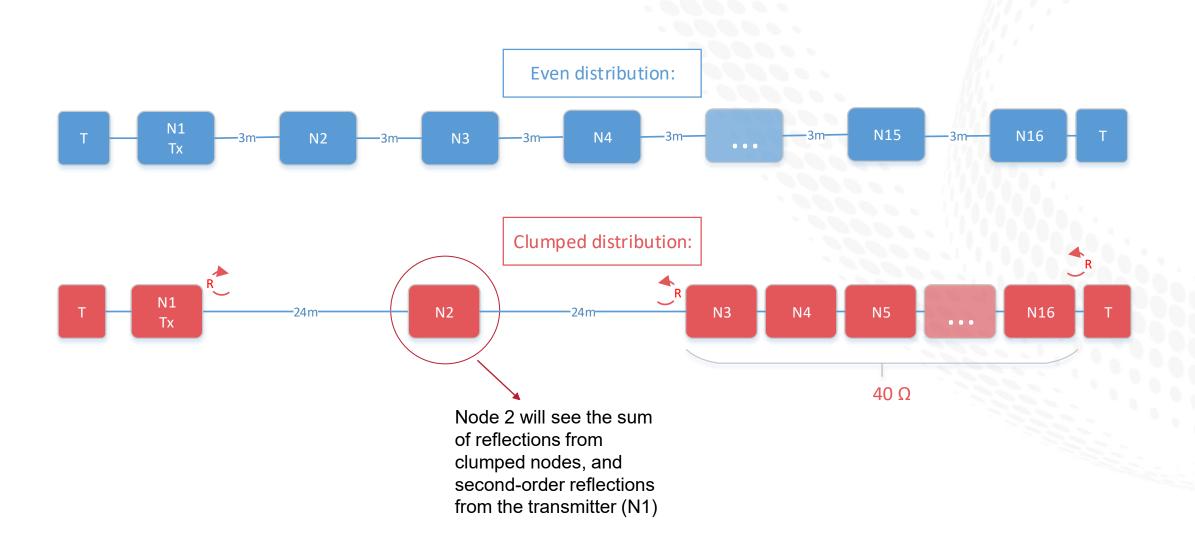
Main problem points do overlap. Differences are likely due to simplified cable model, and slightly different node spacing than in [1]



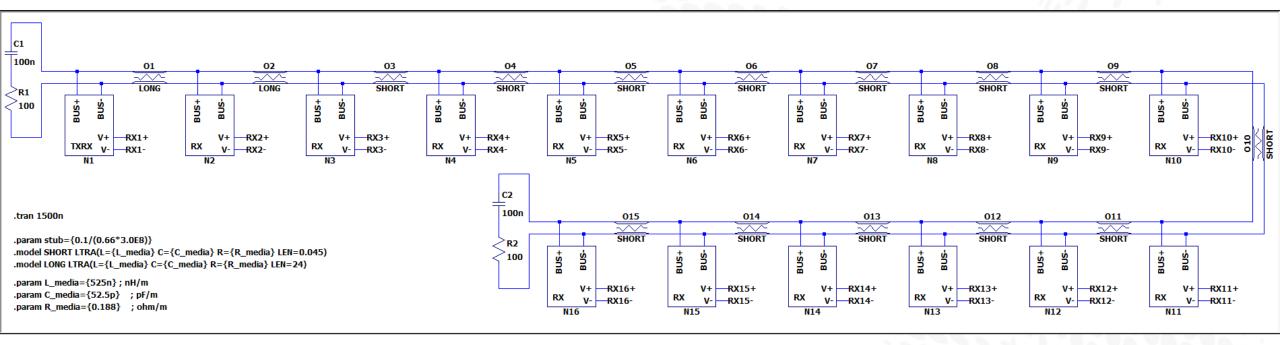
Even vs. clumped distribution, PoDL influence

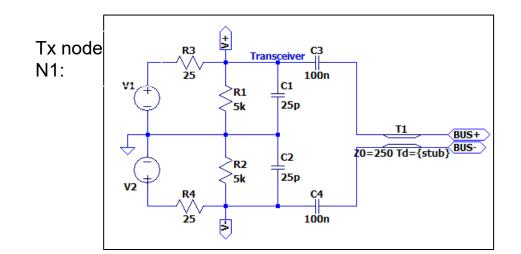


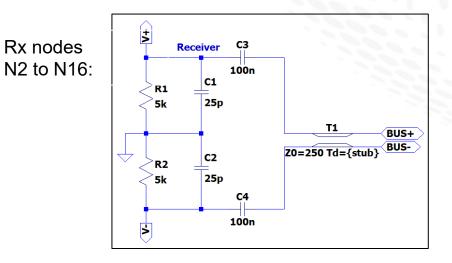
Node distribution – time domain simulation



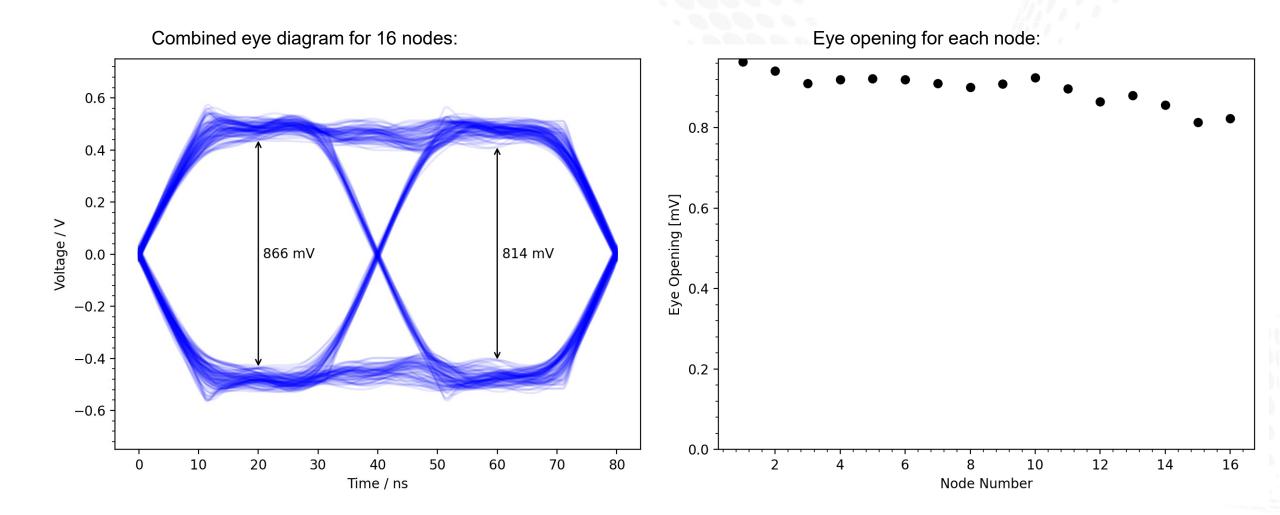
Node distribution – time domain simulation setup (as in [2]/[3])



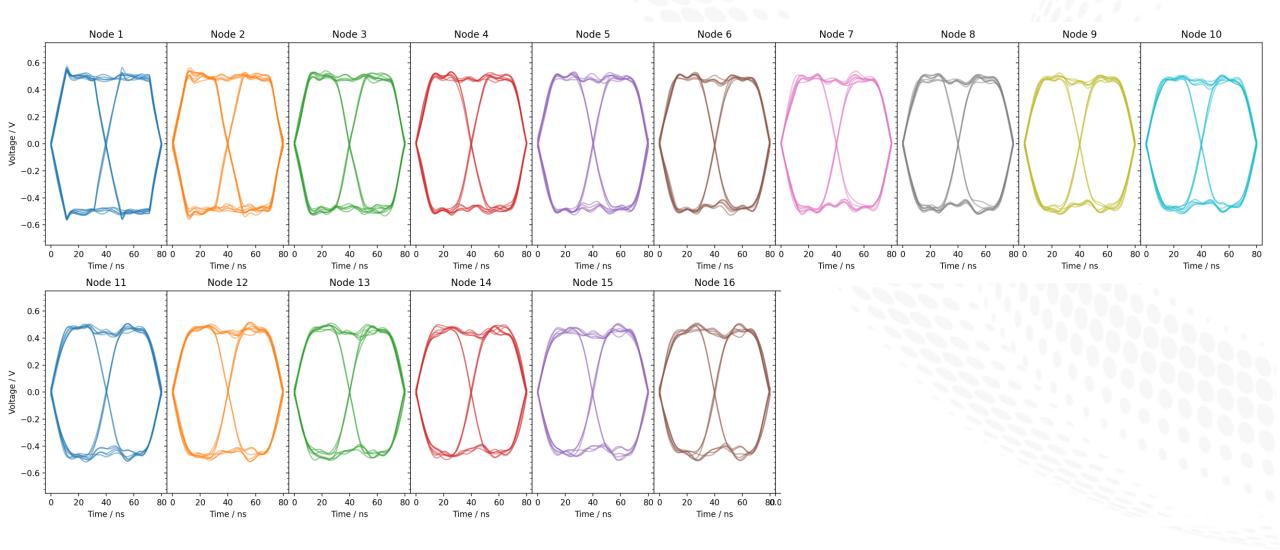




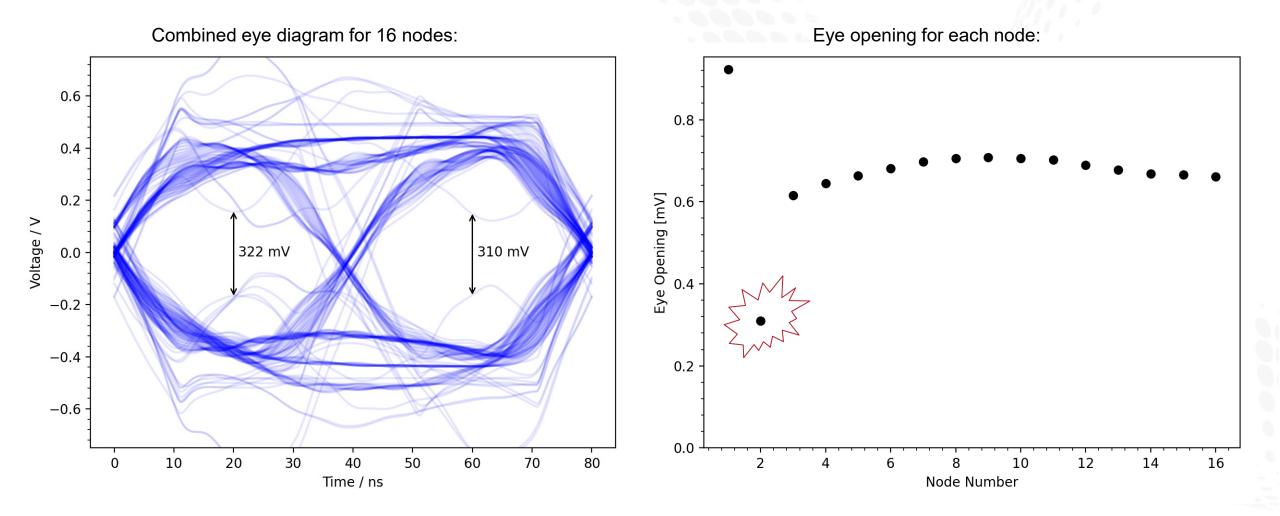
Time domain simulation: even distribution (3m spacing)



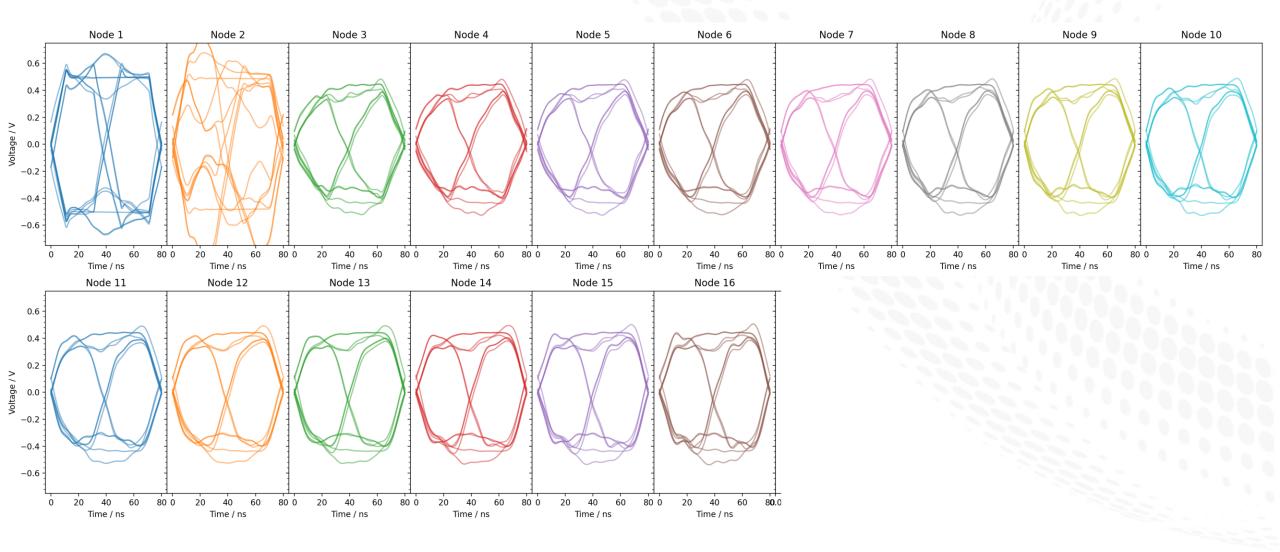
Time domain simulation: even distribution (3m spacing)



Time domain simulation: clumped distribution



Time domain simulation: clumped distribution



Takeaways:

- Node capacitance is detrimental to multi-drop link Signal Integrity
- PoDL does affect the link, but mainly under 3MHz
- Clumped node distribution creates significant signal degradation by lowering the impedance
- Link parameters can be worse for ,inner nodes', than for ,outer nodes' on a multi-drop link
- The worst-case multi-drop node topology may not have yet been discovered
 - Monte Carlo topology simulations estimated (outside organization) to take over 35 years (1s per simulation)
 - theoretical solution needs to be discovered



Thank you



