Power Coupling Inductance and Droop 2

ANALOG DEVICES

AHEAD OF WHAT'S POSSIBLE™

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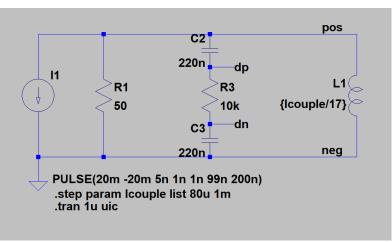


### **Critical Power Parameters**

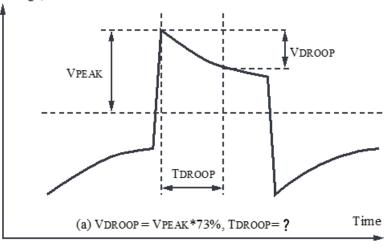


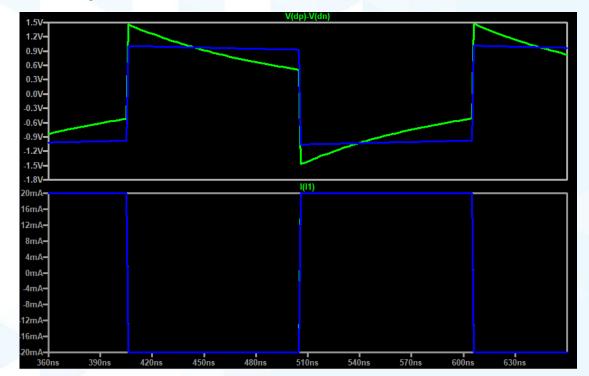
- Several critical system parameters should be resolved to move power specification forward
  - L<sub>PD</sub> Affects economic feasibility and classification timing
  - N (num PDs) Affects Inductor Size L<sub>PD</sub>
  - Droop (VDROOP, TDROOP) Determines LPD
  - Baud Rate Determines L<sub>PD</sub>
- Optimize L<sub>PD</sub> to minimize cost and size
  - Allow high-power PDs to use lower L<sub>PD</sub> since fewer high-power devices can be connected

# Simple Model For Droop w/ 16 PDs + 1 PSE (80uH/node and 1000uH/node)









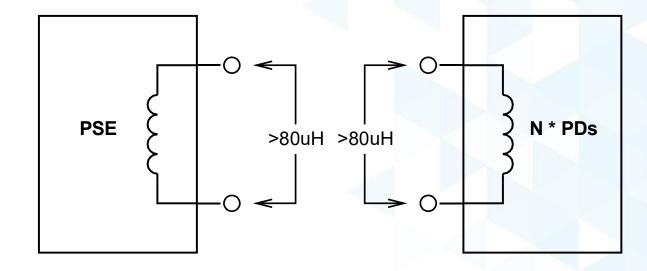
Can we specify a TDROOP and VDROOP?



3 // 15 Leconoci

#### Keep total inductance > 40uH





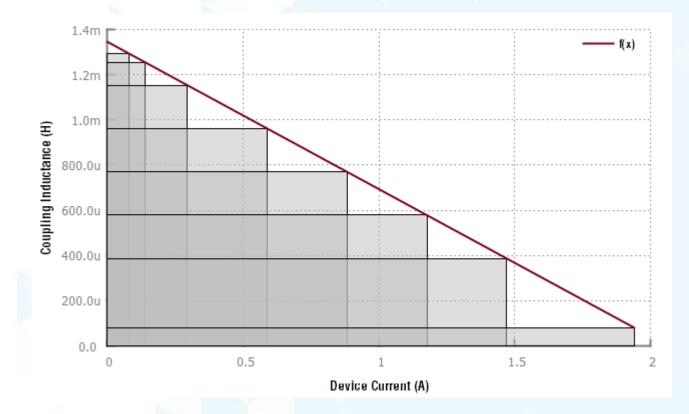
Parameter name	Unit of measure	Minimum value	Maximum value
R	kW	10	—
L	μΗ	80	—
C <sub>tot</sub>	pF	—	180
C <sub>node</sub>	pF	_	15



## **Choose Coupling Inductance By Class**



- Using 802.3bt classes as an example
- LCLASS = 80uH \* (#Nodes / Class)
- Highest power class uses 80uH or 160uH?
  - If 160uH a 2<sup>nd</sup> device can be added w/o causing droop
  - But demoted or not allowed to power up
- Lowest class needs 1280uH per device
  - 80uH \* 16 Nodes
- What is optimal division of classes?



# Continuing work



- Agree on minimum total inductance for the system
  - Trade off inductor size/cost with phy complexity
- Choose classes
- Set power coupling inductance for each class (Lclass)
- Verify CNODE Estimation
- Update Models

