

142A.1 Example of initial control seed sequence

For example, from Table 142–5 the control seed sequence for the first user interleaver is:

0xE3-88-B0-9A-74-F4-94-8E-5D-C0-CC-8A-18-9A-B9-B2

which represents the binary sequence:

```
1110001110001000101100001001101001110100111101001001010010  
0011100101110111000000110011001000101000011000100110101011  
  
1001101100101110 0011 1000 1000 1011 0000 1001 1010  
0111 0100 1111 0100 1001 0100 1000 1110  
0101 1101 1100 0000 1100 1100 1000 1010  
0001 1000 1001 1010 1011 1001 1011 0010
```

From Table 142–4, the switch programming sequence for the first stage of the user interleaver is a circular shift (left rotation) of the above control seed by 17 positions:

```
0101110011011001011100011100010001011000010011010011101001  
1110100100101001000111001011101110000001100110010001010000  
  
110001001101Control bit for switch 127 of the first stage  
↓  
0110 0001 0011 0100 1110 1001 1110 1001  
0010 1001 0001 1100 1011 1011 1000 0001  
1001 1001 0001 0100 0011 0001 0011 0101  
0111 0011 0110 0101 1100 0111 0001 0001  
_____  
Control bit for switch 0 of the first stage
```

From Table 142–4, the switch programming sequence for the second stage of the user interleaver is a circular shift (left rotation) of the above control seed by 34 positions:

```
1000011000100110101011100110110010111000111000100010110000  
1001101001110100111101001001010010001110010111011100000011  
  
001100100010Control bit for switch 127 of the second stage  
↓  
1101 0011 1101 0010 0101 0010 0011 1001  
0111 0111 0000 0011 0011 0010 0010 1000  
0110 0010 0110 1010 1110 0110 1100 1011  
1000 1110 0010 0010 1100 0010 0110 1001  
_____  
Control bit for switch 0 of the second stage
```