

Motions and Unapproved Content Pending Motion on Draft for 802.dd:

G. Zimmerman, 7/20/2021

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Revision History:

7/20/2021 – updated to show d1.0

7/19/2021: noted that Stewart\_3dd\_01\_06292021 provides minor corrections to Stewart\_3dd\_01a\_05252021, and these are shown in d0.2)

6/18/2021: as shown in d0.1

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THE FOLLOWING CHANGES ARE INCLUDED IN DRAFT 1.0:

APPROVED BY MOTION:

5/25/2021 Motion #3:

Move to: Adopt the following as baseline text:

Stewart\_3dd\_01a\_04272021.pdf slide 3

Stewart\_3dd\_01a\_04272021.pdf slide 4

Stewart\_3dd\_01a\_04272021.pdf slide 5

Stewart\_3dd\_01a\_05112021.pdf slide 3

Stewart\_3dd\_01a\_05112021.pdf slide 4

Stewart\_3dd\_01a\_05112021.pdf slide 5

Stewart\_3dd\_01a\_05112021.pdf slide 6

M: Steve Carlson, S: Heath Stewart (Technical >= 75%)

Motion Passes with unanimous consent

See zimmerman\_3dd\_01\_05242021.pdf for the relevant slides. These change:

Stewart..04272021:

Slide 3 – Table 104-5 Vsig\_disable

Slide 4 – Table 104-10, Voltage at PD\_PI reporting tolerance (in description)

Slide 5 – change to DO\_CLASSIFICATION in 104.5.3.6 state diagram Figure 104-8

Stewart..05112021:

Slide 3 – 104.5.3.3 change description of wakeup variable, 104.5.3.6 state diagram

Figure 104-8, change to add branch from PD\_SLEEP to DO\_CLASSIFICATION

Slide 4 – Table 104-8, change to typo (t\_PDLOW)

Slide 5 – 104.5.6.1 Change to text in PD discharge

Slide 6 – 104.5.6.3 Change to PD current during disconnect

7/20/2021 Motion #1:

Move to implement the changes listed on page 2 of zimmerman\_3dd\_01\_07202021.pdf

M: Heath Stewart, S: Geoff Thompson

Approved by Unanimous Consent

These change:

Stewart\_3dd\_01\_06292021: (slides 4-13): (minor update from Stewart\_3dd\_01a\_05252021.pdf)

Slide 4 – Table 104-7, item 15: SCCP watchdog timer

Slide 5 – Table 104-7, item 6b: C\_IN\_Class

Slide 6 - Table 104-8, items 20 & 21: (delete) t\_CHRG, V\_CHRG (note – this drives slides 11, 12, 13)

Slide 7 - Table 104-8, items 16, 15 & 7: t\_MSP, t\_PDL w/CRM, T\_REC

Slide 8 - Table 104-8, items 11 & 6b: t\_MSR and t\_READSLOT

Slide 9 - Table 104-8, items 8, 9, & 6a: t\_WOL, t\_W1L, t\_WRITESLOT

Slide 10- Table 104-8, item 18, t\_F, min

Slide 11 – Figure 104-10, (delete t\_CHRG, V\_CHRG in figure) (contingent on slide 6)

Slide 12 – Figure 104-11, (delete t\_CHRG, V\_CHRG in figure) (contingent on slide 6)

Slide 13 – Figure 104-12, (delete t\_CHRG, V\_CHRG in figure) (contingent on slide 6)

Stewart\_3dd\_01a\_06152021: (slides 3-5)

Slide 3 – 104.7.2 unsupported SCCP registers

Slide 4 – 146.8.6 (referenced by 104.6.2) source current for MDI faults

Slide 4 – 146.8.6 NOTE regarding configuration of powered MDIs (below Table 146-9 in the published 802.3cg)

Slide 5 – 104.6.2 fault tolerance (reference to 146.8.6)

Stewart\_3dd\_01a\_06152021: (slides 3-5)

Slide 3 – 104.7.2 unsupported SCCP registers

Slide 4 – 146.8.6 (referenced by 104.6.2) source current for MDI faults

zimmerman\_3dd\_01a\_06152021.pdf

slide 3 – 104.2 description that Vpse, Vpd are across the conductors of the pair

Slide 4 – 104.6.1 more isolation may be required

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No unapproved changes in D1.0