

Motions and Unapproved Content Pending Motion on Draft for 802.dd:

G. Zimmerman, 9/21/2021

Revision History:

9/21/2021 – updated to show d2.0 (changes to editors notes only)

9/7/2021 – updated to show d1.3 and pending unapproved (includes PICS, candidate for initial WG ballot)

8/24/2021 – updated to show d1.1 (note – PICS are not included at this point, and are subject to Editorial license)

7/20/2021 – updated to show d1.0

7/19/2021: noted that Stewart_3dd_01_06292021 provides minor corrections to Stewart_3dd_01a_05252021, and these are shown in d0.2)

6/18/2021: as shown in d0.1

THE FOLLOWING CHANGES ARE INCLUDED IN DRAFT 1.3:

APPROVED BY MOTION:

5/25/2021 Motion #3:

Move to: Adopt the following as baseline text:

Stewart_3dd_01a_04272021.pdf slide 3

Stewart_3dd_01a_04272021.pdf slide 4

Stewart_3dd_01a_04272021.pdf slide 5

Stewart_3dd_01a_05112021.pdf slide 3

Stewart_3dd_01a_05112021.pdf slide 4

Stewart_3dd_01a_05112021.pdf slide 5

Stewart_3dd_01a_05112021.pdf slide 6

M: Steve Carlson, S: Heath Stewart (Technical >= 75%)

Motion Passes with unanimous consent

See zimmerman_3dd_01_05242021.pdf for the relevant slides. These change:

Stewart..04272021:

Slide 3 – Table 104-5 Vsig_disable

Slide 4 – Table 104-10, Voltage at PD_PI reporting tolerance (in description)

Slide 5 – change to DO_CLASSIFICATION in 104.5.3.6 state diagram Figure 104-8

Stewart..05112021:

Slide 3 – 104.5.3.3 change description of wakeup variable, 104.5.3.6 state diagram Figure 104-8, change to add branch from PD_SLEEP to DO_CLASSIFICATION

Slide 4 – Table 104-8, change to typo (t_PDLOW)

Slide 5 – 104.5.6.1 Change to text in PD discharge

Slide 6 – 104.5.6.3 Change to PD current during disconnect

7/20/2021 Motion #1:

Move to implement the changes listed on page 2 of zimmerman_3dd_01_07202021.pdf

M: Heath Stewart, S: Geoff Thompson

Approved by Unanimous Consent

These changes are included:

Stewart_3dd_01_06292021: (slides 4-13): (minor update from Stewart_3dd_01a_05252021.pdf)

Slide 4 – Table 104-7, item 15: SCCP watchdog timer

Slide 5 – Table 104-7, item 6b: C_IN_Class

Slide 6 - Table 104-8, items 20 & 21: (delete) t_CHRG, V_CHRG (note – this drives slides 11, 12, 13)

Slide 7 - Table 104-8, items 16, 15 & 7: t_MSP, t_PDL w/CRM, T_REC

Slide 8 - Table 104-8, items 11 & 6b: t_MSR and t_READSLOT

Slide 9 - Table 104-8, items 8, 9, & 6a: t_WOL, t_W1L, t_WRITESLOT

Slide 10- Table 104-8, item 18, t_F, min

Slide 11 – Figure 104-10, (delete t_CHRG, V_CHRG in figure) (contingent on slide 6)

Slide 12 – Figure 104-11, (delete t_CHRG, V_CHRG in figure) (contingent on slide 6)

Slide 13 – Figure 104-12, (delete t_CHRG, V_CHRG in figure) (contingent on slide 6)

Stewart_3dd_01a_06152021: (slides 3-5)

Slide 3 – 104.7.2 unsupported SCCP registers

Slide 4 – 146.8.6 (referenced by 104.6.2) source current for MDI faults

Slide 4 – 146.8.6 NOTE regarding configuration of powered MDIs (below Table 146-9 in the published 802.3cg)

Slide 5 – 104.6.2 fault tolerance (reference to 146.8.6)

Stewart_3dd_01a_06152021: (slides 3-5)

Slide 3 – 104.7.2 unsupported SCCP registers

Slide 4 – 146.8.6 (referenced by 104.6.2) source current for MDI faults

zimmerman_3dd_01a_06152021.pdf

slide 3 – 104.2 description that Vpse, Vpd are across the conductors of the pair

Slide 4 – 104.6.1 more isolation may be required

9/21/21 Motion #1:

Stewart_3dd_01a_08242021: (slides 4-6): MDI fault tolerance (exclude Type E PSEs)

Slide 4 – 104.6.2 – exclude Type E PSEs

Slide 5 – 146.8.5 – exclude Type E PSEs

Slide 6 - 146.8.6 – exclude Type E PSEs

Stewart_3dd_02_09072021 (see Stewart_3dd_01_09072021 for rationale):

146.5.4.2 Transmitter output droop (changed w/ Clause 104 PI)

146.8.3 MDI return loss (changed w/Clause 104 PI)