

Suggested Motion

IEEE P802.3dd

Power over Data Lines of Single Pair Ethernet (Maintenance #17)
Task Force

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Changes on Page 2 of Motions on Draft.pdf

- Stewart_3dd_01_06292021.pdf: (slides 4 – 13):
 - (Tables 104-7 & 104-8, and Figures 104-10 through 104-12)
 - Table 104-7: item 15: SCCP watchdog timer, item 6b: C_IN_Class
 - Table 104-8, items 20 & 21: (delete) t_CHRG, V_CHRG, items 16, 15 & 7: t_MSP, t_PDL w/CRM, T_REC, items 11 & 6b: t_MSR and t_READSLOT, items 8, 9, & 6a: t_W0L, t_W1L, t_WRITESLOT, item 18, t_F, min
 - Figures 104-10, 104-11, and 104-12 (delete t_CHRG, V_CHRG in figure)
- Stewart_3dd_01a_06152021: (slides 3-5):
 - 104.7.2 unsupported SCCP registers , 146.8.6 (referenced by 104.6.2) source current for MDI faults, 146.8.6 NOTE regarding configuration of powered MDIs (below Table 146-9 in the published 802.3cg) , 104.6.2 fault tolerance (reference to 146.8.6)
- Stewart_3dd_01a_06152021: (slides 3-5) :
 - 104.7.2 unsupported SCCP registers, 146.8.6 (referenced by 104.6.2) source current for MDI faults
- zimmerman_3dd_01a_06152021.pdf :
 - 104.2 description that Vpse, Vpd are across the conductors of the pair , 104.6.1 more isolation may be required

Motion #1

Move to implement the changes listed on page 2 of
zimmerman_3dd_01_07202021.pdf

M: Heath Stewart

S: Geoff Thompson

Approved by unanimous consent