Geoff Thompson, Comments on P802.3de/D0.9

Technical

Clause 90.1
Page 15, Line 8

Comment/Rationale
There is no technical reason to restrict adding TSSI to only the Clause 147 P2P half-dupex PHY. Artificial restrictions are a bad idea. The rationale presented for this restriction has to do with the capabilities of the xMII versus the AUI. The AUI doesn't pass management information. That is not relevant in the current market nor the market for many years as while 10 Mb/s PHY are not obsolete, the AUI is. All current instances of 10 Mb/s PHYs are integrated with the MAC or use a proprietary interface between the MAC and PHY which, if need be, is fully capable of passing management information.

Change:
The TSSI is defined for the full-duplex mode of operation, as well as Clause 147 PHYs in point-to-point half-duplex modeonly. It supports MAC operation at various data rates. The MII (Clause 22), GMII ( ), XGMII (Clause 46), 25GMII (Clause 106), XLGMII (Clause 81), CGMII (Clause 81), 200GMII (Clause 117), and 400GMII (Clause 117) specifications are all compatible with the gRS sublayer defined in 90.5.

To:
The TSSI is defined for the full-duplex mode of operation, as well as 10 Mb/s PHYs in half-duplex mode provided that such PHYs do not utilize the optional AUI. It supports MAC operation at various data rates. The MII (Clause 22), GMII ( ), XGMII (Clause 46), 25GMII (Clause 106), XLGMII (Clause 81), CGMII (Clause 81), 200GMII (Clause 117), and 400GMII (Clause 117) specifications are all compatible with the gRS sublayer defined in 90.5.